Ordering number : ENN3461B

N-Channel Silicon MOSFET

2SK1458LS



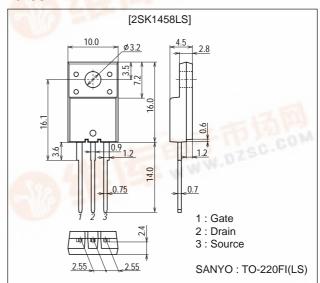
# **Ultrahigh-Speed Switching Applications**

#### **Features**

- · Low ON-resistance.
- · Ultrahigh-speed switching.
- · Micaless package facilitating mounting.

### **Package Dimensions**

unit : mm 2078C



### **Specifications**

Absolute Maximum Ratings at Ta=25°C

Parameter	Symbol	Conditions	Ratings	Unit
Drain-to-Source Voltage	VDSS		900	V
Gate-to-Source Voltage	VGSS		±30	V
Drain Current (DC)	ID	4.26	0.2	Α
Drain Current (Pulse)	IDP	PW≤10μs, duty cycle≤1%	0.4	Α
Allowable Power Dissipation	D=		2.0	W
	PD	Tc=25°C	20	W
Channel Temperature	Tch	L'al Car	150	°C
Storage Temperature	Tstg	C.CO.	-55 to +150	°C

### Electrical Characteristics at Ta=25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Drain-to-Source Breakdown Voltage	V(BR)DSS	ID=1mA, VGS=0	900			V
Zero-Gate Voltage Drain Current	IDSS	VDS=900V, VGS=0			1.0	mA
Gate-to-Source Leakage Current	IGSS	V <sub>GS</sub> =±30V, V <sub>DS</sub> =0		100	±100	nA
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =10V, I <sub>D</sub> =1mA	2.0	M. AL	3.0	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =20V, I <sub>D</sub> =0.1A	0.08	0.15		S

Marking: K1458 Continued on next page.

- Any and all SANYO products described or contained herein do not have specifications that can handle applications that require extremely high levels of reliability, such as life-support systems, aircraft's control systems, or other applications whose failure can be reasonably expected to result in serious physical and/or material damage. Consult with your SANYO representative nearest you before using any SANYO products described or contained herein in such applications.
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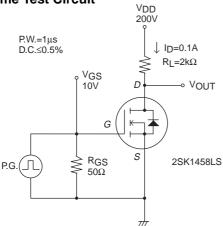
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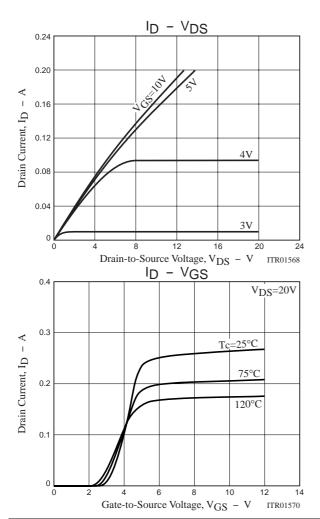
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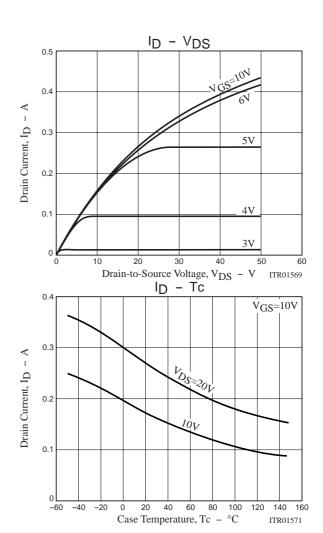
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Unit
Static Drain-to-Source On-State Resistance	RDS(on)	ID=0.1A, VGS=10V		50	70	Ω
Input Capacitance	Ciss	V <sub>DS</sub> =20V, f=1MHz		45		pF
Output Capacitance	Coss	V <sub>DS</sub> =20V, f=1MHz		25		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =20V, f=1MHz		10		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	I <sub>D</sub> =0.1A, V <sub>GS</sub> =10V, V <sub>DD</sub> =200V, R <sub>GS</sub> =50Ω		10		ns
Rise Time	t <sub>r</sub>	I <sub>D</sub> =0.1A, V <sub>G</sub> S=10V, V <sub>DD</sub> =200V, R <sub>G</sub> S=50Ω		15		ns
Turn-OFF Delay Time	t <sub>d</sub> (off)	ID=0.1A, VGS=10V, VDD=200V, RGS=50Ω		30		ns
Fall Time	tf	I <sub>D</sub> =0.1A, V <sub>GS</sub> =10V, V <sub>DD</sub> =200V, R <sub>GS</sub> =50Ω		180		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =0.2A, V <sub>G</sub> S=0			1.8	V

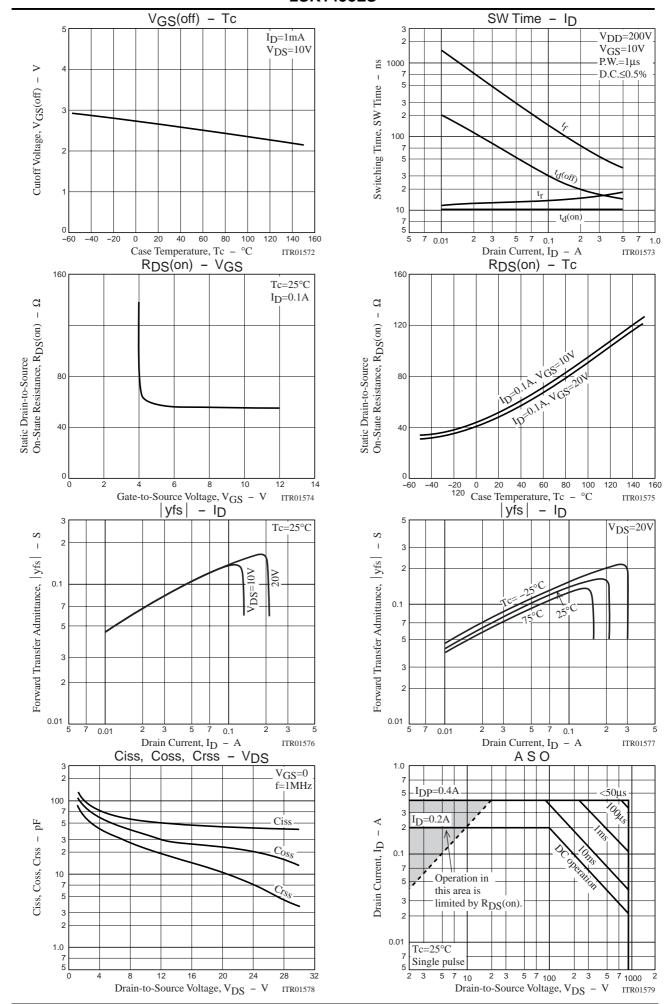
(Note) Be careful in handling the 2SK1458LS because it has no protection diode between gate and source.



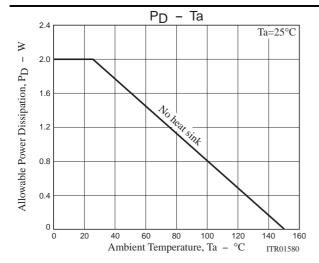


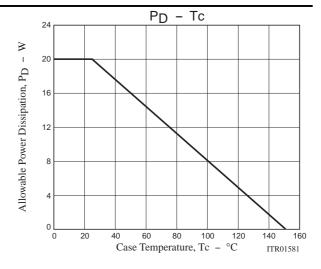






#### 2SK1458LS





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