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TC7MBD3245AFK

TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC7MBD3245AFK

Octal Bus Switch

The TC7MBD3245AFK provides eight bits of high-speed TTL-compatible bus switching in a standard '245 device pinout. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The device is organized as one 8-bit switch. When output enable (\overline{OE}) is low, the switch is on and port A is connected to port B. When \overline{OE} is high, the switch is open and a high-impedance state exists between the two ports.

The device is enable to realize the shift of signal level from 5 V to 3.3 V.

All inputs are equipped with protection circuits against static discharge.

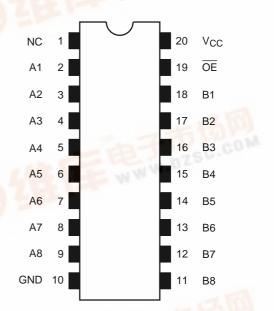
Features

- Operating voltage: $V_{CC} = 4.5 \sim 5.5 \text{ V}$
- High speed: $t_{pd} = 0.32 \text{ ns} (\text{max})$
- Low on resistance: $RON = 5 \Omega$ (typ.)
- ESD performance: Human body model > ±2000 V Machine model > ±200 V
- Compatible with TTL outputs (control inputs)
- Low Power Dissipation: Icc = 10 μA (max.)
- Package: VSSOP (US20)
- Pin compatible with the 74xx245 type.
- Functionally equivalent to (FST/CBT) 3245.



Weight: 0.03 g (typ.)

Pin Assignment (top view)



NC-No Internal Connection

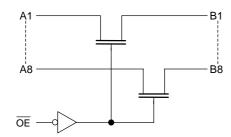


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Truth Table

Inputs	Function	
OE		
L	A port = B port	
Н	Disconnect	

System Diagram



Maximum Ratings

Characteristics	Symbol	Rating	Unit
Power supply range	V _{CC}	-0.5~7.0	V
DC input voltage	V _{IN}	-0.5~7.0	V
DC switch voltage	VS	-0.5~7.0	V
Input diode current	I _{IK}	-50	mA
Continuous channel circuit	IS	128	mA
Power dissipation	PD	180	mW
DC V _{CC} /ground current	I _{CC} /I _{GND}	±100	mA
Storage temperature	T _{stg}	-65~150	°C

Recommended Operating Conditions

Characteristics	Symbol	Rating	Unit
Supply voltage	V _{CC}	4.5~5.5	V
Input voltage	V _{IN}	0~5.5	V
Switch voltage	VS	0~5.5	V
Operating temperature	T _{opr}	-40~85	°C
Input rise and fall time	dt/dv	0~10	ns/V

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Electrical Characteristics

DC Characteristics (Ta = -40~85°C)

Charac	teristics	Symbol	Test Condition		V _{CC} (V)	Min	Typ. (Note 1)	Max	Unit
Innut voltogo	"H" level	VIH	_			2.0	_	_	v
Input voltage	"L" level	VIL			4.5~5.5		_	0.8	v
					4.75	2.3	2.8	3.2	
High-level outp	(Note 2)	V _{OH}	IOH=-1μA V _{IS} = V _{CC}		5.0	2.5	3.0	3.4	V
	(Note 2)				5.25	2.7	3.2	3.6	
Input leakage of	current	I _{IN}	V _{IN} = 0~5.5 V		4.5~5.5		_	±1.0	μA
Power off leaka	age current	IOFF	A, B, \overline{OE} = 0~5.5 V		0	_		±1.0	μA
Off-STATE leal (switch off)	kage current	I _{SZ}	A, B = 0~5.5 V, $\overline{OE} = V_{CC}$		4.5~5.5		_	±1.0	μA
				lia 61 mA	4.5		5	9	
			$V_{IS} = 0 V$	I _{IS} = 64 mA	4.75	_	5	8	
ON resistance		Paul		I _{IS} = 30 mA	4.5	_	5	9	Ω
	(Note 3)	IIS	11S – 30 MA	4.75	_	5	8		
	14.5	Via 22V/ lia 15m	(- 22)/ h- 15 m/		mA 4.5 —	_	35		65
			$V_{IS} = 2.3 \text{ V}, I_{IS} = 15 \text{ mA}$		4.75	_	35	50	
Quiescent supp	oly current	ICC	VIN = VCC or GND,I _{OUT} = 0		5.5		_	10	μA
Increase in I _{CC}	; per input	Δlcc	V _{IN} = 3.4 V (one input)		5.5		_	2.5	mA

Note 1: Typical values are at $V_{CC} = 5 V$, Ta = 25°C.

Note 2: It recommends that this device uses Pull-up resistance when adding and using resistance for an output terminal. Since it couses to drop a VOH voltage level when using Pull-down resistance for an output terminal.

Note 3: Measured by the voltage drop between A and B pins at the indicated current through the switch. On resistance is determined by the lower of the voltages on the two (A or B) pins.

AC Characteristics ($Ta = -40 \sim 85^{\circ}C$)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Min	Max	Unit
Propagation delay time	t _{pLH}	Figure 1, Figure 2 (Note 4)	4.5		0.32	ns
(bus to bus)	t _{pHL}		4.5		0.32	115
Output enable time	t _{pZL}	Figure 1, Figure 3	4.5		7.0	ns
	t _{pZH}		4.5		7.0	115
Output disable time	t _{pLZ}	Figure 1, Figure 3	4.5		7.0	ns
	t _{pHZ}		4.5		7.0	115

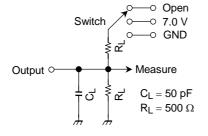
Note 4: The propagation delay time is calculated by the RC (on-resistance and load capacitance) time constant.

Capacitive Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	V _{CC} (V)	Тур.	Unit
Control pin input capacitance	C _{IN}	(Note 5)	5.0	3	pF
Switch terminal capacitance	C _{I/O}	$\overline{OE} = V_{CC}$ (Note 5)	5.0	10	pF

Note 5: This parameter is guaranteed by design.

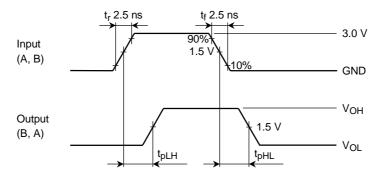
AC Test Circuit

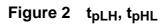


Paramenter	Switch		
t _{pLH} , t _{pHL}	Open		
t _{pLZ} , t _{pZL}	7.0 V		
t _{pHZ} , t _{pZH}	Open		

Figure 1

AC Waveform





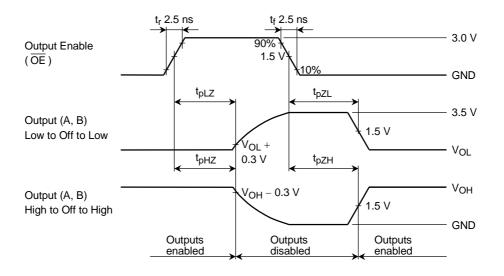


Figure 3 $t_{pLZ}, t_{pHZ}, t_{pZL}, t_{pZH}$

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V_{OH} – V_{CC} Characteristics (typ.)

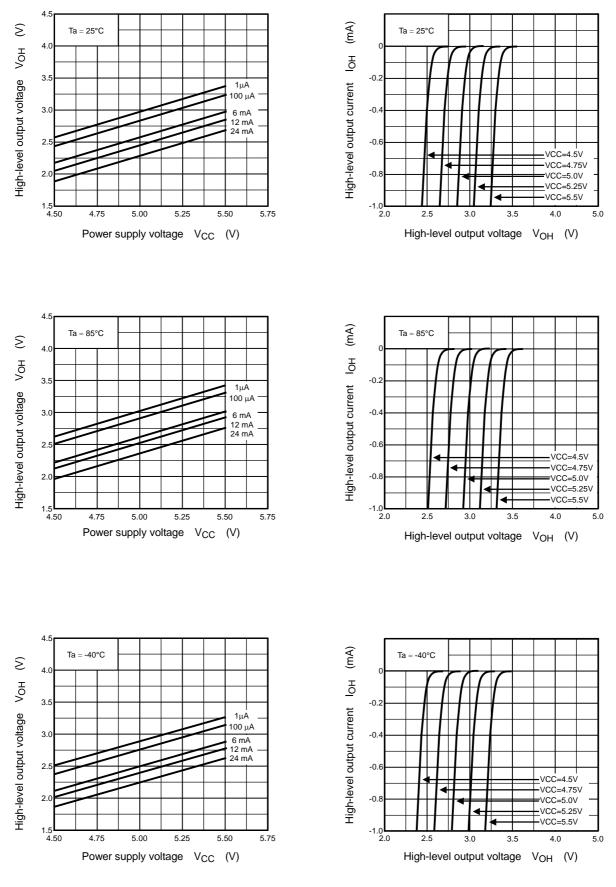
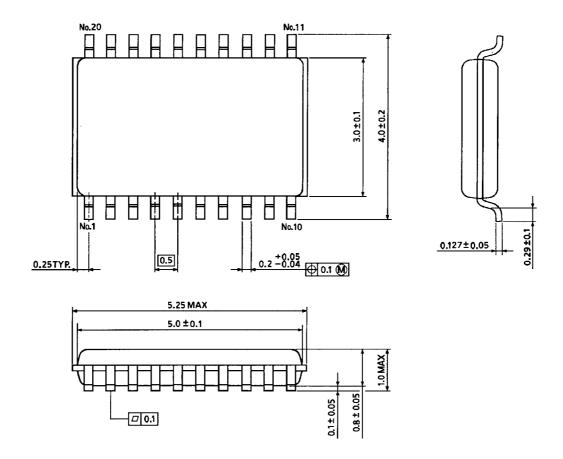


Figure 4

Package Dimensions

VSSOP20-P-0030-0.50

Unit : mm



Weight: 0.03 g (typ.)

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