MIC5306

150mA Micropower µCap Baseband LDO

General Description

The MIC5306 is a micropower, µCap low dropout regulator designed for optimal performance in a small space. It is capable of sourcing 150mA of output current and only draws 16µA of operating current. This high performance LDO offers fast transient response and good PSRR while consuming a minimum of current.

Ideal for battery operated applications; the MIC5306 offers 1% accuracy, extremely low dropout voltage (45mV @ 100mA). Equipped with a TTL logic compatible enable pin, the MIC5306 can be put into a zero-off-mode current state, drawing no current when disabled.

The MIC5306 is a µCap design, operating with very small ceramic output capacitors for stability, reducing required board space and component cost.

The MIC5306 is available in fixed output voltages in Thin SOT23-5 packaging.

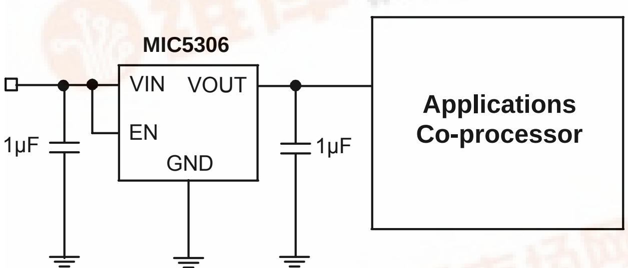
Features

- Input voltage range: 2.25V to 5.5V
- Ultra-low I_Q: Only 16µA operating current
- Stable with ceramic output capacitor
- Low dropout voltage of 45mV @ 100mA
- High output accuracy
  - ±1.0% initial accuracy
  - ±2.0% over temperature
- Thermal Shutdown Protection
- Current Limit Protection

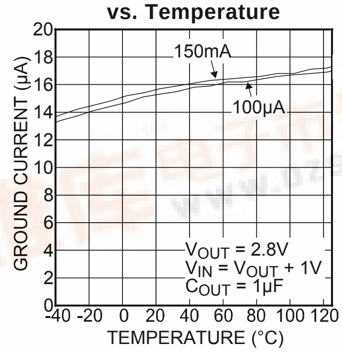
Applications

- Digital Logic Power Supply
- Stand-by power supply
- Cellular phones
- PDAs
- Portable electronics
- Notebook PCs

Typical Application



Ground Pin Current vs. Temperature



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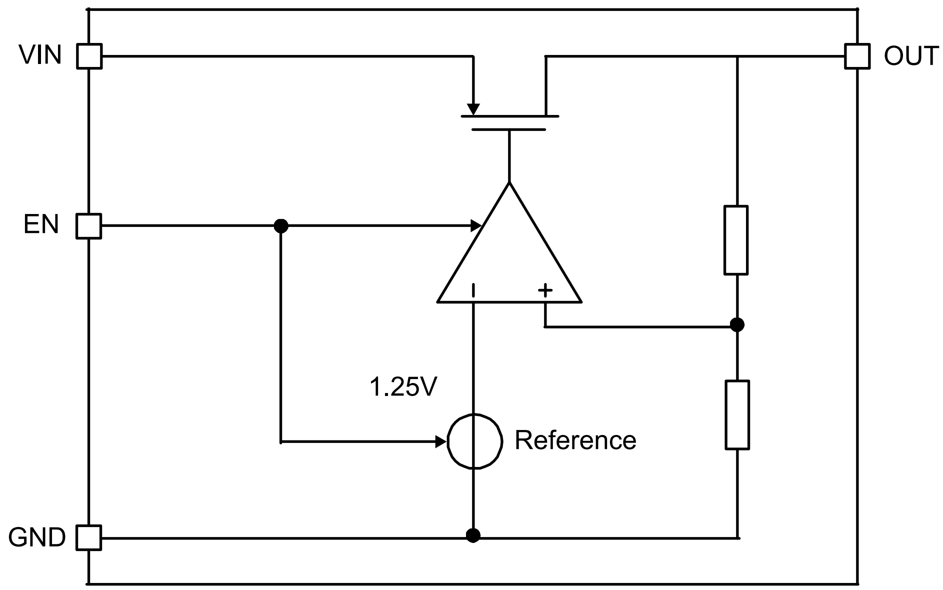
Block Diagram



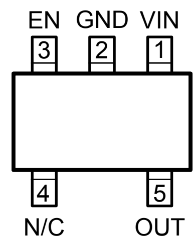
Ordering Information

Part Number	Marking Code	Output Voltage	Junction Temperature Range	Package
MIC5306-1.5YD5	N915	1.5V	-40°C to 125°C	TSOT23-5
MIC5306-2.8YD5	N928	2.8V	-40°C to 125°C	TSOT23-5

Note:

Other Voltage available. Contact Micrel for detail.

Pin Configuration



MIC5306-x.xBD5

Pin Description

TSOT23 Pin No.	Pin Description	Pin Function
1	IN	Supply Input
2	GND	Ground
3	EN	Enable Input. Active High. High = on, low = off. Do not leave floating.
4	NC	No Connect
5	OUT	Output Voltage

Absolute Maximum Ratings[1]

Supply Input Voltage (V_{IN})............................0V to 6V
Enable Input Voltage (V_{EN})............................0V to 6V
Power Dissipation (P_D)..................Internally Limited[3]
Junction Temperature .... -40°C to +125°C
Lead Temperature (soldering, 5sec.)................260°C
Storage Temperature (T_s) .... -65°C to +150°C

Operating Ratings[2]

Supply Input Voltage (V_{IN})......................2.25V to 5.5V
Enable Input Voltage $_{(EN1/EN2/LOWQ)}$................. 0V to VIN
Junction Temperature (T_J) .... -40°C to +125°C
TSOT23-5(θ_{JA}) .... 235°C

Electrical Characteristics

$V_{IN} = V_{OUT} + 1.0V$; $C_{OUT} = 1.0\mu F$, $I_{OUT} = 100\mu A$; $T_I = 25°C$, **bold** values indicate -40°C to +125, unless noted.

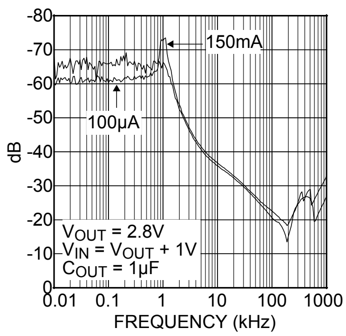
Parameter	Conditions	Min	Typ	Max	Units
Output Voltage Accuracy	Variation from nominal V_{OUT}	-1		+1	%
	Variation from nominal V_{OUT}; -40°C to +125°C	**-2**		**+2**	%
Line Regulation	$V_{IN} = V_{OUT}$ +1V to 5.5V		0.01	0.3 **0.5**	%/V
Load Regulation	I_{OUT} = 100µA to 150mA		0.5	1 **1.5**	%
Dropout Voltage[4]	I_{OUT} = 50mA I_{OUT} = 100mA I_{OUT} = 150mA		25 45 65	 **200**	mV
Ground Pin Current	I_{OUT} = 0mA to 150mA; V_{IN} = 5.5V		16	**25**	µA
Ground Pin Current in Shutdown	$V_{EN} \leq 0.2V$; V_{IN} = 5.5V		0.01	**1**	µA
Ripple Rejection	f = 10Hz to 1kHz; C_{OUT} = 1µF; I_{OUT} = 150mA f = 20kHz; C_{OUT} = 1µF; I_{OUT} = 150mA		62 35		dB
Current Limit	V_{OUT} = 0V	175	285	500	mA
Thermal Shutdown			150		°C
Thermal Shutdown Hysteresis			15		°C
Output Voltage Noise	C_{OUT} = 1µF; 10Hz to 100kHz		91		µVrms
Enable Input					
Enable Input Voltage	Logic Low			**0.2**	V
	Logic High	**1.0**			V
Enable Input Current	$V_{IL} \leq 0.2V$		0.01	**1**	µA
	$V_{IH} \geq 1.0V$		0.01	**1**	µA
Turn-on Time[5]	C_{OUT} = 1µF		250	**500**	µs

Notes:

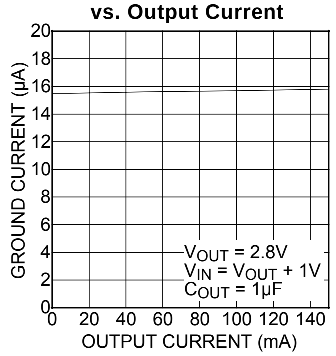
1. Exceeding the absolute maximum rating may damage the device.
2. The device is not guaranteed to function outside its operating rating.
3. The maximum allowable power dissipation of any T_A (ambient temperature) is $P_{D(max)} = T_{J(max)} – T_A / \theta_{JA}$. Exceeding the maximum allowable power dissipation will result in excessive die temperature, and the regulator will go into thermal shutdown.
4. Dropout voltage is defined as the input-to-output differential at which the output voltage drops 2% below its nominal value measured at 1V differential. For outputs below 2.25V, dropout voltage is the input-to-output differential with the minimum input voltage 2.25V.
5. Turn-on time is measured from Ven=1V of the positive edge of the enable signal to 90% of the rising edge of the output voltage of the regulator.

Typical Characteristics

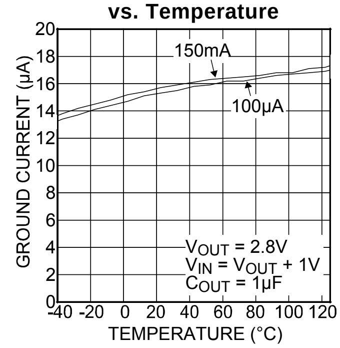
Ripple Rejection



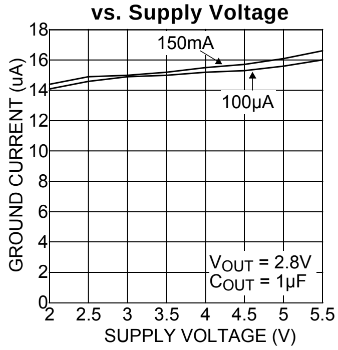
Ground Pin Current vs. Output Current



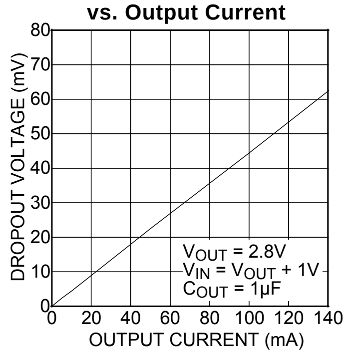
Ground Pin Current vs. Temperature



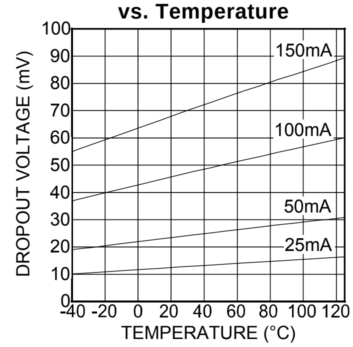
Ground Pin Current vs. Supply Voltage



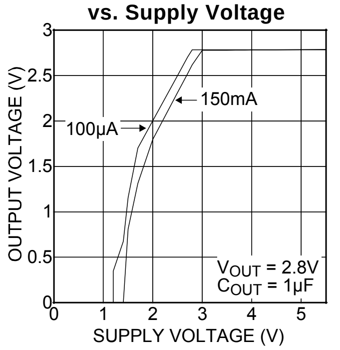
Dropout Voltage vs. Output Current



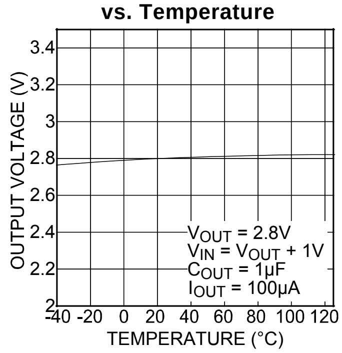
Dropout Voltage vs. Temperature



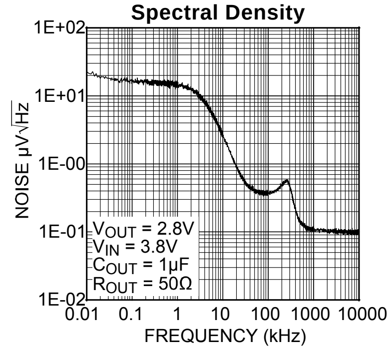
Output Voltage vs. Supply Voltage



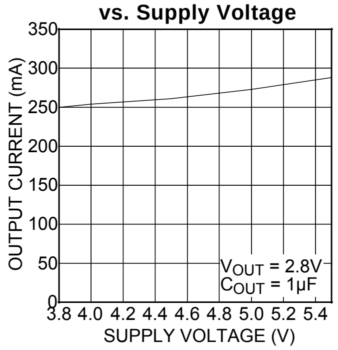
Output Voltage vs. Temperature



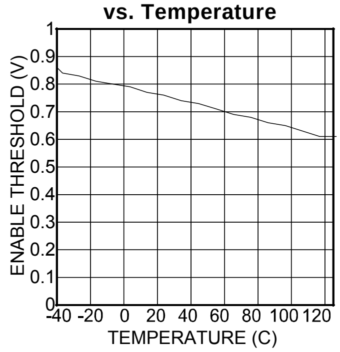
Output Noise Spectral Density



Current Limit vs. Supply Voltage

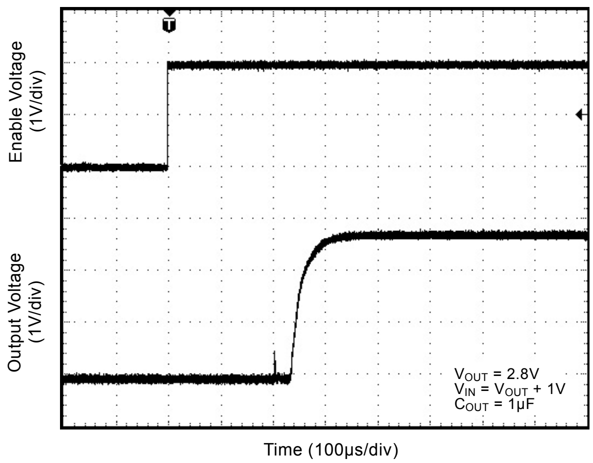


Enable Threshold vs. Temperature

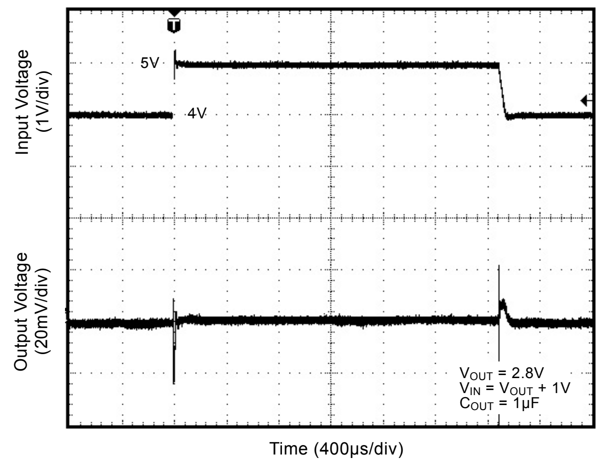


Functional Characteristics

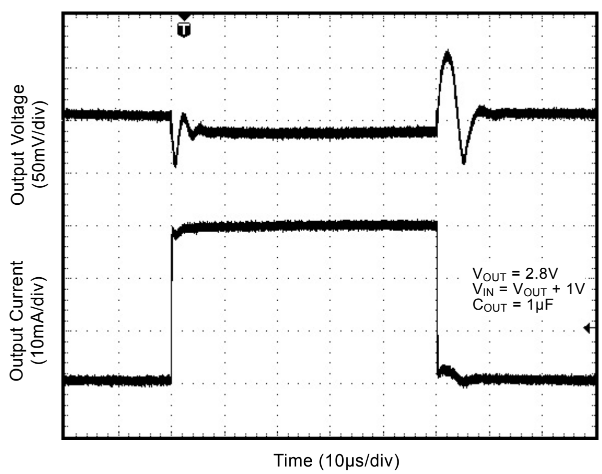
Enable Turn-On Transient



Line Transient Response



Load Transient Response



Applications Information

Input Capacitance

A 1µF capacitor should be placed from IN to GND if there is more than 10 inches of wire between the input and the ac filter capacitor or if a battery is used as the input.

Output Capacitance

An output capacitor is required between OUT and GND to prevent oscillation. Larger values improve the regulator's transient response. The output capacitor value may be increased without limit.

The output capacitor should have below ESR 300mΩ and a resonant frequency above 1MHz. Ultra-low-ESR capacitors can cause a low amplitude oscillation on the output and/or underdamped transient response. Most tantalum or aluminum electrolytic capacitors are adequate; film types will work, but are more expensive. Since many aluminum electrolytics have electrolytes that freeze at about –30°C, solid tantalums are recommended for operation below –25°C.

At lower values of output current, less output capacitance is required for output stability. The capacitor can be reduced to 0.47µF for current below 10mA or 0.33µF for currents below 1mA.

Enable

Forcing EN (enable/shutdown) high (>1V) enables the regulator. EN is compatible with CMOS logic gates. If the enable/shutdown feature is not required, connect EN (pin 3) to IN (supply input, pin 1).

Current Limit

There is overcurrent protection circuitry built into the MIC5306. Even with the output grounded, current will be limited to approximately 285mA. Further protection is provided by thermal shutdown.

Thermal Considerations

The MIC5306 is designed to provide 150mA of continuous current in a very small package. Maximum ambient operating temperature can be calculated based on the output current and the voltage drop across the part. Given that the input voltage is 3.8V, the output voltage is 2.8V and the output current equals 150mA.

The actual power dissipation of the regulator circuit can be determined using the equation:

$$P_D = (V_{IN} - V_{OUT})\, I_{OUT} + V_{IN}\, I_{GND}$$

Because this device is CMOS and the ground current is typically < 50µA over the load range, the power dissipation contributed by the ground current is < 1% and can be ignored for this calculation.

$$P_D = (3.8V - 2.8V) \cdot 150mA$$

$$P_D = 0.15W$$

To determine the maximum ambient operating temperature of the package, use the junction-to-ambient thermal resistance of the device and the following basic equation:

$$P_D(max) = \frac{T_J(max) - T_A}{\theta_{JA}}$$

$T_J(max) = 125°C$, the maximum junction temperature of the die θ_{JA} thermal resistance = 235°C/W

Table 1 shows junction-to-ambient thermal resistance for the MIC5306 in the TSOT23-5 package.

Package	θ_{JA} Recommended Minimum Footprint	θ_{JC}
TSOT23-5	235°C/W	2°C/W

Table 1. TSOT23-5 Thermal Resistance

Substituting PD for PD(max) and solving for the ambient operating temperature will give the maximum operating conditions for the regulator circuit. The junction-to-ambient thermal resistance for the minimum footprint is 235°C/W, from Table 1. The maximum power dissipation must not be exceeded for proper operation.

For example, when operating the MIC5306-2.8 at an input voltage of 3.8V and 150mA load with a minimum footprint layout, the maximum ambient operating temperature T_A can be determined as follows:

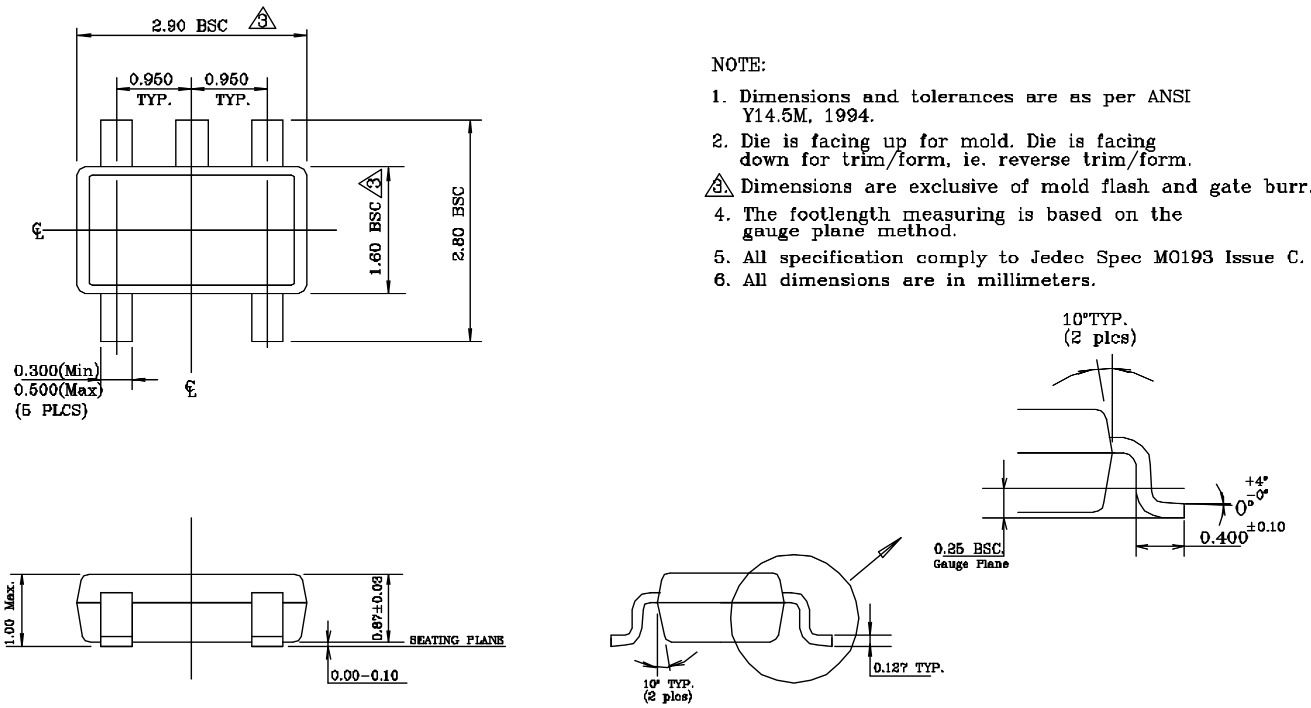
$$0.15W = (125°C - T) / 235C°/W$$

$$T = 89.75°C$$

Therefore, a 2.8V application at 150mA of output current can accept an ambient operating temperature of 89.8°C in a TSOT23-5 package. For a full discussion of heat sinking and thermal effects on voltage regulators, refer to the "Regulator Thermals" section of Micrel's Designing with Low-Dropout Voltage Regulators handbook. This information can be found on Micrel's website at:

http://www.micrel.com/_PDF/other/LDOBk_ds.pdf

Package Information



5-Pin TSOT-23

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