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Advance Information Dual PLLs for 46/49 MHz Cordless Telephones CMOS

These devices are dual phase–locked loop frequency synthesizers intended for use primarily in 46/49 MHz cordless phones with up to 15 channels. These parts contain two mask–programmable counter ROMs for receive and transmit loops with two independent phase detect circuits. A common reference oscillator and reference divider are shared by the receive and transmit circuits.

Other features include a lock detect circuit for the transmit loop, illegal code default, a buffered oscillator output for mixing purposes in the system, and a 5.0 kHz tone output.

- Maximum Operating Frequency: 60 MHz @ Vin = 200 mV p-p
- Operating Temperature Range: 40 to + 75°C
- Operating Voltage Range: 2.5 to 5.5 V
- On–Chip Oscillator Circuit Supports External Crystal
- Operating Power Consumption: 3.0 mA @ 3.0 V
- Lock Detect Signal
- Standby Mode for Power Savings: 1.5 mA @ 3.0 V
- Two Versions:

1/98SC.TN98011400

- MC145168 Up to 15–Channel ROM with 4–Bit Binary Code Input for Channel Pair Selection
- MC145169 Up to 15–Channel ROM with Serial Interface for Channel Pair Selection
- Custom 20–Channel ROM Versions of the MC145169 are Possible; Consult Factory

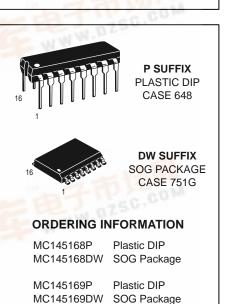
NOT RECOMMENDED FOR NEW DESIGNS; PRODUCT TO BE PHASED OUT. Closest equivalent is MC145162.

PIN ASSIGNMENTS

MC145168				MC145169			
osc _{out} [1•	16] OSC _{in}	osc _{out} [1•	16] osc _{in}
MODE [2	15] ∨ _{DD}	MODE [2	15	DD ^V DD
SB [3	14] f _{in1}	SB [3	14] f _{in1}
5 k [4	13] PD1	5 k [4	13] PD1
D0 [5	12	I V _{SS}	DATA [5	12] ∨ _{SS}
D1 [6	11] PD2	СГК [6	11] PD2
D2 [7	10		ИС [7	10	
D3 [8	9	f _{in2}	ENB [8	9] f _{in2}

NC = NO CONNECTION

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MC145168

MC145169



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