



# **6A05G THRU 6A10G**

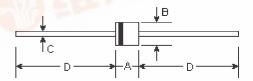
### **GLASS PASSIVATED JUNCTION RECTIFIER**

Reverse Voltage - 50 to 1000 Volts Forward Current - 6.0 Amperes

#### **Features**

- High surge current capability
- Plastic package has Underwriters Laboratory Flammability classification 94V-0 utilizing
   Flame retardant epoxy molding compound
- Glass passivated junction in R-6 package
- High current operation 6.0 ampere @ T₁=75℃

# R-6



## **Mechanical Data**

Case: Molded plastic, R-6

• Terminals: Axial leads, solderable per

MIL-STD-202, method 208

Polarity: Color band denotes cathode

Mounting Position: Any

• Weight: 0.074 ounce, 2.105 grams

DIMENSIONS										
DIM	inches		m	Note						
	Min.	Max.	Min.	Max.	Note					
Α	0.339	0.358	8.6	9.1						
В	0.339	0.358	8.6	9.1	ф					
С	0.047	0.052	1.2	1.3	ф					
D	1.000	-	25.40	-						

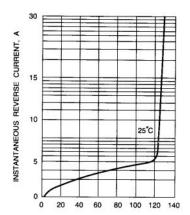
# Maximum Ratings and Electrical Characteristics

\* @T₄=25°C unless otherwise specified. Single phase, half-wave, 60Hz, resistive or inductive load.

	Symbols	6A05G	6A1G	6A2G	6A4G	6A6G	6A8G	6A10G	Units
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	50	100	200	400	600	800	1000	Volts
Maximum RMS voltage	V <sub>RMS</sub>	35	70	140	280	420	560	700	Volts
Maximum DC blocking voltage	V <sub>DC</sub>	50	100	200	400	600	800	1000	Volts
Maximum average forward rectified current at 75°C	I <sub>(AV)</sub>	6.0							Amps
Maximum overload surge current at 1 cycle (Note 1)	I <sub>FSM</sub>	400.0							Amps
Maximum forward voltage at 6.0A DC	at 6.0A DC V <sub>F</sub> 1.0						Volt		
Maximum full load reverse current, full cycle average at 25°C	I <sub>R</sub>	10							
Maximum DC reverse current at rated DC blocking voltage and 100 ℃	I <sub>R</sub>	500							
Typical junction capacitance (Note 2)	C <sub>J</sub>	150.0							ρF
Typical thermal resistance (Note 3)	R <sub>⊕JA</sub> R <sub>⊕JL</sub>	20.0 4.0							°C/W
Operating temperature range	T,	-55 to +150							$^{\circ}$
Storage temperature range	T <sub>STG</sub>	-55 to +175							°C

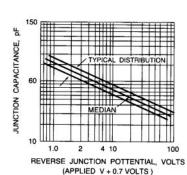
(1) Peak forward surge current, per 8.3 ms single half sine-wave superimposed on rated load (2) Measured at 1.0MHz and applied reverse voltage of 4.0 volts

#### RATINGS AND CHARACTERISTIC CURVES



PERCENT OF RATED PEAK REVERSE VOLTAGE

Fig. 1 - TYPICAL REVERSE CHARACTERISTICS



NOTE: WHEN PLOTTING CAPACITANCE VERSUS VOLTAGE.
IT IS CONVENIENT TO PLOT ON LOGALOG PAPER
AND TO PLOT APPULED VOLTAGE PLUS BARRIER
POTENTIAL (BARRIER POTENTIAL - 0,7 VOLTS), AS
THE ABSCISCA. THIS WILL GIVE A STRAIGHT LINE
OF SLOPE APPROXIMATELY 1/2 OF WHICH CAN BE
EASILY EXTRAPOLATED. CAPACITANCE AT
ZERO APPLIED VOLTS IS FOUND AT 0,7 VOLTS
ON THE PLOG. THIS TECHNIQUE WAS USED
FOR THE CURVE SHOWN.

Fig. 3 - CAPACITANCE CHARACTERISTICS

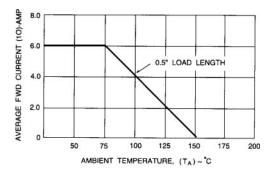


Fig. 2 - FORWARD DERATING CURVE

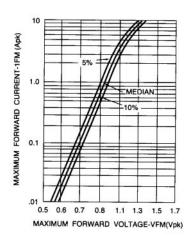


Fig. 4-TYPICAL FORWARD CHARACTERISTICS

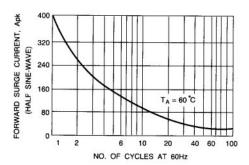


Fig. 5 - MAXIMUM OVERLOAD SURGE CURRENT