

National Semiconductor

May 1999

DS26S10 Quad Bus Transceiver

General Description

The DS26S10 is a quad Bus Transceiver consisting of 4 high speed bus drivers with open-collector outputs capable of sinking 100 mA at 0.8V and 4 high speed bus receivers. Each driver output is connected internally to the high speed bus receiver in addition to being connected to the package pin. The receiver has a Schottky TTL output capable of driving 10 Schottky TTL unit loads.

An active low enable gate controls the 4 drivers so that outputs of different device drivers can be connected together for party-line operation.

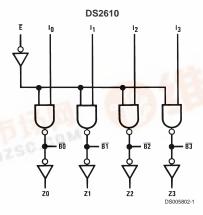
The bus output high-drive capability in the low state allows party-line operation with a line impedance as low as 100Ω . The line can be terminated at both ends, and still give considerable noise margin at the receiver. The receiver typical switching point is 2V.

The DS26S10 features advanced Schottky processing to minimize propagation delay. The device package also has 2 ground pins to improve ground current handling and allow close decoupling between V_{CC} and ground at the package. Both GND 1 and GND 2 should be tied to the ground bus external to the device package.

Features

- Input to bus is inverting on DS26S10
- Quad high speed open-collector bus transceivers
- Driver outputs can sink 100 mA at 0.8V maximum
- Advanced Schottky processing
- PNP inputs to reduce input loading

Logic Diagrams





Absolute Maximum Ratings (Note *NO

TARGET FOR FNXref NS859*)

If Military/Aerospace specified devices are required, please contact the National Semiconductor Sales Office/ Distributors for availability and specifications.

 -65°C to $+150^{\circ}\text{C}$ Storage Temperature -55°C to +125°C Temperature (Ambient) Under Bias Supply Voltage to Ground Potential -0.5V to +7V DC Voltage Applied to Outputs for –0.5V to +V $_{\rm CC}$ Max High Output State DC Input Voltage -0.5V to +5.5V Output Current, Into Bus 200 mA

Output Current, Into Outputs (Except Bus)

30 mA

-30 mA to +5 mA DC Input Current Maximum Power Dissipation (Note 1) at 25°C Molded Package 1362 mW

Operating Conditions

	Min	Max	Units
Supply Voltage (V _{CC})			
DS26S10	4.75	5.25	V
Temperature (T _A)			
DS26S10	0	+70	°C

Note 1: Derate cavity package 9.6 mW/°C above 25°C; derate molded package 10.9 mW/°C above 25°C, derate PLCC package TBD mW/°C above 25°C.

Electrical Characteristics

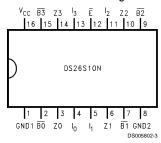
(Unless otherwise noted)

Symbol	Parameter	Conditions		Min	Тур	Max	Units
		(Note 2)			(Note 3)		
V _{OH}	Output High Voltage	V _{CC} = Min, I _{OH} = -1 mA,	Military	2.5	3.4		V
	(Receiver Outputs)	$V_{IN} = V_{IL}$ or V_{IH}	Commercial	2.7	3.4		V
V _{OL}	Output Low Voltage	V _{CC} = Min, I _{OL} = 20 mA,				0.5	V
	(Receiver Outputs)	$V_{IN} = V_{IL}$ or V_{IH}					
V _{IH}	Input High Level	Guaranteed Input Logical High	h for	2.0			V
	(Except Bus)	All Inputs					
V _{IL}	Input Low Level	Guaranteed Input Logical Low	/ for			0.8	V
	(Except Bus)	All Inputs					
V _I	Input Clamp Voltage	V _{CC} = Min, I _{IN} = -18 mA				-1.2	V
	(Except Bus)						
I _{IL}	Input Low Current	V _{CC} = Max, V _{IN} = 0.4V	Enable			-0.36	mA
	(Except Bus)		Data			-0.54	mA
I _{IH}	Input High Current	$V_{CC} = Max, V_{IN} = 2.7V$	Enable			20	μΑ
	(Except Bus)		Data			30	μΑ
I _I	Input High Current	V _{CC} = Max, V _{IN} = 5.5V				100	μΑ
	(Except Bus)						
I _{sc}	Output Short-Circuit Current	V _{CC} = Max, (Note 4)	Military	-20		-55	mA
	(Except Bus)		Commercial	-18		-60	mA
I _{CCL}	Power Supply Current	V _{CC} = Max, Enable = GND	DS26S10		45	70	mA
	(All Bus Outputs Low)		DS26S11			80	mA

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Connection Diagram

Dual-In-Line Package



Top View Order Number DS26S10N See NS Package Number N16A Plastic Chip Carrier

Bus Input/Output Characteristics

Symbol	Parameter	Conditions		Min	Typ(Note	Max	Units
		1)	(Note 2)		3)		
V _{OL}	Output Low Voltage		I _{OL} = 40 mA		0.33	0.5	
		V _{CC} = Min	I _{OL} = 70 mA		0.42	0.05	V
			I _{OL} = 100 mA		0.51	0.8	
Io	Bus Leakage Current		V _O = 0.8V			-50	
		V _{CC} = Max	V _O = 4.5V			100	μA
I _{OFF}	Bus Leakage Currrent (power OFF)	V _O = 4.5V				100	μA
V_{TH}	Receiver Input High Threshold	Bus enable = V _{CC} = Max	: 2.4V	2.25	2.0		V
V _{TL}	Receiver Input Low Threshold	Bus enable = V _{CC} = Min	: 2.4V		2.0	1.75	

Note 2: For conditions shown as min or max, use the appropriate value specified under Electrical Characteristics for the applicable device type.

Note 3: Typical limits are at V_{CC} = 5V, 25°C ambient and maximum loading.

Note 4: Not more than one output should be shorted at a time. Duration of the short circuit test should not exceed one second.

Switching Characteristics

 $(T_A = 25^{\circ}C, V_{CC} = 5V)$

Symbol	Parameter	Conditions		Min	Тур	Max	Units
t _{PLH}	Data Input to Bus	$R_B = 50\Omega, C_B = 50 \text{ pF (Note 5)}$	DS26S10		10	15	ns
t _{PHL}	Data Input to Bus		D320310		10	15	ns
t _{PLH}	Enable Input to Bus		DS26S10		14	18	ns
t _{PHL}	Enable Input to Bus		D326310		13	18	ns
t _{PLH}	Bus to Receiver Out	$R_{\rm B} = 50\Omega, R_{\rm L} = 280\Omega, C_{\rm B} = 50 \text{ pF(Note 5)}$			10	15	ns
t _{PHL}	Bus to Receiver Out	$C_L = 15 pF$			10	15	ns
t _r	Bus	$R_B = 50\Omega, C_B = 50 \text{ pF (Note 5)}$		4.0	10		ns
t _f	Bus			2.0	4.0		ns

Note 5: Includes probe and jig capacitance.

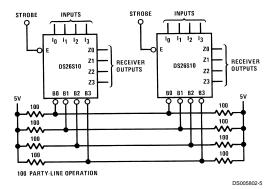
Truth Tables

DS26S10

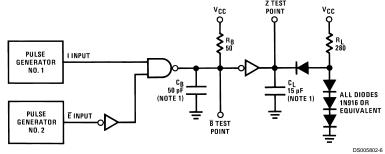
Inputs		Outputs		
Ē	I	B	Z	
L	L	Н	L	
L	Н	L	Н	
Н	×	Y	Y	

H = High voltage level
L = Low voltage level
X = Don't care
Y = Voltage level of bus (assumes control by another bus transceiver)

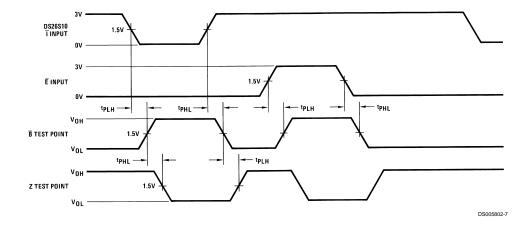
Typical Application



AC Test Circuit and Switching Time Waveforms

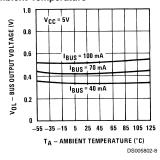


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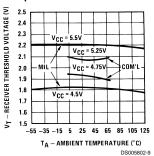


Typical Performance Characteristics

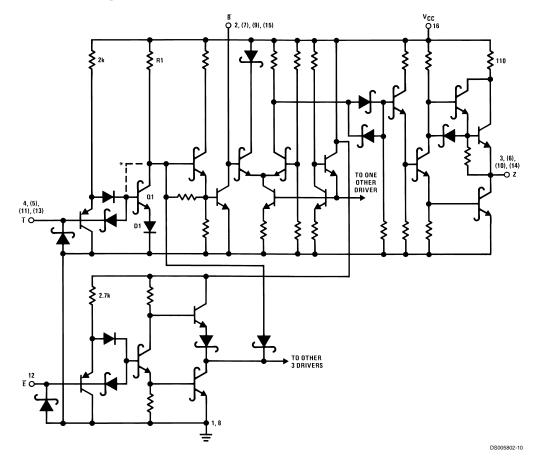
Typical Bus Output Low Voltage vs Ambient Temperature



Receiver Threshold Variation vs Ambient Temperature



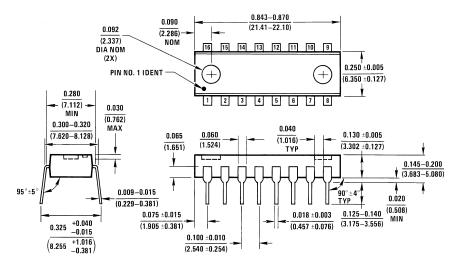
Schematic Diagram



V_{CC} = Pin 16 GND 1 = Pin 1 GND 2 = Pin 8

Connect for DS26S10
*Remove R1, Q1, D1 for DS26S10

Physical Dimensions inches (millimeters) unless otherwise noted



N16A (REV E)

Molded Dual-In-Line Package (N) Order Number DS26S10N NS Package Number N16A

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