

HIGH-EFFICIENCY STEP-DOWN DC-DC CONVERTER

SC1578

January 29, 1998

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DESCRIPTION

The SC1578 is a high performance step-down DC-DC converter, designed to drive an external P-channel MOSFET to generate programmable output voltages. Two main schemes of pulse-skipping and pulsefrequency modulation are employed to maintain low quiescent current and high conversion efficiency over wide ranges of input voltage and loads. The SC1578 delivers 10mA to 2A of output current with 87%-93% efficiency at V_{IN}=9V, V_{OUT}=5V. A current sense comparator with both inverting and non-inverting inputs uncommitted is included to provide the crucial function of either current limit protection or constant output current control. When the SC1578 is used in a highside current sensing step-down constant current source, the efficiency is typically greater than 90%. Duty cycle can be adjusted to greater than 90% by connecting a resistor from the DUTY pin to V_{IN}. Quiescent current is about 90µA and can be reduced to 8µA in shutdown mode. With a switching frequency range of 90kHz to 280kHz, small size switching components may be used, which is ideal for battery powered portable equipment.

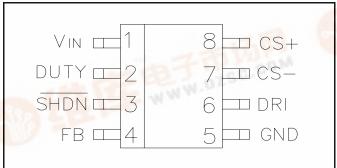
ORDERING INFORMATION

DEVICE ⁽¹⁾	PACKAGE		
SC1578CS	SO-8		

Note:

(1) Add suffix 'TR' for tape and reel.

PIN CONFIGURATION



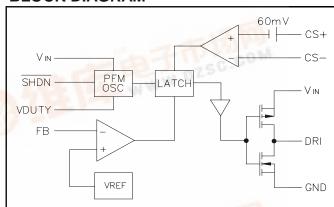
FEATURES

- 4V to 24V input voltage operation.
- High efficiency (up to 95%).
- Low quiescent current at 90µA.
- Pulse-skipping and pulse-frequency modulation.
- Inputs-uncommitted current sense comparator.
- Duty cycle adjustable
- 90kHz to 280kHz oscillator frequency
- Power-saving shutdown mode (8μA Typical).
- Push-pull driver output

APPLICATIONS

- Notebook 5V/3.3V main power
- Step-down DC-DC converter module.
- Constant current source for battery chargers.

BLOCK DIAGRAM



ABSOLUTE MAXIMUM RATINGS

Parameter	Symbol	Maximum	Units	
Supply Voltage	V _{IN}	24	V	
Duty Voltage	V_{DUTY}	20	V	
SHDN Voltage	V _{SHDN}	15	V	
Operating Temperature Range	T _A	0 to 70	°C	
Storage Temperature Range	T _{STG}	-65 to 150	°C	



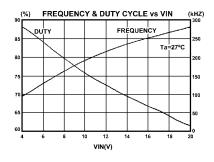


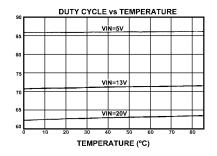
ELECTRICAL CHARACTERISTICS

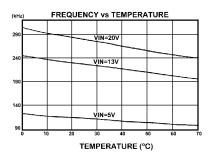
Unless otherwise specified, $T_A = 25$ °C, $V_{IN} = 13$ V

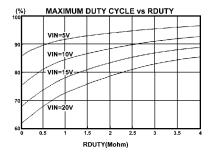
Parameter	Conditions	Min	Тур	Max	Units
Operation Voltage		4		24	V
Quiescent Current	V _{FB} = 1.5V		90	160	μA
Shutdown Mode Current	$V_{\overline{SHDN}} = 0V$		8	20	μA
Internal Reference Voltage		1.16	1.22	1.28	V
Driver Sinking "On resistance"			16		Ω
Driver Sourcing "On resistance"			11		Ω
Current Sense Comparator 157 Threshold 157		42 52	57 57	72 62	mV
Shutdown Threshold		0.8	1.5	2.4	V
SHDN Pin Leakage Current	V _{SHDN} < 15V			1	μA
Duty Cycle	$V_{DUTY} = V_{IN}$		71		%
Oscillator Frequency	$V_{DUTY} = V_{IN}$		225		kHz

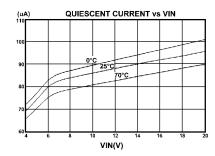
TYPICAL PERFORMANCE CHARACTERISTICS

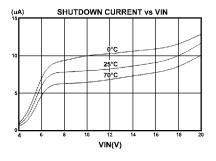




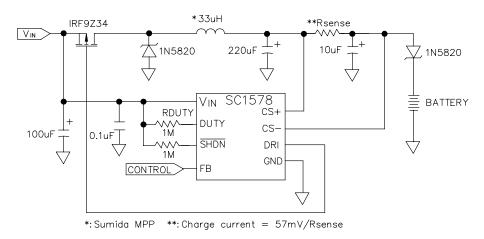








TYPICAL APPLICATION CIRCUIT



APPLICATION EXAMPLES

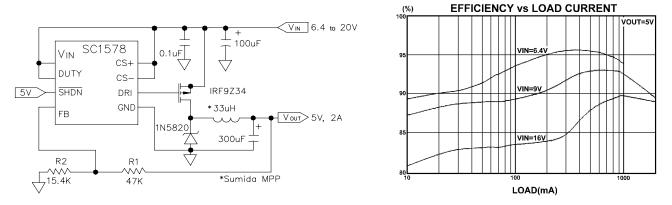


Figure 1. 5V Step-Down Converter

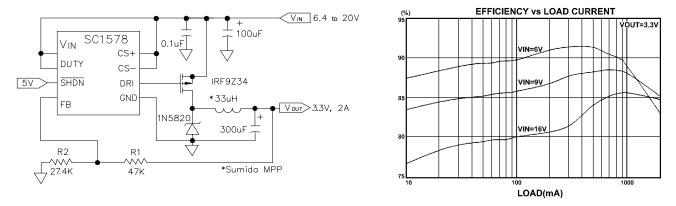


Figure 2. 3.3 Step-Down Converter

TYPICAL APPLICATION CIRCUIT

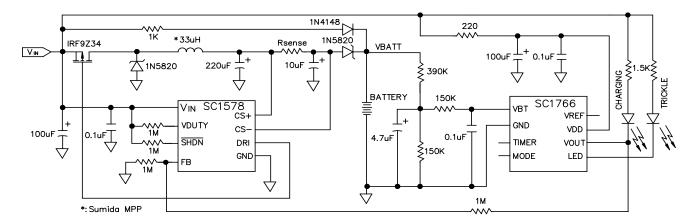


Figure 3. Battery Charge Circuit with High-Side Current Sensing Constant Current Source

NOTE: R_{SENSE} =0.1 ohm, charge current=0.5A ± 10%, V_{IN} > V_{BATT} + 3.5V R_{SENSE} =0.05 ohm, charge current=1A ± 10%, V_{IN} > V_{BATT} + 4V R_{SENSE} =0.033 ohm, charge current=1.5A ± 10%, V_{IN} > V_{RATT} + 4.5V Efficiency>90%, measured at CS- node

PIN DESCRIPTIONS

PIN 1: V_{IN} 4V to 24V input supply voltage.

PIN 2: DUTY Duty cycle adjustment pin. To be tied to the V_{IN} pin directly or through a resistor to adjust oscillator duty cycle. See Electrical Characteristics: R_{DUTY}

must be above $1M\Omega$ if $V_{IN}>20V$.

PIN 3: SHDN Logical input to shutdown the chip:

 V_{SHDN} = high for normal operation, V_{SHDN} = low for shutdown.

This pin should not be left floating or forced greater than 15V. In shutdown

mode the DRI pin is high.

PIN 4: FB Feedback comparator input, to compare the feedback voltage with the internal reference voltage. Connecting a resistor R1 to the converter output and

a resistor R2 to ground yields the output voltage:

 $V_{OUT} = 1.22 \times (R1+R2)/R2$

PIN 5: GND Power ground.

PIN 6: DRI Push-Pull driver output to drive an ex-

> ternal P-channel MOSFET or PNP transistor. When driving a PNP bipolar transistor, a base resistor and a capacitor to the base of PNP are recom-

mended.

PIN 7: CS-Current sense comparator inverting

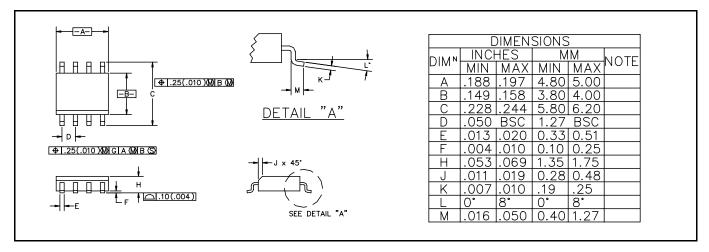
input, not to exceed V_{IN}.

PIN 8: CS+ Current sense comparator non-

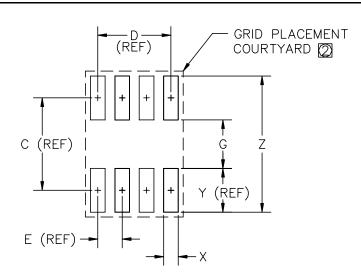
inverting input, not to exceed V_{IN}.



DEVICE OUTLINE - SO-8



LAND PATTERN - SO-8



	DIMENSIONS (1)					
DIM	INCHES		MM		NOTE	
	MIN	MAX	MIN	MAX		
С	_	.19	1	5.00	1	
D	_	.15	-	3.81	l	
E	_	.05		1.27		
G	.10	.11	2.60	2.80	l	
Χ	.02	.03	.60	.80	_	
Y	_	.09		2.40		
Ζ	_	.29	7.20	7.40	_	

- GRID PLACEMENT COURTYARD IS 12x16 ELEMENTS
 (6 mm X 8mm) IN ACCORDANCE WITH THE
 INTERNATIONAL GRID DETAILED IN IEC PUBLICATION 97.
- CONTROLLING DIMENSION: MILLIMETERS