Rohm

Structure : Silicon Monolithic Integrated Circuit Product Name : Power control for portable CDs

Device Name : BH6584KV

:

Features

- Low power consumption
- Low ON resistance
- VQFP_48C package
- <Power Control Part>
- Step-up DC/DC converter for microcomputers (With a built-in switching Tr)
- Main step-up/step-down DC/DC converter (With a built-in switching Tr)
- Sub step-up DC/DC converter (With a built-in switching Tr)
- VG step-up circuit for driving power MOS (With a built-in switching Tr)
- Ripple filter circuit
- Step-down regulator circuit (2.9V/3.8V, 1.5V)
- Reset circuit
- <Driver Part>
- Incorporating four channels of power MOS H-bridge drivers
- Supporting digital input
- Direct PWM driving method

O ABSOLUTE MAXIMUM RATINGS (Ta=25°C)

Parameter	Symbol	Limits	Unit
H-bridge Power Supply Voltage	PVCC	7	V
Battery Power Supply Voltage	PVCC0,PVCC1	7	V
Power Supply Voltage for Microcomputer	VMC	7	V
Power Supply Voltage for Core	REGOUT	7	V
System Power Supply Voltage	SVCC	7	V
Sub Power Supply Voltage	VSUB	7	v
AC Adaptor Power Supply Voltage	PVCC2	7	v
Pre Driver Power Supply Voltage	VG	7	v
DSW, USW Output Current	Ю	3 "	А
Power Dissipation	Pd	1180 *2	mW
Operating Temperature Range	Topr	-30 to 85	°C
Storage Temperature Range	Tstg	-55 to 150	°C

*1)Maximum time period for applying voltage: 5msec, interrupted current with Duty1/10 or less

*2)When mounted on the PCB board (glass/epoxy board with the size: 70 mm×70 mm, the thickness: 1.6 mm). Over Ta=25°C, derating at the rate of 9.5mW/°C.

O RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN	TYP	MAX	Unit
H-bridge Power Supply Voltage	PVCC	1.5	2.4	4.5	V
Battery Power Supply Voltage	PVCC0,PVCC1	1.5	2.4	4.5	V
Power Supply Voltage for Microcomputer	VMC	2.0	2.5	4.5	V
Power Supply Voltage for Core	REGOUT	1.4	1.5	1.6	v
System Power Supply Voltage	SVCC	2.0	2.5	4.5	
Sub Power Supply Voltage	VSUB	2.0	3.2	4.5	v
AC Adaptor Power Supply Voltage	PVCC2	2.0	4.5	6.5	V
Pre Driver Power Supply Voltage ³	VG	5.0	6.0	6.9	V
Ambient Temperature	Ta	-10	25	70	°C

*3)When supplied externally without using the built-in step-up circuit.

This product has not been checked for the strategic materials (or service) defined in the Foreign Exchange and Foreign Trade Control Low of Japan so that a verification work is required before exporting it.

Not designed for radiation resistance.

O ELECTRIC CHARACTERISTICS

(PVCC1=2.4V, SVCC=VMC=2.5V, VSUB=3.2V, VG=6V, Ta=25°C, unless otherwise noted.)

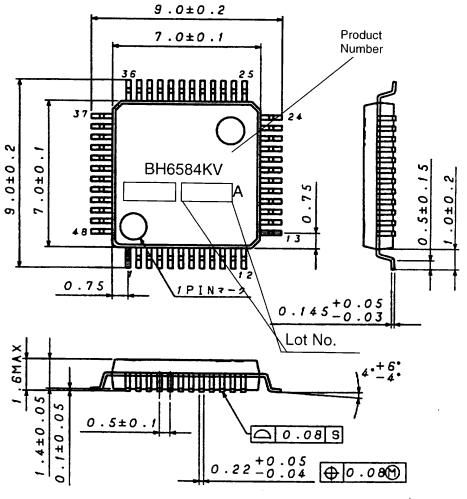
(PVCC1=2.4V, SVCC=VMC=2 Parameter	Symbol	, v.	TYP		Unit	Condition
<pre></pre>		IVIIIN		IVIAA	Unit	Condition
PVCC0+PVCC1+PVCC Standby Current	IPVCC01ST	-	100	170	uA	SVCC=VSUB=0V, VMC=3V
VMC Standby Current	IVMCST		23	50	uA	SVCC=VSUB=0V, VMC=3V
VG Standby Current	IVGST	-	- 23	3	uA uA	SVCC=VSUB=0V, VMC=3V
PVCC0 + PVCC1 Consumption Current	IPVCC01	-	0.7	1.3	mA	3400=4300=04, 4140=34
PVCC2 Consumption Current	IPVCC01		0.9	1.7	mA	PVCC2=5V
SVCC Consumption Current	ISVCC		1.8	2.7	mA	1 4002-54
VSUB Consumption Current	IVSUB	-	0.1	0.2	mA	
LMC Terminal Leak Current	INCLK		-	3	uA	LMC=6V, SVCC=VSUB=0V
VG Consumption Current	IVG		0.14	0.3	uA mA	fCLK=88.2kHz
LG Terminal Leak Current	ILGLK	-		3	uA	LG=6V, SVCC=VSUB=0V
H-bridge Driver>	I LOLK	-	-	<u></u>	UA	LG=0V, SVCC=VS0B=0V
				r		Sum of ON resistances (top +
Output ON Resistance	RON	-	1.8	2.8	Ω	bottom)
H-level Input Voltage	VIH	1.75	-	-	V	
L-level Input Voltage	VIL		-	0.75	V	
Output Propagation Delay Time	tRISE	-	65	300	nsec	
	tFALL	-	40	300	nsec	
Output Propagation Delay Time Difference	∆tRF	-50	25	200	nsec	tRISE-tFALL
Minimum Input Pulse Width	Tmin	-150	-	-	nsec	Output pulse width 1/2tmin or above
< PVCC2 Monitor Terminal>	4I					
Monitor Terminal Voltage 1	VMON1	1.1	1.2	1.3	V	
Monitor Terminal Voltage 2	VMON2	1.4	1.55	1.7	V	SVCC=1.5V, PVCC1=4.5V
<step-up converter="" step-down=""></step-up>			•	•		
SVCC Terminal Threshold Voltage	SVCCTH	2.43	2.50	2.57	V	
EO Terminal Output Voltage H	VEOTH	1.1	1.3	-	V	IEO=100 μA
EO Terminal Output Voltage L	VEOL	•		0.4	V	IEO=60 μA
VSUB Terminal Threshold Voltage	VSUBTH	3.00	3.20	3.40	v	fCLK=88.2kHz
<pre><short part="" protect=""></short></pre>			1			
SPRT Terminal Voltage (normal)	VSPRTIN	-	-	0.2	V	
SPRT Terminal Current 1 EO = H	ISPRT1	4	6	9	uA	
SPRT Terminal Current 2 Overcurrent	ISPRT2	8	12	18	uA	PVCC2=6.8V
SPRT Terminal Current 3 Overcurrent						
At Ground Fault	ISPRT3	1	2.5	4	UA	SVCC=0V
SPRT Terminal Impedance	RSPRT	240	320	400	kΩ	
SPRT Terminal Threshold Voltage	VSPRTTH	0.4	0.5	0.6	V	
SPRT Terminal Threshold Voltage At Ground Fault	VSPRTTH2	0.2	0.35	0.5	V	SVCC=0V
Overvoltage Protection Detection Voltage	VHVPR	6.2	6.5	6.8	v	
<pre></pre> <pre><</pre>		0.2	0.5	0.0	v	
SOFT Terminal Output Voltage	VSOFT	1.5	[25	v	
SOFT Terminal Current	ISOFT	<u> </u>	2 10	2.5 13	uA	
SOFT Terminal Current	RSOFT	180	220	280		
<pre>SOFT Terminal Impedance </pre>		100	220	280	kΩ	
START Terminal ON Threshold Voltage	VETATUA		1		1	
	VSTATH1	· ·		0.3	V	
START Terminal OFF Threshold Voltage	VSTATH2	1.0		-	V	
START Terminal Bias Current	ISTART	51	61	77	uA	START=2.5V
CLK Terminal Threshold Voltage H	VCLKTHH	1.5	-	-	<u>v</u>	fCLK=88.2KHz
CLK Terminal Threshold Voltage L	VCLKYHL			0.5	V	fCLK=88.2KHz
CLK Terminal Bias Current	VCLK	11	18	25	uA	VCLK=2.5V

Parameter	Symbol	MIN	TYP	MAX	Unit	Condition
<coil drive="" part=""></coil>	- Official					oonanon
DSW Terminal ON Resistance H	RDSWH	-	0.6	1.0	Ω	IDSW=500mA
DSW Terminal ON Resistance L	RDSWL	-	0.6	1.0	Ω	IDSW=-500mA
USW Terminal ON Resistance H	RUSWH					IUSW=500mA
USW Terminal ON Resistance L	RUSWL	-	0.6	1.0	Ω	
SSW Terminal ON Resistance H	RSSWH					ISSW=100mA
SSW Terminal ON Resistance L	RSSWL	-	2	3.3	Ω	
DSW Terminal Leak Current H	IDSWLKH	-	-	3	uA	START=2.5V, DSW=0V
DSW Terminal Leak Current L	IDSWLKL	-3		-	uA	START=2.5V, DSW=2.4V
USW Terminal Leak Current H	IUSWLKL		-	3	uA	START=2.5V, USW=0V
USW Terminal Leak Current L	IUSWLKL	-3			uA	START=2.5V, SSW=0V
SSW Terminal Leak Current H	ISSWLKH			3	uA	START=2.5V, SSW=3.2V
SSW Terminal Leak Current L	ISSWLKL	-3	-	-	uA	01111-2:01, 0011-0:21
<power for="" microcomputer="" part="" supply=""></power>				Į		
LMC Terminal Oscillation Frequency	fLMC	70	105	140	kHz	LMC=2.2V
LMC Terminal Output Voltage L	VLLMC		0.1	0.2	V	LMC=2.2V
	1					LMC=2.3V
VMC Terminal Threshold Voltage	VVMCTH	2.12	2.30	2.48	V	VMC=2.3→3V
······································						LMC=2.2V
VMC Terminal Threshold Hysteresis Voltage	VVMCHYS	30	60	100	mV	VMC=VVMCTH→2V
SVCC-VMC ON Resistance	RVMCSV	_	8	15	Ω	IVMC=50mA
SVCC-VINO OIT Resistance	110101030		0	15	52	(Turn OFF the step-up
LMCSW Terminal ON Threshold Voltage	VLMSWON	1.5	_		v	converter oscillation circuit for
Elvicer reminarer micshold voltage	LINGTON	1.5		-	•	microcomputer)
· · ··		-				(Turn ON the step-up converter
LMCSW Terminal OFF Threshold Voltage	VLMSWOFF	-	-	0.5	v v	oscillation circuit for
				0.0		microcomputer)
<starter circuitry=""></starter>	·		1	.		
LG Terminal Oscillation Frequency	fLG	75	100	125	kHz	VG=LG=5V
LG Terminal Maximum Pulse Duty	tLGMIN	72	80	88	%	VG=LG=5V
LG Terminal Minimum Pulse Width		0.4	-	0.5		LG=1V,
LG Terminal Minimum Pulse Width	DLGMIN	0.1	-	0.5	Usec	VG=5→7V
LG Terminal Output Voltage L	VLGL	-	0.1	0.2	V	VG=LG=5V
VG Terminal Starter Detection Voltage	VVGSTTH	3.1	3.6	4.1	V	VG=3→4.5V
VG Terminal Starter Detection Hysteresis			0.00	0.05		
Width	VVGHYS	0.05	0.20	0.35	V	VG=VVGSTTH→3V
VG Terminal Threshold Voltage	VVGTH	5.1	6.0	6.9	V	VLG=5V, VG=5→7V
<main converter="" dc=""></main>	·			•	-	- I
DSW Terminal Oscillation Frequency 1	fDSW1	46	58	70	kHz	EO=0.4V
DSW Terminal Oscillation Frequency 2	fDSW2	-	88.2	-	kHz	EO=0.4V, fCLK=88.2kHz
DSW Terminal Minimum Pulse Width	+DOMANNI	0.01		0.5		EO=0.4V→0.7V
Dow Terminal Minimum Pulse Width	tDSWMIN	0.01	-	0.5	usec	fCLK=88.2kHz
USW Terminal Oscillation Frequency 1	fUSW1	46	58	70	kHz	
USW Terminal Oscillation Frequency 2	fUSW2	-	88.2	-	kHz	fCLK=88.2kHz
USW Terminal Maximum Pulse Duty	DUSWMAX	65	73	81	%	fCLK=88.2kHz
						EO=0.7→0.4V
USW Terminal Minimum Pulse Width	tUSWMIN	0.01	-	0.5	usec	fCLK=88.2kHz
SSW Terminal Oscillation Frequency 1	fSSW1	46	58	70	kHz	
SSW Terminal Oscillation Frequency 2	fSSW2	-	88.2	-	kHz	fCLK=88.2kHz
USW Terminal Maximum Pulse Duty	DSSWMAX	65	73	81	%	fCLK=88.2kHz
SSW Terminal Minimum Pulse Width	tSSWMIN	0.01	-	2	usec	
					1	EO=0.4→0.7V sweep
UP/DOWN Switching EO Dead Band Width	VUDDEAD	-80	-45	-10	mV	FCLK=88.2kHz
USW Terminal Starter Frequency	fUSWSTA	75	100	125	kHz	
USW Terminal Starter Duty	DUSWSTA	16	24	32	%	
		. •	· · · ·			

Parameter	Symbol	MIN	TYP	MAX	Unit	Condition
<svcc circuitry=""></svcc>						· · · · · · · · · · ·
Discharge Reset Voltage	VDIS	1.40	1.51	1.62	V	
Starter→Normal Switching Voltage	VSTN	1.72	1.80	1.88	V	
Normal-Starter Switching Voltage	VNST	1.65	1.73	1.81	V	
Starter→Normal Switching Hysteresis Width	VSTNHIS	40	70	110	mV	VSTN-VNST
<reset circuitry=""></reset>	•					<u> </u>
RESET1 ON Voltage 1	VRESON11	-	-	0.4	V	SPRT=0.6V
RESET1 ON Voltage 2	VRESON12	1.34	1.42	1.50	V	LMCSW="L"
RESET1 ON Voltage 3	VRESON13	1.71	1.8	1.89	V	LMCSW="H"
RESET1 Reset Voltage	VORESOFT1	1.99	2.1	2.21	V	LMCSW="L", "H"
RESET1 Terminal Output Voltage	VORESET1	-	-	0.4	V	IRESET1=50 µA
RESET1 Terminal Leak Current	LKRESET1	-	-	3	uA	
<ripple filter="" part=""></ripple>						
AVCC-VSUB Voltage	VRF	170	205	240	mV	IAVCC=5mA
AVCC Terminal ON Resistance	RAVCC	-	3	10	Ω	IAVCC=15mA, VCRP=3.2V
CRP Terminal Input Resistance	RCRP	16	21	25	kΩ	ICRP=5 μA, 10 μA
AVCC Discharge Current	IAVCC	1.7	2.5	4.0	mA	VAVCC=3.1V
<regulator 1="" circuitry=""></regulator>			•			
Regulator Output Voltage 1	VREG1	3.6	3.8	4.0	v	PVCC1=OPEN, PVCC2=6V, VCNT=2.5V
Regulator Output Voltage 2	VREG2	2.7	2.9	3.1	v	PVCC1=OPEN, PVCC2=6V, VCNT=0.6V
REGB Terminal Output Voltage	VOREGB	-	-	1.7	V	PVCC2=6V, IREG=10mA
REGB Terminal Leak Current	ILKREGB	-	-	3	uA	PVCC1=3.2V, PVCC2=6V, REGB=6V
REGB Terminal Limit Current	ILMREGB	12	17	23	mA	PVCC2=6V
REGB Terminal Current at Ground Fault	IDPREGB	-	100	300	uA	PVCC1=0V, PVCC2=6V, REGB=6V
PNPsat Limit Voltage	VSATLIM	10	30	60	mV –	PVCC2=3.5V
PVCC2 Reduced-voltage Mute ON Voltage	VPVCC2LV	-	-	1.3	v	PVCC2 specified
PVCC2 Reduced-voltage Mute OFF Voltage	VPVCC2OF	1.9	-	-	v	PVCC2 specified
VCNT Terminal ON Threshold Voltage	VVCNTTH	1.5	-		V	
VCNT Terminal OFF Threshold Voltage	VVCNOTH	-	-	0.5	V	
VCNT Terminal Bias Current H	IVCNTH	15	33	50	uA	VCNT=2.5V
VCNT Terminal Bias Current L	IVCNTL	-	-	3	uA	VCNT=0V
<regulator 2="" circuitry=""></regulator>	•		• ·	•		
REGOUT Output Voltage	VREGOUT	1.4	1.6	1.6	V	
REGOUT Terminal ON Resistance	RREGOUT	-	-	20	Ω	IREGOUT=50mA

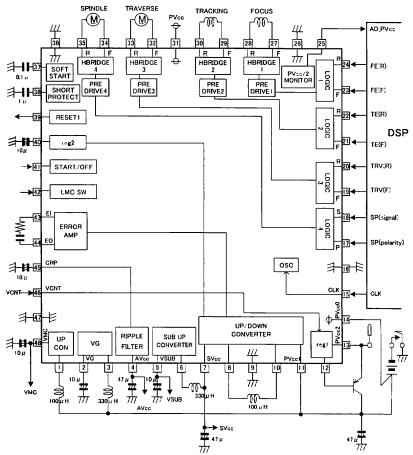


O OUTLINE DIMENSIONS



(UNIT: mm)

O APPLICATION CIRCUIT DIAGRAM



O PIN Description

~		Descriptiv	<u></u>			
	No.	Pin name	Description	No.	Pin name	Description
	1	LMC	Microcomputer power supply step-up coil driving terminal	25	VMON	PVCC/2 monitor terminal
	2	VG	Power MOS driving circuit power supply terminal	26	PGND2	Power part ground 2
	3	LG	VG step-up coil driving terminal	27	OUT_1F	CH1 forward output
	4	AVCC	Ripple filter output terminal	28	OUT_1R	CH1 reverse output
	5	VSUB	Sub power supply terminal	29	OUT_2F	CH2 forward output
L	6	SSW	Sub step-up coil driving terminal	30	OUT_2R	CH2 reverse output
Ĺ	7	SVCC	Control circuit power supply terminal	31	PVCC	Power part power supply input terminal
	8	USW	Main step-up/step-down coil driving terminal 2	32	OUT_3F	CH3 forward output
	9	PGND1	Power part ground 1	33	OUT_3R	CH3 reverse output
	10	DSW	Main step-up/step-down coil driving terminal 1	34	OUT_4F	CH4 forward output
Г	11	PVCC1	Battery power supply input terminal 1	35	OUT_4R	CH4 reverse output
	12	REGB	Tr for regulator 1 driving terminal	36	PGND3	Power part ground 3
Γ	13	PVCC2	AC adaptor power supply input terminal	37	SOFT	Soft start setting terminal
Γ	14	PVCC0	Battery power supply input terminal 0	38	SPRT	Power OFF time constant setting terminal
	15	CLK	DC/DC converter external clock input terminal	39	RESET1	Reset 1 detection output terminal
	16	GND	Pre part power supply ground	40	REGOUT	Regulator 2 output terminal
	17	IN4P	CH4 input terminal (polarity)	41	START	DC/DC converter ON/OFF terminal
	18	IN4S	CH4 input terminal (signal)	42	LMCSW	Step-up converter for microcomputer oscillation circuit ON/OFF terminal
	19	IN3F	CH3 forward input terminal	43	EI	Step-up/step-down DC/DC converter error amp input terminal
	20	IN3R	CH3 reverse input terminal	44	EO	Step-up/step-down DC/DC converter error amp output terminal
	21	IN2F	CH2 forward input terminal	45	CRP	Ripple filter smoothing capacitor connection terminal
	22	IN2R	CH2 reverse input terminal	46	VCNT	Regulator voltage switching terminal
L	23	IN1F	CH1 forward input terminal	47	PGND4	Power part ground 4
E	24	IN1R	CH1 reverse input terminal	48	VMC	Power supply terminal for microcomputer

Notes

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Please contact our sales offices for details ;

U.S.A / San Diego Atlanta Dallas	TEL : +1(858)625-3630 TEL : +1(770)754-5972 TEL : +1(972)312-8818	FAX : +1(858)625-3670 FAX : +1(770)754-0691 FAX : +1(972)312-0330
Germany / Dusseldorf	TEL : +49(2154)9210	FAX : +49(2154)921400
United Kingdom / London	TEL:+44(1)908-282-666	FAX : +44(1)908-282-528
France / Paris	TEL : +33(0)1 56 97 30 60	FAX : +33(0) 1 56 97 30 80
China / Hong Kong Shanghai Dilian Beijing	TEL : +852(2)740-6262 TEL : +86(21)6279-2727 TEL : +86(411)8230-8549 TEL : +86(10)8525-2483	FAX : +852(2)375-8971 FAX : +86(21)6247-2066 FAX : +86(411)8230-8537 FAX : +86(10)8525-2489
Taiwan / Taipei	TEL : +866(2)2500-6956	FAX : +866(2)2503-2869
Korea / Seoul	TEL : +82(2)8182-700	FAX : +82(2)8182-715
Singapore	TEL : +65-6332-2322	FAX : +65-6332-5662
Malaysia / Kuala Lumpur	TEL : +60(3)7958-8355	FAX : +60(3)7958-8377
Philippines / Manila	TEL : +63(2)807-6872	FAX : +63(2)809-1422
Thailand / Bangkok	TEL : +66(2)254-4890	FAX : +66(2)256-6334

Tokyo	2-1-1, Yaesu, Chuo-ku, Tokyo 104-0082 TEL : +81(3)5203-0321 FAX : +81(3)5203-0300
Yokohama	2-4-8, Shin Yokohama, Kohoku-ku, Yokohama, Kanagawa 222-8575 TEL : +81(45)476-2131 FAX : +81(45)476-2128
Nagoya	Dainagayo Building 9F 3-28-12, Meieki, Nakamura-ku, Nagoya,Aichi 450-0002 TEL : +81(52)581-8521 FAX : +81(52)561-2173
Kyoto	579-32 Higashi Shiokouji-cho, Karasuma Nishi-iru, Shiokoujidori, Shimogyo-ku Kyoto 600-8216 TEL : +81(75)311-2121 FAX : +81(75)314-6559
Contact addr	ess for overseas customers in Japan)
Yokohama	TEL : +81(45)476-9270 FAX : +81(045)476-9271

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