

查询TLP251供应商

**SEMICONDUCTOR**  
**TOSHIBA**  
TECHNICAL DATA

(TLP251)

INVERTER FOR AIR CONDITIONER  
INDUCTION HEATING  
TRANSISTOR INVERTER  
POWER MOS FET GATE DRIVE  
IGBT GATE DRIVE

The Toshiba TLP251 consists of a GaA $\ell$ As light emitting diode and a integrated photodetector.

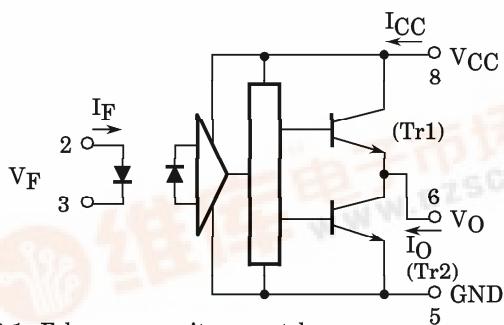
This unit is 8-lead DIP package.

TLP251 is suitable for gate driving circuit of IGBT or power MOS FET. Especially TLP251 is capable of "direct" gate drive of lower power IGBTs. (~15A)

\* Target Specifications \*

- Input Threshold Current :  $I_F = 5\text{mA}$  (Max.)
- Supply Current ( $I_{CC}$ ) :  $11\text{mA}$  (Max.)
- Supply Voltage ( $V_{CC}$ ) :  $10\text{--}35\text{V}$
- Output Current ( $I_O$ ) :  $\pm 0.1\text{A}$  (Min.)
- Switching Time ( $t_{pLH}/t_{pHL}$ ) :  $1\mu\text{s}$  (Max.)
- Isolation Voltage :  $2500\text{VRms}$  (Min.)

SCHMATIC



A  $0.1\mu\text{F}$  bypass capacitor must be connected between pin 8 and 5 (See note 5).

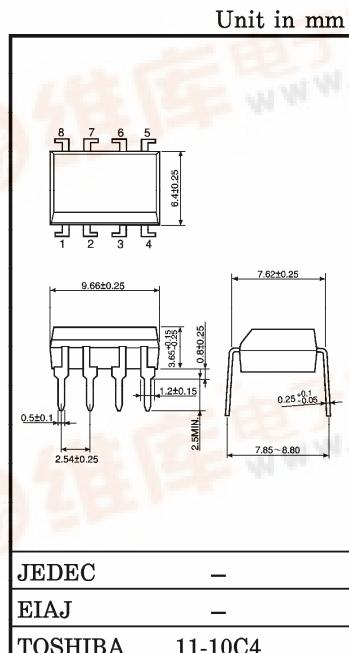
TRUTH TABLE

		Tr1	Tr2
Input	ON	ON	OFF
LED	OFF	OFF	ON

捷多邦，专业PCB打样工厂，24小时加急出货  
TOSHIBA PHOTOCOUPLER

**TLP251**

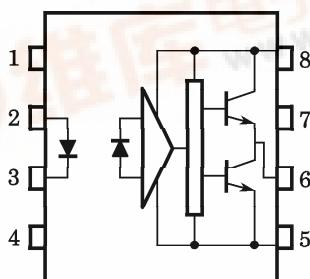
GaA $\ell$ As IRED & PHOTO-IC



JEDEC —  
EIAJ —  
TOSHIBA 11-10C4

Weight : 0.54g

PIN CONFIGURATION (TOP VIEW)



- 1 : N.C.
- 2 : ANODE
- 3 : CATHODE
- 4 : N.C.
- 5 : GND
- 6 :  $V_O$  (OUTPUT)
- 7 : N.C.
- 8 :  $V_{CC}$

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ABSOLUTE MAXIMUM RATINGS ( $T_a = 25^\circ\text{C}$ )

CHARACTERISTIC		SYMBOL	RATING	UNIT
LED	Forward Current	$I_F$	20	mA
	Forward Current Derating ( $T_a \geq 70^\circ\text{C}$ )	$\Delta I_F / \Delta T_a$	-0.36	mA / $^\circ\text{C}$
	Peak Transient Forward Current (Note 1)	$I_{FPPT}$	1	A
	Reverse Voltage	$V_R$	5	V
	Junction Temperature	$(T_j)$	125	$^\circ\text{C}$
DETECTOR	"H" Peak Output Current ( $P_W \leq 2.0\ \mu\text{s}$ , $f \leq 15\text{kHz}$ ) (Note 2)	$I_{OPH}$	-0.4	A
	"L" Peak Output Current ( $P_W \leq 2.0\ \mu\text{s}$ , $f \leq 15\text{kHz}$ ) (Note 2)	$I_{OPL}$	+0.4	A
	Output Voltage	$V_O$	35	V
	( $T_a \leq 70^\circ\text{C}$ )		24	
	Supply Voltage	$V_{CC}$	35	V
	( $T_a \leq 70^\circ\text{C}$ )		24	
	Output Voltage Derating ( $T_a \geq 70^\circ\text{C}$ )	$\Delta V_O / \Delta T_a$	-0.73	V / $^\circ\text{C}$
	Supply Voltage Derating ( $T_a \geq 70^\circ\text{C}$ )	$\Delta V_{CC} / \Delta T_a$	-0.73	V / $^\circ\text{C}$
	Junction Temperature	$(T_j)$	125	$^\circ\text{C}$
Operating Frequency (Note 3)		$f$	25	kHz
Operating Temperature Range		$T_{opr}$	-20~85	$^\circ\text{C}$
Storage Temperature Range		$T_{stg}$	-55~125	$^\circ\text{C}$
Lead Solder Temperature (10s)		$T_{sol}$	260	$^\circ\text{C}$
Isolation Voltage (AC, 1min., R.H. $\leq 60\%$ , $T_a = 25^\circ\text{C}$ ) (Note 4)		BVS	2500	Vrms

Note 1 : Pulse width  $P_W \leq 1\ \mu\text{s}$ , 300pps

Note 2 : Exponential Waveform

Note 3 : Exponential Waveform,  $I_{OPH} \leq -0.25\text{A}$  ( $\leq 2.0\ \mu\text{s}$ ),  $I_{OPL} \leq +0.25\text{A}$  ( $\leq 2.0\ \mu\text{s}$ )

Note 4 : Device considered a two terminal device : pins 1,2,3 and 4 shorted together, and pins 5, 6, 7 and 8 shorted together.

Note 5 : A ceramic capacitor ( $0.1\ \mu\text{F}$ ) should be connected from pin 8 to pin 5 to stabilize the operation of the high gain linear amplifier. Failure to provide the bypassing may impair the switching property. The total lead length between capacitor and coupler should not exceed 1cm.

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT
Input Current, ON	$I_F(\text{ON})$	7	8	10	mA
Input Voltage, OFF	$V_F(\text{OFF})$	0	—	0.8	V
Supply Voltage	$V_{CC}$	10	—	30	V
Peak Output Current	$I_{OPH}/I_{OPL}$	—	—	$\pm 0.1$	A
Operating Temperature	$T_{opr}$	-20	25	70	$^\circ\text{C}$

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ELECTRICAL CHARACTERISTICS ( $T_a = -20\sim70^\circ C$ , Unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION		MIN.	TYP.*	MAX.	UNIT
Input Forward Voltage	$V_F$	—	$I_F=10mA, T_a=25^\circ C$		—	1.6	1.8	V
Temperature Coefficient of Forward Voltage	$\Delta V_F / \Delta T_a$	—	$I_F=10mA$		—	-2.0	—	mV / °C
Input Reverse Current	$I_R$	—	$V_R=5V, T_a=25^\circ C$		—	—	10	$\mu A$
Input Capacitance	$C_T$	—	$V=0, f=1MHz, T_a=25^\circ C$		—	45	250	pF
Output Current	“H” Level	$I_{OPH}$	3	$I_F=10mA$ $V_{CC}=30V$ (*)1	-0.1	-0.25	—	A
	“L” Level	$I_{OPL}$	2	$I_F=0$ $V_{6-5}=2.5V$	0.1	0.2	—	
Output Voltage	“H” Level	$V_{OH}$	4	$V_{CC1}=+15V, V_{EE1}=-15V$ $R_L=200\Omega, I_F=5mA$	11	13.2	—	V
	“L” Level	$V_{OL}$	5	$V_{CC1}=+15V, V_{EE1}=-15V$ $R_L=200\Omega, V_F=0.8V$	—	-14.5	-12.5	
Supply Current	“H” Level	$I_{CCH}$	—	$V_{CC}=30V, I_F=10mA$ $T_a=25^\circ C$	—	7.5	—	mA
	“L” Level		—	$V_{CC}=30V, I_F=10mA$	—	—	11	
	“H” Level	$I_{CCL}$	—	$V_{CC}=30V, I_F=0mA$ $T_a=25^\circ C$	—	8	—	
	“L” Level		—	$V_{CC}=30V, I_F=0mA$	—	—	11	
Threshold Input Current	“Output L→H”	$I_{FLH}$	—	$V_{CC1}=+15V, V_{EE1}=-15V$ $R_L=200\Omega, V_O>0V$	—	1.2	5	mA
Threshold Input Voltage	“Output H→L”	$V_{FHL}$	—	$V_{CC1}=+15V, V_{EE1}=-15V$ $R_L=200\Omega, V_O<0V$	0.8	—	—	V
Supply Voltage	$V_{CC}$	—	—	—	10	—	35	V
Capacitance (Input-Output)	$C_s$	—	$V_s=0, f=1MHz$ $T_a=25^\circ C$	—	1.0	2.0	—	pF
Resistance (Input-Output)	$R_s$	—	$V_s=500V, T_a=25^\circ C$ R.H.≤60%	$5\times10^{10}$	$10^{12}$	—	—	Ω

\* All typical values are at  $T_a=25^\circ C$       (\*)1 : Duration of  $I_O$  time  $\leq 50\mu s$

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SWITCHING CHARACTERISTICS ( $T_a = -20\sim70^\circ C$ , Unless otherwise specified)

CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.*	MAX.	UNIT
Propagation Delay Time	$t_{pLH}$	6	$I_F = 8mA$	—	0.25	1.0	$\mu s$
	$t_{pHL}$		$V_{CC1} = +15V, V_{EE1} = -15V$	—	0.25	1.0	
Output Rise Time	$t_r$		$R_L = 200\Omega$	—	—	—	
Output Fall Time	$t_f$			—	—	—	
Common Mode Transient Immunity at High Level Output	$C_{MH}$	7	$V_{CM} = 600V, I_F = 8mA$ $V_{CC} = 30V, Ta = 25^\circ C$	-5000	—	—	$V/\mu s$
Common Mode Transient Immunity at Low Level Output	$C_{ML}$	7	$V_{CM} = 600V, I_F = 0mA$ $V_{CC} = 30V, Ta = 25^\circ C$	5000	—	—	$V/\mu s$

\* All typical values are at  $T_a = 25^\circ C$

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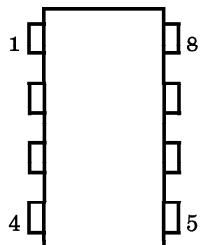
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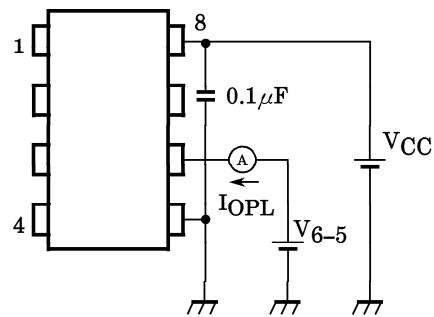


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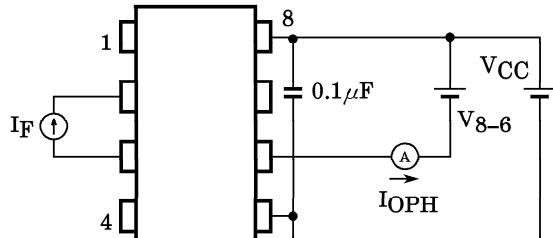
TEST CIRCUIT 1 :



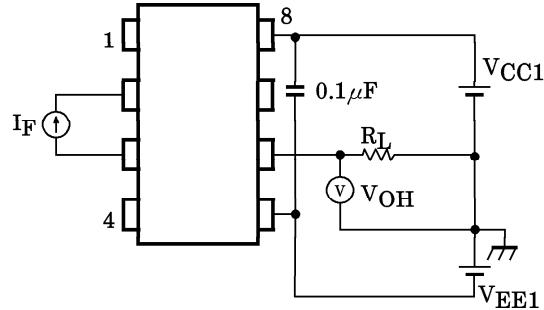
TEST CIRCUIT 2 :  $I_{OPL}$



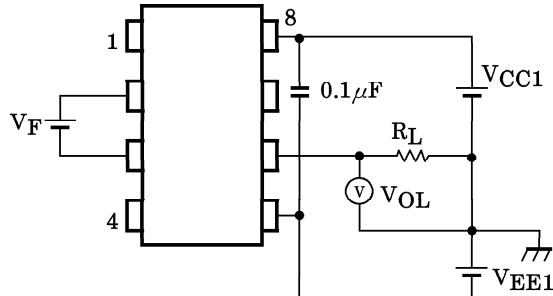
TEST CIRCUIT 3 :  $I_{OPH}$



TEST CIRCUIT 4 :  $V_{OH}$



TEST CIRCUIT 5 :  $V_{OL}$



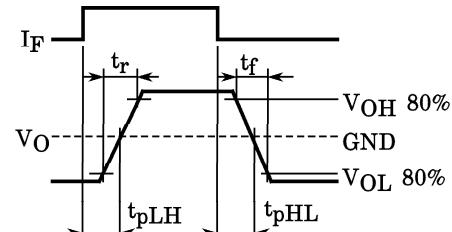
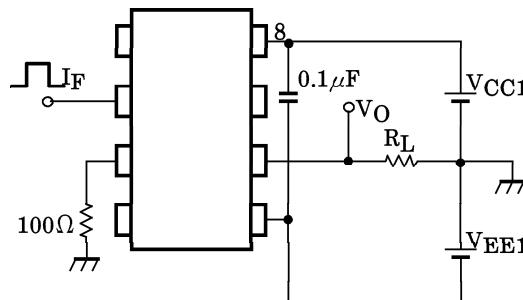
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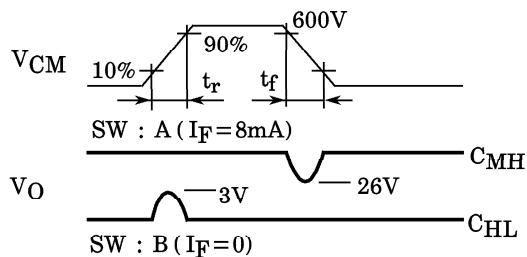
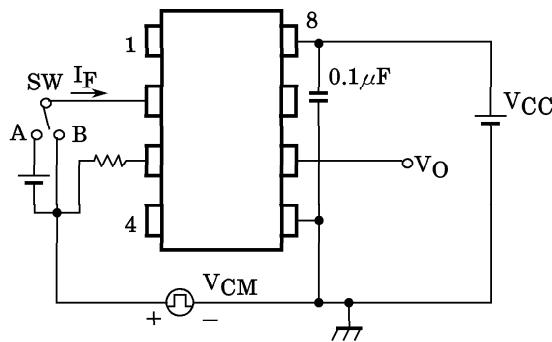
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TEST CIRCUIT 6 :  $t_{PLH}$ ,  $t_{PHL}$ ,  $t_r$ ,  $t_f$



TEST CIRCUIT 7 :  $C_{ML}$ ,  $C_{MH}$



$$C_{ML} = \frac{480(V)}{t_r(\mu s)}$$

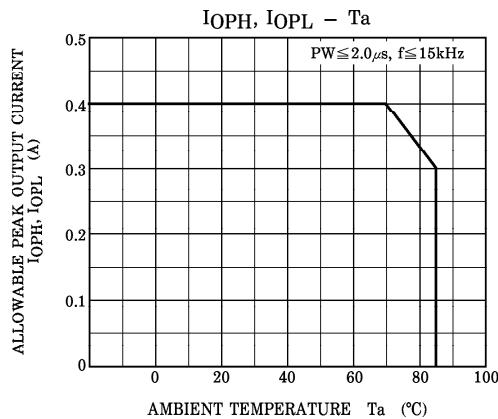
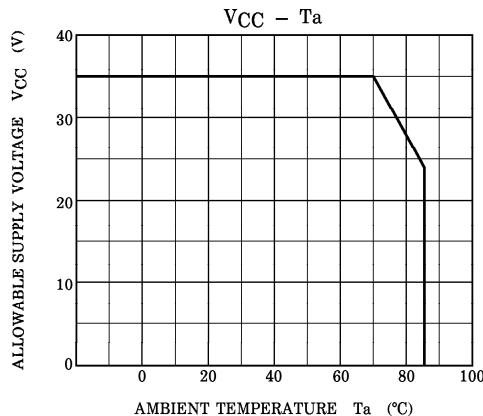
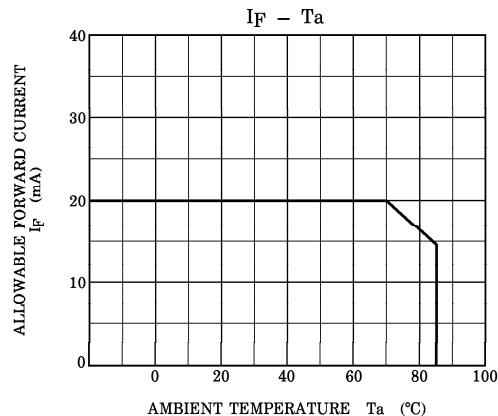
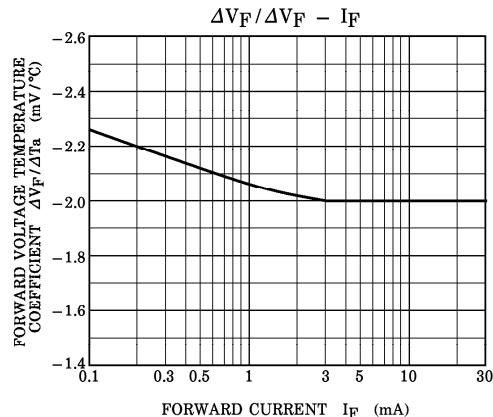
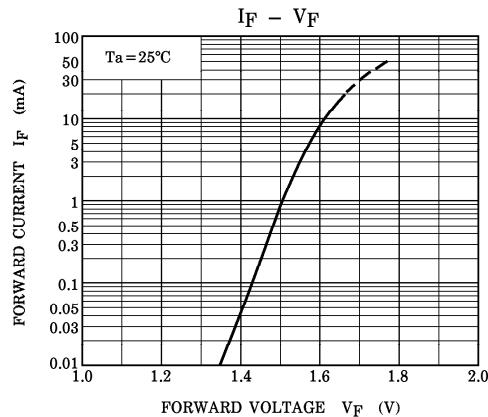
$$C_{MH} = \frac{480(V)}{t_f(\mu s)}$$

$C_{ML}$  ( $C_{MH}$ ) is the maximum rate of rise (fall) of the common mode voltage that can be sustained with the output voltage in the low (high) state.

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