# **Lucent Technologies**Bell Labs Innovations



# DW025 Dual Output-Series Power Modules: dc-dc Converters; 36 Vdc to 75 Vdc Input; Dual Outputs; 25 W



The DW025-Series Power Modules use advanced, surfacemount technology and deliver high-quality, compact, dc-dc conversion at an economical price.

#### **Features**

- Small size: 71.1 mm x 61.0 mm x 12.7 mm (2.80 in. x 2.40 in. x 0.50 in.)
- Low output noise
- Industry-standard pinout
- Metal case with separate case ground pin
- 2:1 input voltage range
- Remote on/off (positive logic)
- UL\* Recognized, CSA<sup>†</sup> Certified, and VDE Licensed
- Within FCC and CISPR Class A Radiated Limits
- CE mark meets 73/23/EEC and 93/68/EEC directives<sup>‡</sup>
- Two tightly regulated outputs

#### **Applications**

- Distributed power architectures
- Telecommunications

#### **Options**

- Higher-accuracy output voltage clamp set point
- Short pins: 2.79 mm ± 0.25 mm (0.110 in. ± 0.010 in.)
- Heat sink available for extended operation
- Negative logic remote on/off

#### Description

The DW025-Series Power Modules are a family of dc-dc converters that operate over an input voltage range of 36 Vdc to 75 Vdc and provide two regulated outputs. These modules offer low noise levels with industry-standard pinouts in a small footprint. Each highly reliable and efficient unit features remote on/off and current limit.

Each output is individually regulated by its own control circuit and has an independent overvoltage clamp. With standard outputs of +3.3 V,  $\pm 5 \text{ V}$ ,  $\pm 12 \text{ V}$ , and  $\pm 15 \text{ V}$ , the DW025-Series is flexible enough to provide modified standard units with any combination of output voltages from 2 V to 15 V.

Efficiency greater than 80%, a wide operating temperature range, and a metal case are additional features of these modules. They have *UL*, *CSA*, and VDE recognition and operate within FCC and CISPR Class A limits for radiated emissions.

The DW025AJ-M, BK-M, and CL-M Power Modules are available with common output ground pins (pinout A); the DW025AA-M, AB-M, AF-M, BB-M, and CC-M Power Modules are available with separate output ground pins (pinout B).

- \* UL is a registered trademark of Underwriters Laboratories, Inc.
- † CSA is a registered trademark of the Canadian Standards Association.
- ‡ This product is intended for integration into end-use equipment. All the required procedures for CE marking of end-use equipment should be followed. (The CE mark is placed on selected products.)



#### **Absolute Maximum Ratings**

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. These are absolute stress ratings only. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operations sections of the data sheet. Exposure to absolute maximum ratings for extended periods can adversely affect device reliability.

Parameter	Symbol	Min	Max	Unit
Input Voltage				
Continuous	Vı		80	V
Transient (<10 ms)	VI, trans		100	V
I/O Isolation Voltage				
dc	. 1 — 1	—	500	V
Transient (1 minute)	·   -		850	V
Operating Case Temperature	Tc	-40	100	°C
Storage Temperature	Tstg	<b>–</b> 55	125	°C

#### **Electrical Specifications**

Note: Two pinouts are available for the DW025-Series Power Modules. The DW025AJ-M, AB-M, BK-M, and CL-M Power Modules, which have both positive and negative outputs, have a common output ground pin (pinout A). The DW025AA-M, AB-M, AF-M, BB-M, and CC-M Power Modules, which have dual positive outputs, have separate output ground pins (pinout B). See Outline Diagrams section.

Unless otherwise indicated, specifications apply to all modules over all operating input voltage, resistive load, and temperature conditions.

**Table 1. Input Specifications** 

Parameter	Symbol	Min	Тур	Max	Unit
Operating Input Voltage	Vı	36	48	75	Vdc
Maximum Input Current (VI = 0 V to 75 V; Io = Io, max; see Figure 1.)	lt, max		_	2.0	Α
Inrush Transient	i <sup>2</sup> t		<del>-</del>	0.8	A <sup>2</sup> s
Input Reflected-ripple Current, Peak-to-Peak (5 Hz to 20 MHz, 12 μH source impedance; Tc = 25 °C) (See Figure 11 and Design Considerations section.)	<del></del>		25	_	mAp-p
Input Ripple Rejection (120 Hz)		_	60	_	dB

#### **Fusing Considerations**

CAUTION: This power module is not internally fused. An input line fuse must always be used.

This encapsulated power module can be used in a wide variety of applications, ranging from simple stand-alone operation to an integrated part of a sophisticated power architecture. To preserve maximum flexibility, internal fusing is not included; however, to achieve maximum safety and system protection, always use an input line fuse. The safety agencies require a normal-blow, dc fuse with a maximum rating of 5 A (see Safety Considerations section). Based on the information provided in this data sheet on inrush energy and maximum dc input current, the same type of fuse with a lower rating can be used. Refer to the fuse manufacturer's data for further information.



#### **Electrical Specifications** (continued)

**Table 2. Output Specifications** 

Parameter	Device	Symbol	Min	Тур	Max	Unit
Output Voltage	DW025AJ-M	V01	4.80	-	5.25	Vdc
(Over all operating input voltage,		V02	-4.80	_	-5.25	Vdc
resistive load, and temperature	DW025BK-M	Vo1	11.40		12.60	Vdc
conditions until end of life. See		<b>V</b> O2	-11.40	—	-12.60	Vdc
Figure 13.)	DW025CL-M	Vo1	14.25	_	15.75	Vdc
		Vo <sub>2</sub>	-14.25		-15.75	Vdc
	DW025AA-M	Vo <sub>1</sub>	4.80	_	5.25	Vdc
		<b>V</b> 02	4.80	<del></del>	5.25	Vdc
	DW025AB-M	Vo <sub>1</sub>	4.80		5.25	Vdc
		V02	11.40	<del></del>	12.60	Vdc
	DW025AF-M	Vo <sub>1</sub>	4.80	<del></del>	5.25	Vdc
	D\4400=DD 44	V02	3.16		3.45	Vdc
	DW025BB-M	Vo <sub>1</sub>	11.40	_	12.60	Vdc
	D11/00=00 14	V02	11.40	_	12.60	Vdc
	DW025CC-M	Vo1	14.25	<del></del>	15.75	Vdc
		V02	14.25		15.75	Vdc
Output Voltage Set Point	DW025AJ-M	VO1, set	4.90	5.0	5.10	Vdc
$(V_1 = 48 \text{ V}; I_0 = I_0, max; T_A = 25 ^{\circ}\text{C})$		VO2, set	-4.90	-5.0	-5.10	Vdc
	DW025BK-M	VO1, set	11.76	12.0	12.24	Vdc
		VO2, set	-11.76	-12.0	-12.24	Vdc
	DW025CL-M	VO1, set	14.70	15.0	15.30	Vdc
		VO2, set	-14.70	-15.0	-15.30	Vdc
	DW025AA-M	VO1, set	4.90	5.0	5.10	Vdc
		VO2, set	4.90	5.0	5.10	Vdc
	DW025AB-M	VO1, set	4.90	5.0	5.10	Vdc
		VO2, set	11.76	12.0	12.24	Vdc
	DW025AF-M	VO1, set	4.90	5.0	5.10	Vdc
		VO2, set	3.23	3.3	3.37	Vdc
	DW025BB-M	VO1, set	11.76	12.0	12.24	Vdc
		VO2, set	11.76	12.0	12.24	Vdc
	DW025CC-M	VO1, set	14.70	15.0	15.30	Vdc
		VO2, set	14.70	15.0	15.30	Vdc
Output Regulation:			-			
Line $(V_1 = 36 \text{ V to } 75 \text{ V})$	All		_	0.1	0.2	%
Load ( $101 = 10$ , min to $10$ , max, $102 = 10$ , max)	All			0.1	0.4	%
Load (102 = 10, min to 10, max, 101 = 10, max)	DW025AJ-M,			0.1	0.4	%
(	AA-M, BK-M,		_	0.1	0.4	70
	BB-M, CL-M,					
	CC-M, AB-M					
_	DW025AF-M	_	<del></del>	0.4	0.8	%
Temperature	DW025AJ-M,		_	15	70	mV
$(Tc = -40  ^{\circ}C  to +100  ^{\circ}C)$	AA-M, AF-M					
	DW025BK-M.			40	150	mV
	BB-M			40	150	HIV
	DW025CL-M,					
				40	190	m۷
	СС-М					
	DW025AB-M			15	70	mV
	(5 V output)			_		••
	1 ' '					
	DW025AB-M	_		40	150	mV

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#### **Electrical Specifications** (continued)

Table 2. Output Specifications (continued)

Parameter	Device	Symbol	Min	Тур	Max	Unit
Output Ripple and Noise		·				
(See Figure 12.): RMS	DW025AJ-M, AA-M, AF-M		_	_	15	mVrms
	DW025BK-M, BB-M	_			20	mVrms
	DW025CL-M, CC-M		_	_	25	mVrms
	DW025AB-M (5 V output)	_	<u> </u>		15	mVrms
	DW025AB-M (12 V output)	_	_		20	mVrms
Peak-to-peak (5 Hz to 20 MHz)	DW025AJ-M, AA-M, AF-M		_		150	mVp-p
	DW025BK-M, BB-M				200	mVp-p
	DW025CL-M, CC-M	<b>-</b>		_	250	mVp-p
	DW025AB-M (5 V output)	_	_	<del>-</del>	150	mVp-p
	DW025AB-M (12 V output)	<u>—</u>	_	_	200	mVp-p
Output Current	DW025AJ-M,	lo <sub>1</sub>	0.20		2.50	Α
(At lo < lo, min, the modules may exceed output ripple specifications.)	AA-M, AF-M	lo2	0.20	<b>—</b>	2.50	A
output ripple specifications.)	DW025BK-M, BB-M	101 102	0.10 0.10	_	1.04 1.04	A
	DW025CL-M, CC-M	lo <sub>1</sub>	0.08		0.83	A
		lo <sub>2</sub>	0.08	—	0.83	A
	DW025AB-M	lo1 lo2	0.20 0.10	_ _	2.50 1.04	A A
Output Current-limit Inception (Vo = 90% of Vo, nom; see Figure 2.)	DW025AJ-M, AA-M, AF-M	_	_	3.7	6.5	Α
	DW025BK-M, BB-M	_	_	1.5	2.9	Α
	DW025CL-M, CC-M	_	_	1.3	2.7	Α
	DW025AB-M	lo <sub>1</sub>	_	3.7	6.5	Α
Outside Objects along it Outside Objects	DWOODA LAA	lo <sub>2</sub>		1.5	2.9	A
Output Short-circuit Current (Vo = 250 mV)	DW025AJ-M, AA-M, AF-M	<u> </u>		3.5	7.0	A
	DW025BK-M, BB-M	_		1.0	3.0	Α
	DW025CL-M, CC-M	_		1.0	3.0	A
	DW025AB-M	lo1 lo2		3.5 1.0	7.0 3.0	A A
Efficiency (VI = 48 V; Io = Io, max;	DW025AJ-M, AA-M	η	75	78	5.0	%
Tc = 25 °C; see Figures 3 and 13.)	DW025AF-M	''   η	68	75 75		%
	DW025AB-M	η	75	79	_	%
	DW025BK-M, BB-M	η	79	82	_	%
	DW025CL-M, CC-M	η	79	82		%

#### **Electrical Specifications** (continued)

Table 2. Output Specifications (continued)

Parameter	Device	Symbol	Min	Тур	Max	Unit
Dynamic Response ( $\Delta$ lo/ $\Delta$ t = 1 A/10 μs, V <sub>I</sub> = 48 V, T <sub>A</sub> = 25 °C):						
Load Change from Io = 50% to 75% of Io, max: (See Figures 7 to 9.):						
Peak Deviation	DW025AJ-M, AA-M, AF-M		_	160	_	mV
	DW025BK-M, BB-M	_	_	250	_	mV
·	DW025CL-M, CC-M		_	250		mV
	DW025AB-M (5 V output)	_	_	160	<u></u>	mV
	DW025AB-M (12 V output)	_	_	250	_	mV
Settling Time (Vo < 10% peak deviation)	All	_		3	_	ms
Load Change from lo = 50% to 25% of lo, max (See Figures 4 to 6.):						
Peak Deviation	DW025AJ-M, AA-M, AF-M	_	_	160	_	mV
	DW025BK-M, BB-M		_	250	_	mV
	DW025CL-M, CC-M		_	250		mV
	DW025AB-M (5 V output)	_		160	_	mV
	DW025AB-M (12 V output)		_	250	_	mV
Settling Time (Vo < 10% peak deviation)	All	_	_	3		ms

#### **Table 3. Isolation Specifications**

Parameter	Min	Тур	Max	Unit
Isolation Capacitance		1200		pF
Isolation Resistance	10	_	_	MΩ

#### **General Specifications**

Parameter	Min	Тур	Max	Unit
Calculated MTBF (Io = 80% of Io, max; Tc = 40 °C)	2,800,000			hours
Weight			113 (4.0)	g (oz.)

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#### **Feature Specifications**

Unless otherwise indicated, specifications apply over all operating input voltage, resistive load, and temperature conditions. See Feature Descriptions and Design Considerations for further information.

Parameter	Device	Symbol	Min	Тур	Max	Unit
Remote On/Off (positive logic)					Ì	
$(V_I = 0 V \text{ to } 75 V; \text{ open collector or})$						
equivalent compatible; signal						
referenced to V <sub>I</sub> (–) terminal. See						
Figures 10, 14 and Feature						
Descriptions.):						
DW025xx-M Positive Logic:						
Logic Low—Module Off						
Logic High—Module On						
DW025xx1-M Negative Logic:						
Logic Low—Module On						
Logic High—Module Off						
Module Specifications:						
On/Off Current—Logic Low	All	lon/off	-	-	1.0	mA
On/Off Voltage:						
Logic Low	All	Von/off	0		1.2	V
Logic High (Ion/off = 0)	All	Von/off		-	10	V
Open Collector Switch Specifications:						_
Leakage Current During Logic High	All	lan/off		-	50	μΑ
(Von/off = 10 V)	l			ľ		
Output Low Voltage During Logic	All	Von/off		-	1.2	V
Low (lon/off = 1 mA)						
Turn-on Time (lo = 80% of lo, max; Vo	All	_	_	30		ms
within ±1% of steady state)			]		_	6/
Output Voltage Overshoot	All	_	_	0	5	%
Output Overvoltage Clamp	DW025AJ-M	Vo <sub>1</sub>	<u> </u>	_	7	V
		<b>V</b> O2	<del>-</del>	_	<b>-</b> 7	V
	DW025BK-M	<b>V</b> O1	<u> </u>	_	16	V
		Vo <sub>2</sub>	<del> </del>	_	-16	V
	DW025CL-M	<b>V</b> 01		l —	20	V
		<b>V</b> 02	<del></del>	_	-20	V
	DW025AA-M	Vo <sub>1</sub>	_	-	7	V
		V02	_	-	7	V
	DW025AB-M	Vo <sub>1</sub>	_	-	7	V
	DIA 2054 5 3 4	<b>V</b> 02	_	_	16	V
	DW025AF-M	Vo <sub>1</sub>	_	_	7	V
	DIMOSEDE 1:	V02	_		5	٧
	DW025BB-M	Vo <sub>1</sub>	_		16	V
	DIMONECO	V02	_		16	٧
	DW025CC-M	Vo <sub>1</sub>	_	-	20	V
		Vo <sub>2</sub>			20	V
Input Undervoltage Lockout:						
Module On	All	Vuvlo		28	36	V
Module Off	All	<b>V</b> uvlo	20	28		٧.

#### **Characteristic Curves**

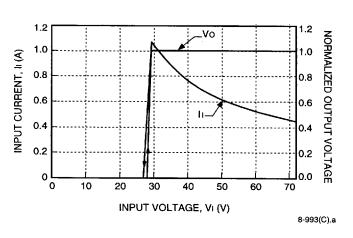


Figure 1. DW025-Series Input Current and Normalized Output Voltage vs. Input Voltage at Full Load and TA = 25 °C

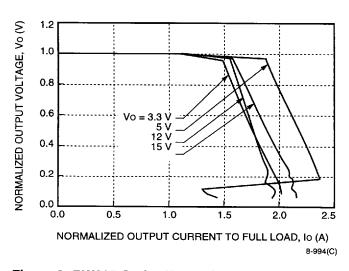


Figure 2. DW025-Series Normalized Output Current vs. Normalized Output Voltage at VI = 48 V and TA = 25 °C

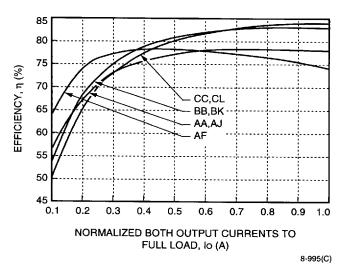


Figure 3. DW025-Series Efficiency vs. Normalized Both Output Currents to Full Load at VI = 48 V and TA = 25 °C

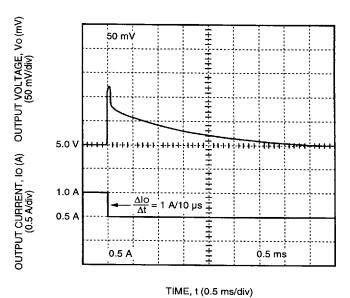


Figure 4. DW025-Series Typical 5 V Output Voltage Response to a Step Load Change from 50% to 25% of Io, max at VI = VI, nom and TA = 25 °C

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#### Characteristic Curves (continued)

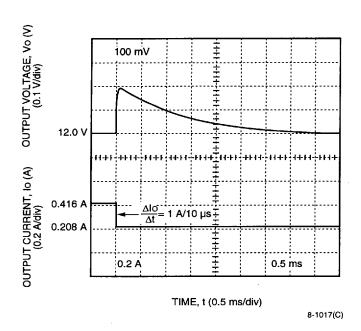


Figure 5. Typical DW025-Series 12 V Output Voltage Response to a Step Load Change from 50% to 25% of Io, max at VI = VI, nom and TA = 25 °C

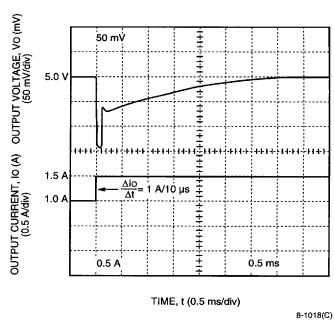


Figure 7. Typical DW025-Series 5 V Output Voltage Response to a Step Load Change from 50% to 75% of Io, max at VI = VI, nom and TA = 25 °C

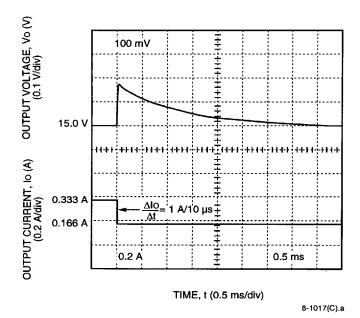


Figure 6. Typical DW025-Series 15 V Output Voltage Response to a Step Load Change from 50% to 25% of lo, max at VI = VI, nom and TA = 25 °C

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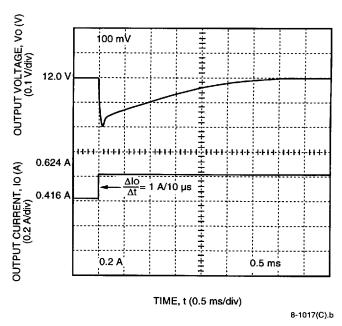


Figure 8. Typical DW025-Series 12 V Output Voltage Response to a Step Load Change from 50% to 75% of Io, max at VI = VI, nom and TA = 25 °C

#### Characteristic Curves (continued)

# 0.1 V/div) 0.2 A/div) 0.333 A 0.2 A/div) 0.333 A 0.2 A/div) 0.3 A/div) 0.3 A/div) 0.5 ms

TIME, t (0.5 ms/div)

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Figure 9. Typical DW025-Series 15 V Output Voltage Response to a Step Load Change from 50% to 75% of Io, max at VI = VI, nom and TA = 25 °C

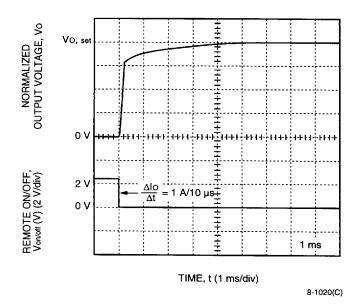
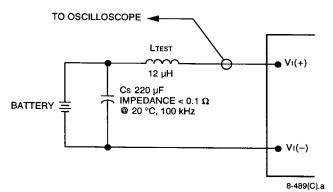


Figure 10. Typical DW025-Series Output Voltage Rise Time with Remote On/Off at VI = VI, nom, IO = 0.8 (IO, max) and TA = 25 °C

#### **Test Configurations**

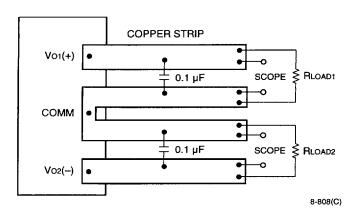


Note: Measure input reflected-ripple current with a simulated source impedance (LTEST) of 12 µH. Capacitor Cs offsets possible battery impedance. Current is measured at the input of the module.

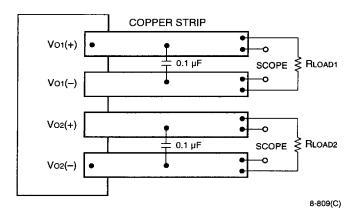
Figure 11. Input Reflected-Ripple Test Setup



#### Test Configurations (continued)



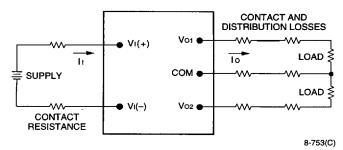
#### A. Pinout A (DW025AJ-M, BK-M, CL-M) Configuration



## B. Pinout B (DW025AA-M, AB-M, AF-M, BB-M, CC-M) Configuration

Note: Use a 0.1 µF ceramic capacitor. Scope measurement should be made by using a BNC socket. Position the load between 50 mm (2 in.) and 75 mm (3 in.) from the module.

Figure 12. Output Noise Measurement Test Setup



Note: All measurements are taken at the module terminals. When socketing, place Kelvin connections at module terminals to avoid measurement errors due to socket contact resistance. In addition, Vi(–) is internally connected to case.

Pinout A 
$$\eta = \frac{\sum_{J=1}^{2} |[Vo_{J} - COM]|o_{J}|}{[V_{I}(+) - V_{I}(-)]I_{I}} \times 100$$

Pinout B 
$$\eta = \frac{\sum_{j=1}^{2} |[Voj(+) - Voj(-)]Ioj|}{[Vi(+) - Vi(-)]I_{1}} \times 100$$

Figure 13. Output Voltage and Efficiency Measurement Test Setup

#### **Design Considerations**

#### Input Source Impedance

The power module should be connected to a low acimpedance input source. Highly inductive source impedances can affect the stability of the power module. A 33  $\mu F$  electrolytic capacitor (ESR < 0.7  $\Omega$  at 100 kHz) mounted close to the power module helps ensure stability of the unit.

#### **Design Considerations** (continued)

#### **Safety Considerations**

For safety-agency approval of the system in which the power module is used, the power module must be installed in compliance with the spacing and separation requirements of the end-use safety agency standard, i.e., *UL*-1950, *CSA* 22.2-950, EN60950.

For the converter output to be considered to be meeting the requirements of safety extra-low voltage (SELV), one of the following must be true:

- All inputs are SELV and floating with the output also floating.
- All inputs are SELV and grounded with the output also grounded.
- Any non-SELV input must be provided with reinforced insulation from any other hazardous voltages, including the ac mains, and must have a SELV reliability test performed on it in combination with the converters.

If the input meets extra-low voltage (ELV) requirements, then the converter's output is considered ELV.

The input to these units is to be provided with a maximum 5 A normal-blow fuse in the ungrounded lead.

#### Input/Output Voltage Reversal

CAUTION: Applying a reverse voltage across the module input or output forward biases an internal diode. Attempting to start the module under this condition can damage the module.

#### **Feature Descriptions**

#### Output Overvoltage Clamp

The output overvoltage clamp consists of control circuitry, independent of the primary regulation loop, that monitors the voltage on the output terminals. The control loop of the clamp has a higher voltage set point than the primary loop (see Feature Specifications table). This provides a redundant voltage control that reduces the risk of output overvoltage.

#### **Current Limit**

To provide protection in a fault (output overload) condition, the unit is equipped with internal current-limiting circuitry and can endure current-limiting for an unlimited duration. At the point of current-limit inception, the unit shifts from voltage control to current control. If the output voltage is pulled very low during a severe fault, the current-limit circuit can exhibit either foldback or tailout characteristics (output current decrease or increase). The unit operates normally once the output current is brought back into its specified range.

#### Remote On/Off

Two remote on/off options are available. Positive logic remote on/off turns the module on during a logic high voltage on the remote on/off pin, and off during a logic low. Negative logic remote on/off, code suffix "1," turns the module off during a logic high and on during a logic low.

To turn the power module on and off, the user must supply a switch to control the voltage between the on/off terminal and the V<sub>I</sub>(–) terminal (V<sub>on/off</sub>). The switch can be an open collector or equivalent (see Figure 14). A logic low is V<sub>on/off</sub> = 0 V to 1.2 V. The maximum lon/off during a logic low is 1 mA. The switch should maintain a logic-low voltage while sinking 1 mA.

During a logic high, the maximum  $V_{\text{on/off}}$  generated by the power module is 10 V. The maximum allowable leakage current of the switch at  $V_{\text{on/off}} = 10 \text{ V}$  is 50  $\mu\text{A}$ .

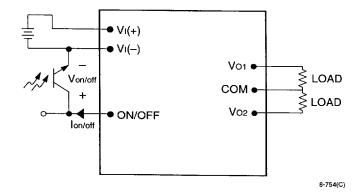
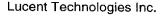
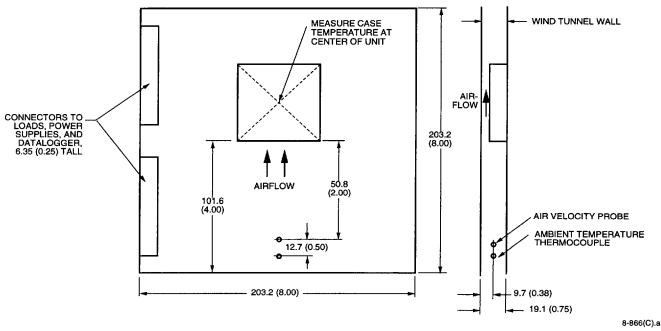


Figure 14. Remote On/Off Implementation



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#### **Thermal Considerations**



Note: Dimensions are in millimeters and (inches). Drawing is not to scale.

Figure 15. Thermal Test Setup

The 25 W dual output power modules are designed to operate in a variety of thermal environments. As with any electronic component, sufficient cooling must be provided to help ensure reliable operation of the unit. Heat-dissipating components inside the module are thermally coupled to the case. Heat is removed by conduction, convection, and radiation to the surrounding environment.

The thermal data presented is based on measurements taken in a wind tunnel. The test setup shown in Figure 15 was used to collect data. Actual performance can vary depending on the particular application environment.

#### Thermal Considerations (continued)

#### **Basic Thermal Performance**

The DW025-Series Dual Output Power Modules have a separate power stage for each of the outputs. This means that the maximum operating temperature can be predicted quite closely by treating each output individually and then summing the results. Figures 16 through 21 are used to predict the safe operating condition for many different operating and environmental conditions.

The method used to determine the maximum ambient temperature at a given air velocity is a four-step process.

- 1. Find the power dissipated for output 1 by using the appropriate chart (Figures 16 through 19) for a particular output condition (lo<sub>1</sub>).
- 2. Repeat step 1 for output 2 using Figures 16 through 19.
- Find the total power dissipated by summing the power dissipated on each of the outputs: (PDOUT1 + PDOUT2) = Pototal
- Use the total power dissipated with Figure 20 to determine the maximum ambient temperature at different air velocities.

For example, the DW025AF-M power module operating at full load on both outputs with 54 V input has a power dissipation of 3.6 W (from Figure 17) plus 3.1 W (from Figure 16) for a total of 6.7 W. Using Figure 20, it can be determined that the maximum ambient temperature at natural convection that the DW025AF-M can operate at is approximately 56 °C.

Keep in mind that these are approximations of the temperatures and airflows required to keep the case temperature below its maximum rating. The maximum case temperature at the point shown in Figure 15 must be kept at 100 °C or less.

#### **Air Velocity**

The air velocity required to maintain a desired maximum case temperature for a given power dissipation and ambient temperature can be calculated using Figure 21 and the following equation:

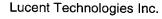
$$\theta_{CA} = \frac{T_{C, max} - T_{A}}{P_{D}total}$$

where  $\theta$ ca is the thermal resistance from case-to-ambient air (°C/W), Tc, max is the desired maximum case temperature (°C), Ta is the ambient inlet temperature (°C), and Pototal is the total power dissipated from the module (W).

For example, to maintain a maximum case temperature of 85 °C with an ambient inlet temperature of 55 °C and a power dissipation of 6.7 W, the thermal resistance is:

$$\theta$$
CA  $\leq \frac{85 \text{ °C} - 55 \text{ °C}}{6.7 \text{ W}} = 4.5 \text{ °C/W}$ 

This corresponds to an air velocity greater than 0.46 ms<sup>-1</sup> (90 fpm) in Figure 21.



#### Thermal Considerations (continued)

#### Air Velocity (continued)

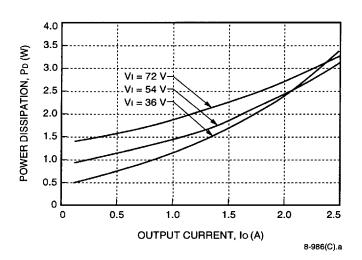


Figure 16. 3.3 V Output Power Dissipation vs.
Output Current

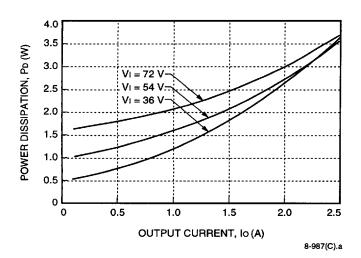


Figure 17. 5 V Output Power Dissipation vs. Output Current

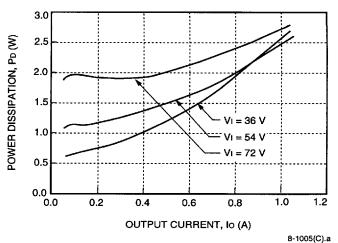


Figure 18. 12 V Output Power Dissipation vs.
Output Current

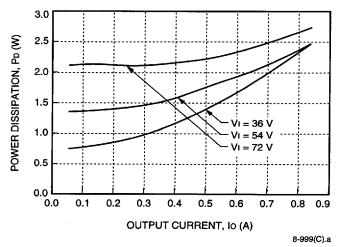


Figure 19. 15 V Output Power Dissipation vs.
Output Current

#### Thermal Considerations (continued)

#### Air Velocity (continued)

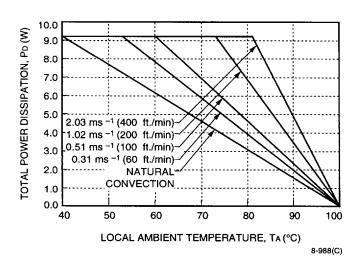


Figure 20. Total Power Dissipation vs. Local
Ambient Temperature and Air Velocity

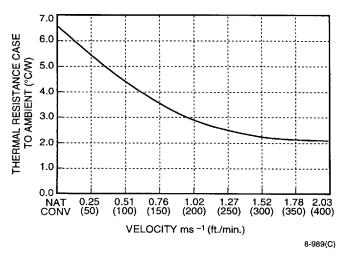
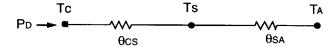


Figure 21. Case-to-Ambient Thermal Resistance vs.
Air Velocity

#### **Use of Heat Sinks and Cold Plates**

The DW025-Series case includes through-threaded M3 x 0.5 mounting holes allowing attachment of heat sinks or cold plates from either side of the module. The mounting torque must not exceed 0.56 N/m (5 in.-lb.).

The following thermal model can be used to determine the required thermal resistance of the sink to provide the necessary cooling:



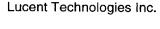
where Pp is the power dissipated by the module,  $\theta$ cs represents the interfacial contact resistance between the module and the sink, and  $\theta$ sA is the sink-to-ambient thermal impedance (°C/W). For thermal grease or foils, a value of  $\theta$ cs = 0.1 °C/W—0.3 °C/W is typical.

The required  $\theta s \mathbf{A}$  is calculated from the following equation:

$$\theta$$
sa = [(Tc - Ta)/PD] -  $\theta$ cs

Note that this equation assumes that all dissipated power must be shed by the heat sink. Depending on the user-defined application environment, a more accurate model including heat transfer from the sides and bottom of the module can be used. This equation provides a conservative estimate in such instances.

For further information, refer to the *Thermal Energy Management CC-, CW-, DC-, and DW-Series 25 W to 30 W Board-Mounted Power Modules* Technical Note.



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#### **Outline Diagrams**

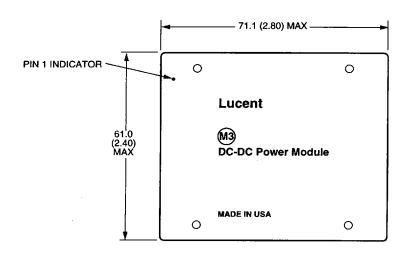
Dimensions are in millimeters and (inches).

Copper paths must not be routed beneath the power module standoffs.

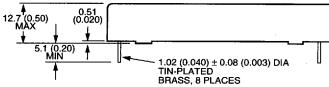
Tolerances:  $x.x \pm 0.5$  mm (0.02 in.),  $x.xx \pm 0.25$  mm (0.010 in.).

#### Pinout A (DW025AJ-M, BK-M, CL-M)

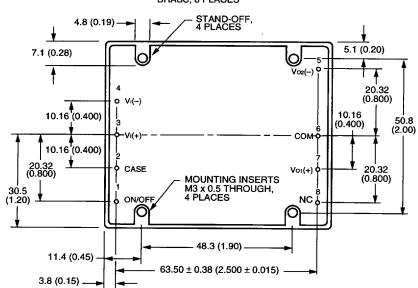
#### **Top View**



**Side View** 



#### **Bottom View**



8-845(C).a

#### Outline Diagrams (continued)

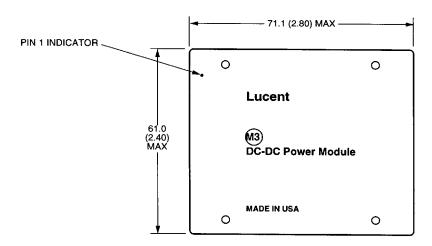
Dimensions are in millimeters and (inches).

Copper paths must not be routed beneath the power module standoffs.

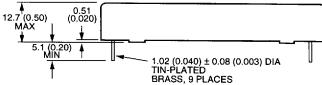
Tolerances:  $x.x \pm 0.5$  mm (0.02 in.),  $x.xx \pm 0.25$  mm (0.010 in.).

### Pinout B (DW025AA-M, AB-M, AF-M, BB-M, CC-M)

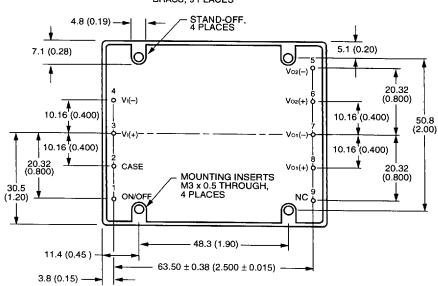
#### **Top View**



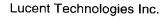
#### Side View



#### **Bottom View**



8-846(C).a



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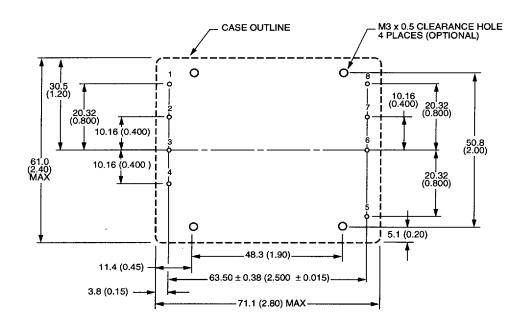
#### **Recommended Hole Patterns**

Component-side footprint.

Dimensions are in millimeters and (inches).

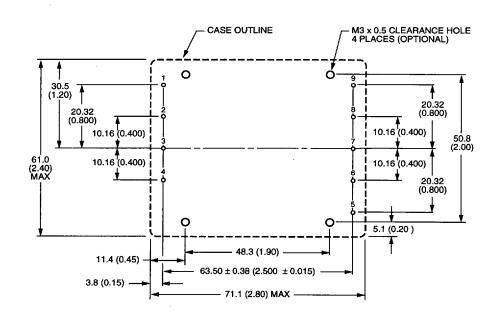
Recommended hole size for pin: 1.27 mm (0.050 in.).

#### Pinout A (DW025AJ-M, BK-M, CL-M)



8-845(C).a

#### Pinout B (DW025AA-M, AB-M, AF-M, BB-M, CC-M)



8-846(C).a

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#### **Ordering Information**

Table 4. Ordering Information Table

Input Voltage	Output Voltage	Output Power	Remote On/Off Logic	Device Code	Pinout	Comcode
36 V to 75 V	5.0 V, ~5.0 V	25 W	positive	DW025AJ-M	Α	107587545
36 V to 75 V	12.0 V, -12.0 V	25 W	positive	DW025BK-M	Α	107587602
36 V to 75 V	15.0 V, -15.0 V	25 W	positive	DW025CL-M	Α	107587628
36 V to 75 V	5.0 V, -5.0 V	25 W	positive	DW025AA-M	В	107731598
36 V to 75 V	5.0 V, 3.3 V	25 W	positive	DW025AF-M	В	107587529
36 V to 75 V	12.0 V, 12.0 V	25 W	positive	DW025BB-M	В	107587586
36 V to 75 V	15.0 V, 15.0 V	25 W	positive	DW025CC-M	В	TBD
36 V to 75 V	5.0 V, 12.0 V	25 W	positive	DW025AB-M	В	107587503

Optional features may be ordered using the device code suffixes shown below. To order more than one option, list suffixes in numerically descending order followed by the -M suffix, indicating metric (M3 x 0.5 heat sink hardware). The heat sinks designed for this package have an M prefix, i.e., MHSTxxx45 and MHSLxxx45 (see *Thermal Energy Management CC-, CW-, DC-, and DW-Series 25 W to 30 W Board-Mounted Power Modules* Technical Note).

**Table 5. Options Table** 

Option	Device Code Suffix
Short pins: 2.79 mm ± 0.25 mm (0.110 in. ± 0.010 in.)	8
Negative Remote on/off logic	1

Please contact your Lucent Technologies Microelectronics Group Account Manager or Application Engineer for pricing and availability of options.

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