TYPES SN75497, SN75498 MOS-TO-LED 7- OR 9-CHANNEL DRIVERS

BULLETIN NO. DL-S 7712490, MAY 1977

- 100-mA Output Sink Current Capability
- Low-Voltage Operation
- MOS and TTL-Compatible Inputs
- Input Threshold . . . 2.7 V Max
- 7 Drivers (SN75497) or 9 Drivers

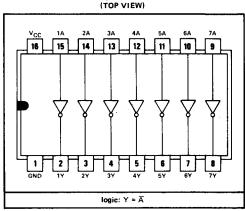
(SN75498) per Package

- Low-Voltage Saturating Outputs
- Low Standby Power

description

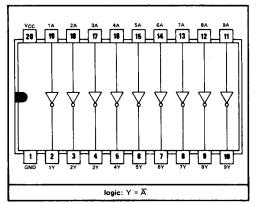
The SN75497 and SN75498 are designed to drive common-cathode LED's in serially addressed multi-digit displays used in conjunction with MOS calculator circuits. The input of each circuit is capable of interfacing with an MOS push-pull output buffer while the output is capable of sinking the output current from a strobed LED display. These drivers are also essentially compatible with TTL inputs. They have a guaranteed threshold of 2.7 volts maximum, making them ideal for two-battery calculators or other low-voltage battery systems. They are designed to be used with active-pull-down MOS devices, but can also be used with open-drain MOS outputs with the addition of pull-down resistors on each input.

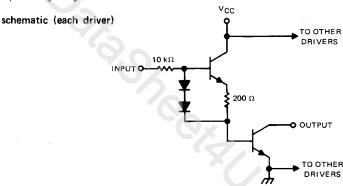
The 100-mA output current capability (open collector) and low output saturation voltage makes these devices ideal for other applications such as lamp drivers, relay drivers, line drivers, high-fan-out TTL buffers, etc. The advantages over earlier digit drivers include lower operating voltage, lower output saturation voltage, lower input current, and higher input voltage range.



SN75497 . . . N DUAL-IN-LINE PACKAGE

SN75498 . . . N DUAL-IN-LINE PACKAGE (TOP VIEW)





columns
Each of
is which
der, for
out may
a worst
ppose a
display
is 4 pF.
were on

er must

i.6 mA. required ll the N nown in at zero

> RS RS

s 200 V de must

3 an also

ww.Da

TYPES SN75497, SN75498 MOS-TO-LED 7- OR 9-CHANNEL DRIVERS

Input voltage		•																		
Continuous output collector current										 ٠				٠		٠				٠
Ground-terminal current												٠			٠	٠	٠	٠	•	
Continuous total dissipation over ent	tire o	pe	rati	ing	ran	nge														
Operating free-air temperature range												٠	-		٠			٠) ۰	ľC
Storage temperature range											٠				٠		٠	- (00	CI
Lead temperature 1/16 inch from ca	se fo	ır 1	0 s	ecc	ond	s								٠	٠			٠		•

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

recommended operating conditions

																								MIN		MAX	U
Supply voltage, VCC																								2.7	,	6.6	,
High-level input voltage, VIH	•	•	•	٠	•	·									_									2.7	•	Vcc	,
High-level input voltage, VIH	•	•	•	•	•		•	•	•	•	•	•	٠	•	•	•								-8 F		ő	,
Low-level input voltage, VIL	٠	•	٠	•	٠	•	•	•	•	•	٠	•	•	•	•	•	•	•	•	•	•	•	•	0.4		100	n
Output Current, IO		٠			•	٠	٠	•	٠	٠	٠	٠	•	٠	٠	•	٠	٠	٠	•	•	•	•	٠.,	٠.	100	0
Operating free-air temperature, TA								٠					٠	-	-		•	٠	٠	٠	٠	٠	٠	Ĺ	J	/0	

electrical characteristics over recommended free-air temperature range (unless otherwise noted)

	PARAMETER	1		TEST CONDITIONS		MIN	TYP	MAX	U
IO(off)	Off-state output		V _{CC} = 6.6 V,	A at 0 V,	Y at 6.6 V		10	100	4
10(6)1)	0			A at 6.6 V thru 500 Ω,	I _O = 100 mA		0.24	0.4	Γ
VO(on) On-state output voltage	voltage			I _O = 50 mA		0.12	0.25]	
VO(on)	011-312-12-12-1		V _{CC} = 2.7 V,	A at 2.7 V thru 500 Ω	IO = 100 mA		0.24	0.4	1
		Vcc.		A at 6.6 V,	I _O = 100 mA		0.6	1	Ι,
liH.	High-level input of	current	V _{CC} = 2.7 V,	A at 2.7 V,	I _O = 100 mA			0.4	Ľ
			V _{CC} = 6.6 V,	A at -8.5 V,	V _O = 6.6 V		-10	-100	Ι
11L	Low-level input of	current	V _{CC} = 2.7 V,	A at -8.5 V,	V _O = 2.7 V			-100	1
		V _{CC} = 6.6 V,	One A input at 6.6 V,			2.5	5	Ι.	
Icc	Supply current One driver of		10 = 100 mA,	Other A inputs at 0 V		L			L
00		All drivers off	V _{CC} = 6.6 V,	All inputs at 0 V				200	

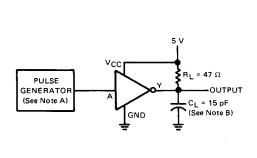
[†] All typical values are at TA = 25°C

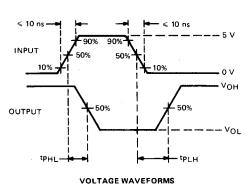
TYPES SN75497, SN75498 MOS-TO-LED 7- OR 9-CHANNEL DRIVERS

switching characteristics, VCC = 5 V, TA = 25°C

PARAMETER	TEST CONDITIONS	MIN TYP MAX	UNIT
tPLH Propagation delay time, low-to-high level output	C ₁ = 15 pF. R ₁ = 47 Ω	250	ns
tpht Propagation delay time, high-to-low level output	C[-15pr, H[-4/1	40	ns

PARAMETER MEASUREMENT INFORMATION





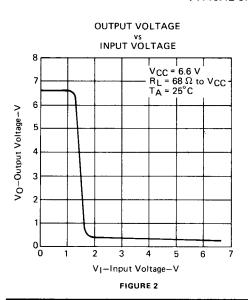
TEST CIRCUITS

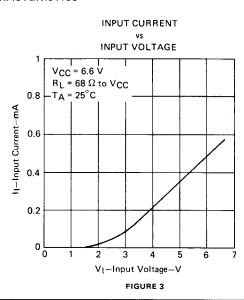
NOTES: A. The pulse generator has the following characteristics: Z_{out} = 50 Ω , PRR = 100 kHz, t_{W} = 1 μ s.

B. C₁ includes probe and jig capacitance.

FIGURE 1-PROPAGATION DELAY TIMES

TYPICAL CHARACTERISTICS





der, for out may a worst ppose a display is 4 pF. were on

:olumns Each of is which

er must

i.6 mA. equired. Il the N nown in : at zero

> IN 35

s 200 V de must

i.6 mA. can also