

BIPOLAR ANALOG INTEGRATED CIRCUIT $\mu PC319$

HIGH SPEED DUAL COMPARATOR

DESCRIPTION

The μ PC319 is a precision high speed dual comparator designed to operate over a wide range of supply voltage down to a 5 V logic supply and ground. Further advantage, they have excellent input characteristics and direct drive capability to all the popular logic families.

FEATURES

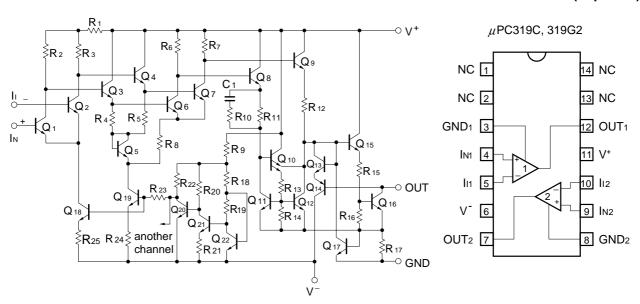
- Operate from single 5 V supply
- ullet Typically 80 ns response time at $\pm 15~V$
- Open collector output
- Minimum fan-out of 2 each side (TTL)
- High common mode slew rate

ORDERING INFORMATION

Part Number	Package
μPC319C	14-pin plastic DIP (7.62 mm (300))
μ PC319G2	14-pin plastic SOP (5.72 mm (225))

EQUIVALENT CIRCUIT (1/2 Circuit)

PIN CONFIGURATION (Top View)



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Not all devices/types available in every country. Please check with local NEC representative for availability and additional information.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C)

Par	ameter	Symbol	Ratings	Unit
Voltage between V ⁺ a	nd V ^{- Note 1}	$V^+ - V^-$	-0.3 to +36	V
Differential Input Volta	age	V _{ID}	±5	V
Input Voltage ^{Note 2}		Vı	V ⁻ -0.3 to V ⁺ +0.3	V
Output to Negative S	upply Voltage ^{Note 3}	Vo – V	-0.3 to +36	V
Ground to Negative S	Supply Voltage Note 3	V _{GND} - V ⁻	-0.3 to +25	V
Ground to Positive So	upply Voltage ^{Note 3}	V ⁺ – V _{GND}	-0.3 to +18	V
Power Dissipation	C Package Note 4	PT	570	mW
	G2 Package Note 5		550	mW
Output Short Circuit Duration Note 6			10	sec
Operating Ambient Temperature		TA	-20 to +80	°C
Storage Temperature		T _{stg}	-55 to +125	°C

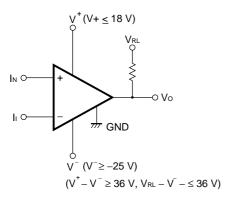
- **Notes 1.** Reverse connection of supply voltage can cause destruction.
 - 2. The input voltage should be allowed to input without damage or destruction. Even during the transition period of supply voltage, power on/off etc., this specification should be kept. The normal operation will establish when the both inputs are within the Common Mode Input Voltage Range of electrical characteristics.
 - **3.** This specification is the voltage which should be allowed to supply to the output and GND terminal from external without damage or destructive. Even during the transition period of supply voltage, power on/off etc., this specification should be kept.
 - **4.** Thermal derating factor is –7.6 mW/°C when operating ambient temperature is higher than 50°C.
 - **5.** Thermal derating factor is –5.5 mW/°C when operating ambient temperature is higher than 25°C.
 - **6.** Pay careful attention to the total power dissipation not to exceed the absolute maximum ratings, Note 4 and Note 5.

RECOMMENDED OPERATING CONDITIONS

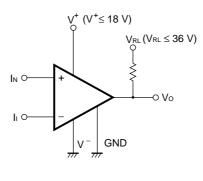
Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage (split)	V [±]	±5		±16	V
Supply Voltage (V ⁻ = GND)	V ⁺	+5		+32	V

TYPICAL CONNECTIONS

SPLIT SUPPLIES



SINGLE SUPPLY



2

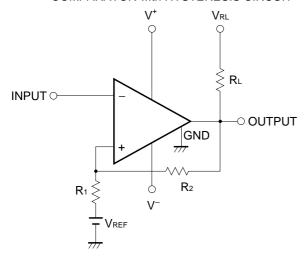


ELECTRICAL CHARACTERISTICS (Ta = 25°C, V^{\pm} = ± 15 V)

Parameter	Symbol	Conditions	MIN.	TYP.	MAX.	Unit
Input Offset Voltage	Vio	$V^{+} - V^{-} = 5 \text{ to } 30 \text{ V}, \text{Rs} \le 5 \text{ k}\Omega$		±2.0	±8.0	mV
Input Offset Current	lio	$V^+ - V^- = 5 \text{ to } 30 \text{ V}$		±80	±200	nA
Input Bias Current	Ів	V ⁺ – V ⁻ = 5 to 30 V		400	1,000	nA
Voltage Gain	Av		8,000	40,000		
Response Time		Input 100 mV, Overdrive 5 mV		80		ns
Output Saturation Voltage	Vol	V _I ≤ −10 mV, I _O = 25 mA		0.75	1.5	V
Output Leakage Current	IOLEAK	V₁ ≥ 10 mV, Vo = 35 V		0.2	10	μΑ
Positive Supply Current	I ⁺	$V^+ = 5 \text{ V}, V^- = 0 \text{ V}, \text{ Io} = 0 \text{ A},$		4.3		mA
		Both Comparators				
Positive Supply Current	I ⁺	Io = 0 A, Both Comparators		8.0	12.5	mA
Negative Supply Current	Γ_	Io = 0 A, Both Comparators		3.0	5.0	mA
Input Offset Voltage	Vio	$V^{+} - V^{-} = 5 \text{ to } 30 \text{ V}, \text{ Rs} \le 5 \text{ k}\Omega,$			±10	mV
		T _A = 0 to 70°C				
Input Offset Current	lio	$V^{+} - V^{-} = 5 \text{ to } 30 \text{ V}, T_{A} = 0 \text{ to } 70^{\circ}\text{C}$			±300	nA
Input Bias Current	lв	$V^{+} - V^{-} = 5 \text{ to } 30 \text{ V}, T_{A} = 0 \text{ to } 70^{\circ}\text{C}$			1,200	nA
Common Mode Input Voltage	Vісм			±13		V
Range						
Output Saturation Voltage	VoL	$V^+ = 4.5 \text{ V}, V^- = 0 \text{ V}, \text{ V}_1 \le -10 \text{ mV},$ $I_0 \le 3.2 \text{ mA}$		0.23	0.4	V

TYPICAL APPLICATION CIRCUIT

COMPARATOR with HYSTERESIS CIRCUIT



Threshold Voltage

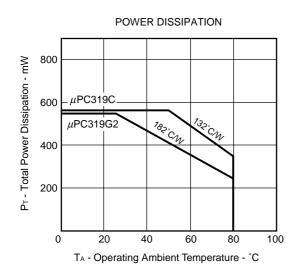
$$V_{TH (High)} = V_{REF} + \frac{R_1}{R_L + R_2 + R_1} (V_{RL} - V_{REF})$$

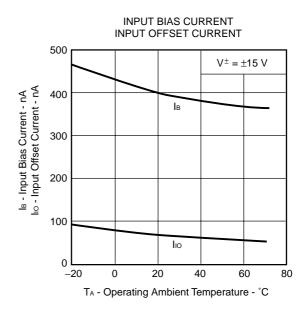
$$V_{TH (LOW)} = V_{REF} - \frac{R_1}{R_1 + R_2} (V_{REF} - V_{OL})$$

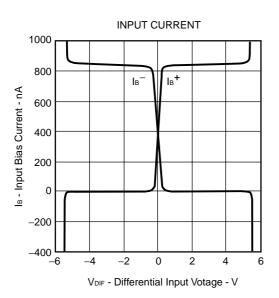
 $(V_{RL} > V_{REF} > V_{OL})$

3

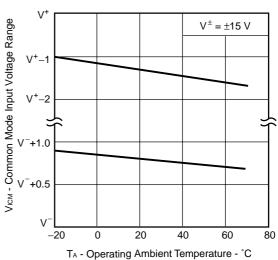
TYPICAL PERFORMANCE CHARACTERISTICS (TA = 25°C, TYP.)

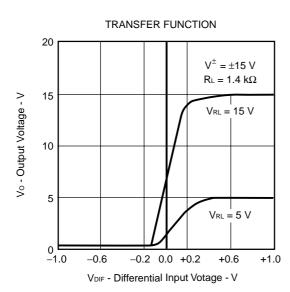


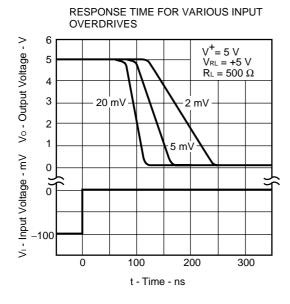


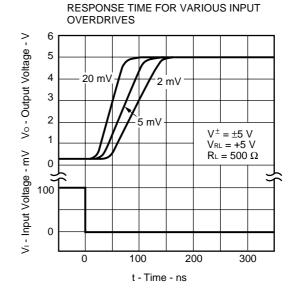


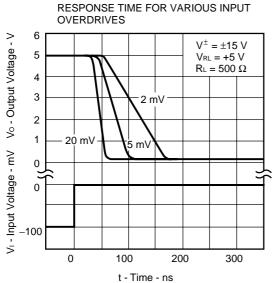


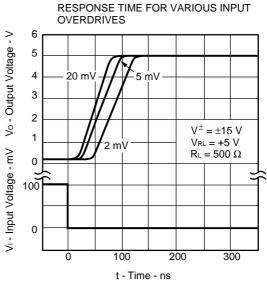


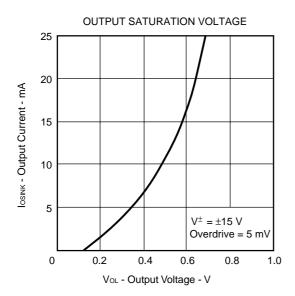


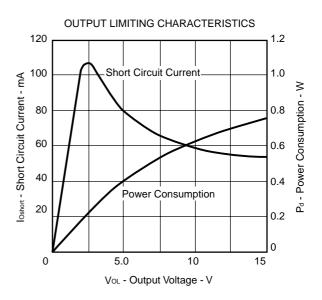




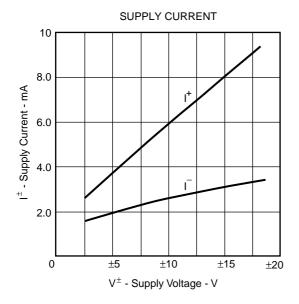


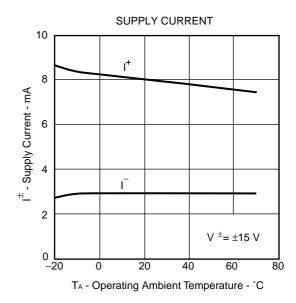


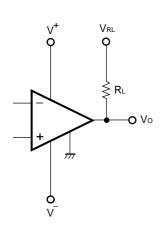


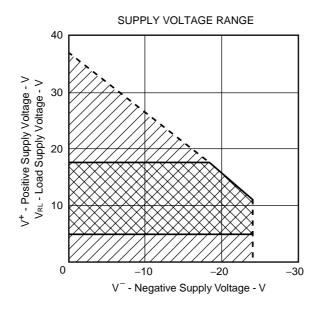


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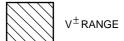






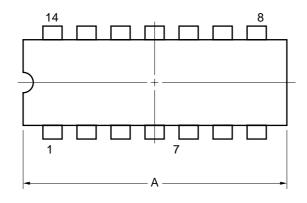


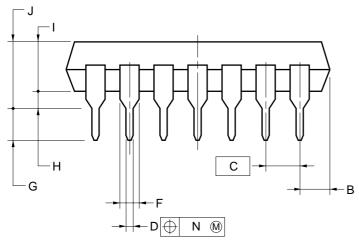
VRL RANGE

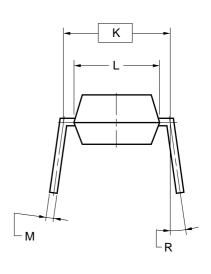


PACKAGE DRAWINGS (Unit: mm)

* 14-PIN PLASTIC DIP (7.62 mm (300))







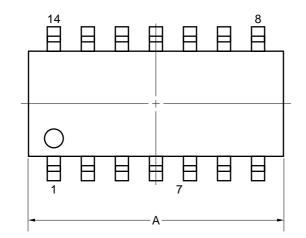
NOTES

- 1. Each lead centerline is located within 0.25 mm of its true position (T.P.) at maximum material condition.
- 2. Item "K" to center of leads when formed parallel.

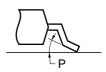
ITEM	MILLIMETERS
Α	19.22±0.2
В	2.14 MAX.
С	2.54 (T.P.)
D	0.50±0.10
F	1.32±0.12
G	3.6±0.3
Н	0.51 MIN.
- 1	3.55
J	4.3±0.2
K	7.62 (T.P.)
L	6.4±0.2
М	$0.25^{+0.10}_{-0.05}$
N	0.25
R	0~15°

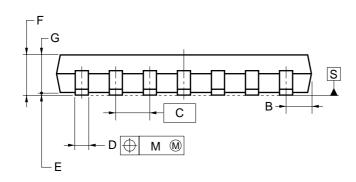
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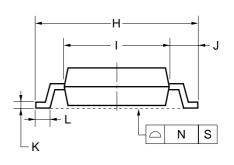
* 14-PIN PLASTIC SOP (5.72 mm (225))



detail of lead end







NOTE

Each lead centerline is located within 0.1 mm of its true position (T.P.) at maximum material condition.

ITEM	MILLIMETERS
Α	10.2±0.26
В	1.42 MAX.
С	1.27 (T.P.)
D	$0.42^{+0.08}_{-0.07}$
Е	0.1±0.1
F	$1.59^{+0.21}_{-0.2}$
G	1.49
Н	6.5±0.2
I	4.4±0.1
J	1.1±0.16
K	$0.17_{-0.07}^{+0.08}$
L	0.6±0.2
М	0.1
N	0.10
Р	3°+7°

S14GM-50-225B, C-6



★ RECOMMENDED SOLDERING CONDITIONS

When soldering this product, it is highly recommended to observe the conditions as shown below. If other soldering processes are used, or if the soldering is performed under different conditions, please make sure to consult with our sales offices.

For more details, refer to our document "SEMICONDUCTOR DEVICE MOUNTING TECHNOLOGY MANUAL" (C10535E).

Type of Surface Mount Device

 μ PC319G2: 14-pin plastic SOP (5.72 mm (225))

Process	Conditions	Symbol
Infrared Ray Reflow	Peak temperature: 230°C or below (Package surface temperature),	IR30-00-1
	Reflow time: 30 seconds or less (at 210°C or higher),	
	Maximum number of reflow processes: 1 time.	
Vapor Phase Soldering	Peak temperature: 215°C or below (Package surface temperature),	VP15-00-1
	Reflow time: 40 seconds or less (at 200°C or higher),	
	Maximum number of reflow processes: 1 time.	
Wave Soldering	Solder temperature: 260°C or below, Flow time: 10 seconds or less,	WS60-00-1
	Maximum number of flow processes: 1 time,	
	Pre-heating temperature: 120°C or below (Package surface temperature).	
Partial Heating Method	Pin temperature: 300°C or below,	_
	Heat time: 3 seconds or less (Per each side of the device).	

Caution Apply only one kind of soldering condition to a device, except for "partial heating method", or the device will be damaged by heat stress.

Type of Through-hole Device

μPC319C: 14-pin plastic DIP (7.62 mm (300))

	· · · · · · · · · · · · · · · · · · ·
Process	Conditions
Wave Soldering	Solder temperature: 260°C or below,
(only to leads)	Flow time: 10 seconds or less.
Partial Heating Method	Pin temperature: 300°C or below,
	Heat time: 3 seconds or less (per each lead).

Caution For through-hole device, the wave soldering process must be applied only to leads, and make sure that the package body does not get jet soldered.

Data Sheet G15950EJ4V0DS 9

NEC μ PC319

[MEMO]

NEC μ PC319

[MEMO]

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