

**Attansic** Technology Corporation.

# ATSP8 CPU Smart Guard

Release Date: Dec. 2001

**Revision: 1.0**

**Revision History**

<b>Version</b>	<b>Date</b>	<b>Changes from Last Version</b>
0.23	02/05/2002	1. Add Time Table (p3-1)

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## 1. General Description

ATSP8 is an integration device that includes a 1.5 V low dropout voltage regulator and a control logic circuit for Advanced CPU Thermo-trip function. The regulator has a low dropout voltage at full load 150mA. The control logic circuit supports Thermo-trip function, turn off power, PWM and clock to protect CPU and system when Thermo-trip activating.

## 2. Features

- Low Dropout voltage regulator (1.2V/150mA)
- Package: SOP 8-Pin
- Advanced CPU Thermal-trip function

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### 3. Pin Configuration

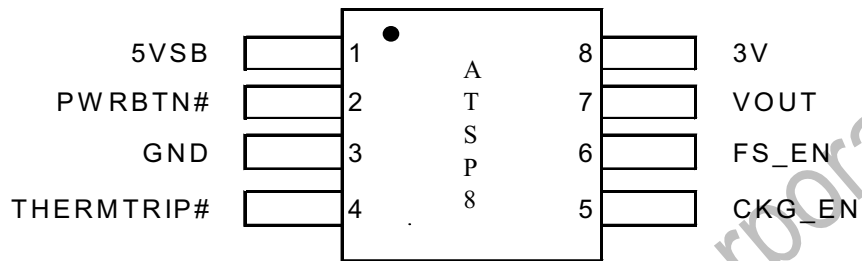
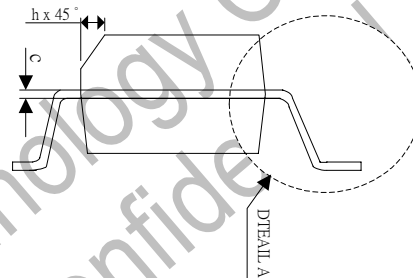
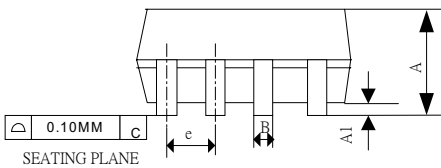
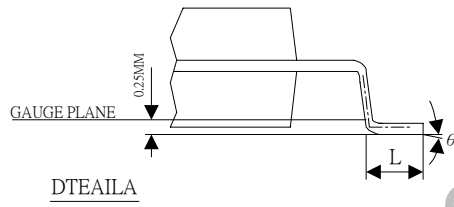
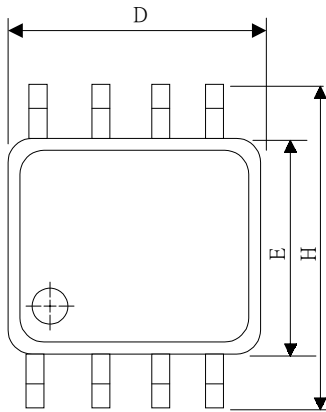


Figure 1. ATSP8 Pin Diagram (Top View)

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## 4. Package Information SOP8pin Outline Dimension



Symbol	Dimension in mm		Dimension in inch	
	Min	Max	Min	Max
A	1.35	1.75	0.0532	0.0688
A1	0.10	0.25	0.0040	0.0098
B	0.33	0.51	0.013	0.020
C	0.19	0.25	0.0075	0.0098
e	1.27BSC		0.050BSC	
D	4.80	5.00	0.1890	0.1968
H	5.80	6.20	0.2284	0.2440
E	3.80	4.00	0.1497	0.1574
L	0.40	1.27	0.016	0.050
h	0.25	0.50	0.0099	0.0196
θ	0°	8°	0°	8°
JEDEC	MS-012 (AA)			

\*NOTES: DIMENSION "D" DOES NOT INCLUDE MOLD FLASH, PROTRUSIONS OR GATE BURRS.

MOLD FLASH, PROTRUSIONS AND GATE BURRS SHALL NOT EXCEED 0.15 MM (0.006 INCH) PER SIDE.



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