SILICON TRANSISTOR 2SC1623A

NPN SILICON EPITAXIAL TRANSISTOR MINI MOLD

FEATURES

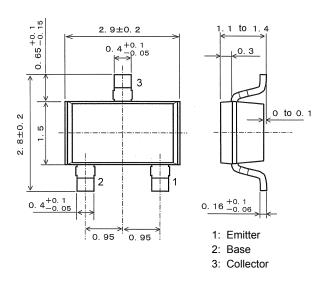
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- High DC Current Gain: hFE = 200 TYP. (VcE = 6.0 V, lc = 1.0 mA)
- High Voltage: VCEO = 50 V

ABSOLUTE MAXIMUM RATINGS (T_A = 25°C)

Collector to Base Voltage	Vсво	60	V
Collector to Emitter Voltage	VCEO	50	V
Emitter to Base Voltage	Vebo	5.0	V
Collector Current (DC)	lc	100	mA
Total Power Dissipation	Pτ	200	mW
Junction Temperature	Tj	150	°C
Storage Temperature Range	Tstg	–55 to +150	°C

PACKAGE DRAWING (Unit: mm)



ELECTRICAL CHARACTERISTICS (T_A = 25°C)

CHARACTERISTIC	SYMBOL	MIN.	TYP.	MAX.	UNIT	TEST CONDITIONS
Collector Cut-off Current	Ісво			0.1	μA	V _{CB} = 60 V, I _E = 0 A
Emitter Cut-off Current	Іево			0.1	μA	V _{EB} = 5.0 V, I _C = 0 A
DC Current Gain	hfe	90	200	600		$V_{CE} = 6.0 \text{ V}, \text{ Ic} = 1.0 \text{ mA}^{Note}$
Collector Saturation Voltage	VCE(sat)		0.15	0.3	V	I_{C} = 100 mA, I_{B} = 10 mA ^{Note}
Base to Saturation Voltage	V _{BE(sat)}		0.86	1.0	V	$I_{\rm C}$ = 100 mA, $I_{\rm B}$ = 10 mA ^{Note}
Base to Emitter voltage	VBE	0.55	0.62	0.65	V	$V_{CE} = 6.0 \text{ V}, \text{ Ic} = 1.0 \text{ mA}^{Note}$
Gain Bandwidth Product	f⊤		250		MHz	V _{CE} = 6.0 V, I _E = -10 mA
Output Capacitance	Cob		3.0		pF	V _{CB} = 6.0 V, I _E = 0 A, f = 1.0 MHz

Note Pulsed: PW \leq 350 μ s, Duty Cycle \leq 2%

hfe CLASSIFICATION

Marking	L4	L5	L6	L7
hfe	90 to 180	135 to 270	200 to 400	300 to 600

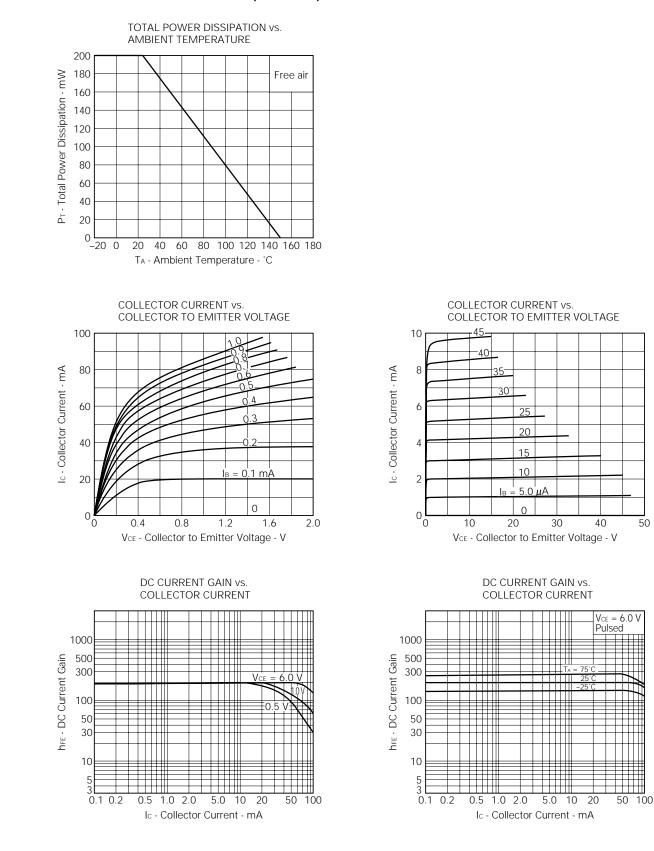
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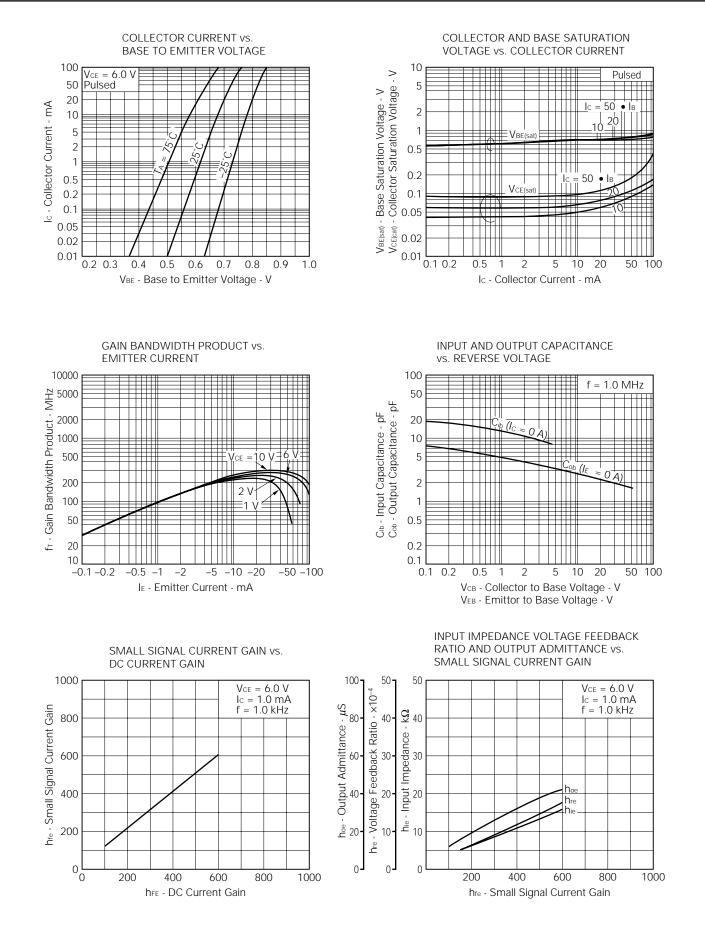
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The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

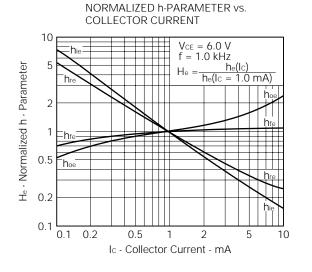


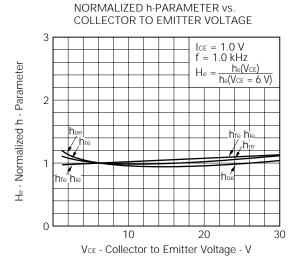


<R> TYPICAL CHARACTERISTICS (TA = 25°C)



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