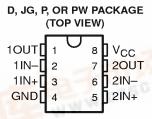
捷多邦,专业PCk**M科93,LM293**加**4M29**3A,LM393 LM393A,LM393Y,LM2903,LM2903Q DUAL DIFFERENTIAL COMPARATORS

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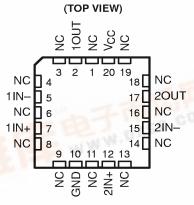
- Single Supply or Dual Supplies
- Wide Range of Supply Voltage... 2 V to 36 V
- Low Supply-Current Drain Independent of Supply Voltage . . . 0.5 mA Typ
- Low Input Bias Current . . . 25 nA Typ
- Low Input Offset Current . . . 3 nA Typ (LM193)
- Low Input Offset Voltage . . . 2 mV Typ
- Common-Mode Input Voltage Range Includes Ground
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . ±36 V
- Low Output Saturation Voltage
- Output Compatible With TTL, MOS, and CMOS

description

These devices consist of two independent voltage comparators that are designed to operate from a single power supply over a wide range of voltages. Operation from dual supplies also is possible as



FK PACKAGE



NC - No internal connection

long as the difference between the two supplies is 2 V to 36 V and V_{CC} is at least 1.5 V more positive than the input common-mode voltage. Current drain is independent of the supply voltage. The outputs can be connected to other open-collector outputs to achieve wired-AND relationships.

The LM193 is characterized for operation from –55°C to 125°C. The LM293 and LM293A are characterized for operation from –25°C to 85°C. The LM393 and LM393A are characterized for operation from 0°C to 70°C. The LM2903 and LM2903Q are characterized for operation from –40°C to 125°C and is manufactured to demanding automotive requirements.

logic diagram (each comparator)





Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.



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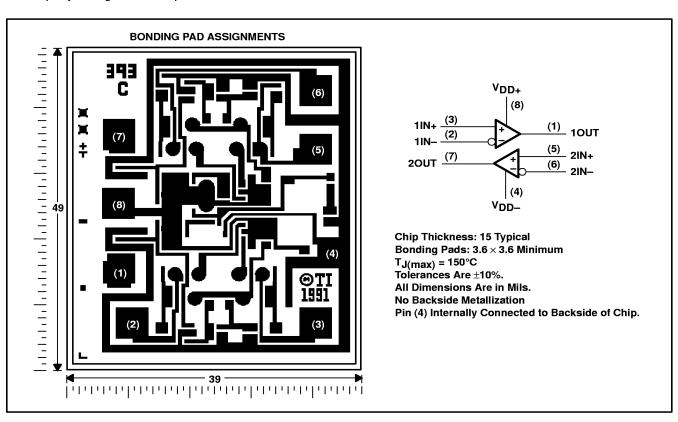
AVAILABLE OPTIONS

			PAC	KAGED DEV	ICES		CHIP			
TA	V _{IO(max}) AT 25°C	SMALL OUTLINE (D)†	CHIP CARRIER (FK)	CERAMIC DIP (JG)	PLASTIC DIP (P)	TSSOP (PW)‡	FORM (Y)§			
0°C to 70°C	5 mV	LM393D	_	_	LM393P	LM393PW	LM393Y			
0 0 10 70 0	2 mV	LM393AD	_	_	LM393AP	_	_			
-25°C to 85°C	5 mV	LM293D	_	_	LM293P	_	_			
-25 C 10 65 C	2 mV	LM293AD	_	_	LM293AP	_	_			
-40°C to 125°C	7 mV	LM2903D	_	_	LM2903P	LM2903PW	_			
-40°C to 125°C	/ IIIV	LM2903QD	_	_	LM2903QP	_	_			
–55°C to 125°C	5 mV	LM193D	LM193FK	LM193JG	LM193P	_	_			

[†] The D package is available taped and reeled. Add the suffix R (e.g., LM393DR).

LM393Y chip information

This chip, when properly assembled, displays characteristics similar to the LM393. Thermal compression or ultrasonic bonding can be used on the doped-aluminum bonding pads. Chips can be mounted with conductive epoxy or a gold-silicon preform.





as of the same

[‡] The PW package is only available left-end taped and reeled.

[§] Chips are tested at 25°C (see LM393Y for electrical characteristics).

COMPONENT COUNT Epi-FET Diodes

Resistors

Transistors

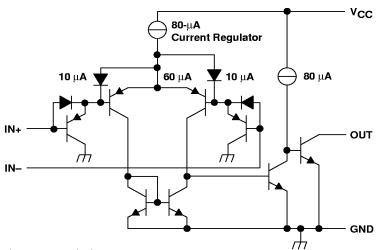
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2

2

30

schematic (each comparator)



Current values shown are nominal.

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Supply voltage, V _{CC} (see Note 1)	36 V
Differential input voltage, V _{ID} (see Note 2)	±36 V
Input voltage range, V _I (either input)	–0.3 V to 36 V
Output voltage, V _O	
Output current, IO	20 mA
Duration of output short-circuit to ground (see Note 3)	unlimited
Continuous total power dissipation	. See Dissipation Rating Table
Operating free-air temperature range, T _A : LM193	–55°C to 125°C
LM293, LM293A	
LM393, LM393A	0°C to 70°C
LM2903, LM2903Q	–40°C to 125°C
Storage temperature range, T _{stq}	
Case temperature for 60 seconds: FK package	
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds: D, P, or PW	package 260°C
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds: JG package	300°C

[†] Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. All voltage values, except differential voltages, are with respect to the network ground.
 - 2. Differential voltages are at IN+ with respect to IN-.
 - 3. Short circuits from outputs to V_{CC} can cause excessive heating and eventual destruction.

DISSIPATION RATING TABLE

PACKAGE	T _A ≤ 25°C POWER RATING	DERATING FACTOR	DERATE ABOVE T _A	T _A = 70°C POWER RATING	T _A = 85°C POWER RATING	T _A = 125°C POWER RATING
D	725 mW	5.8 mW/°C	25°C	464 mW	377 mW	145 mW
FK	900 mW	11.0 mW/°C	68°C	880 mW	715 mW	275 mW
JG	900 mW	8.4 mW/°C	43°C	672 mW	546 mW	210 mW
Р	900 mW	8.0 mW/°C	37°C	640 mW	520 mW	200 mW
PW	525 mW	4.2 mW/°C	25°C	336 mW	273 mW	N/A



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electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

PARAMETER		TEST CO	NDITIONS	T _A †	L	LM193			M293 M393		UNIT
				,	MIN	TYP	MAX	MIN	TYP	MAX	
\/\.	Input offset voltage	V _{CC} = 5 V to 3	0 V,	25°C		2	5		2	5	mV
VIO	input onset voltage	$V_O = 1.4 V$, $V_{IC} = V_{IC(min)}$		Full range			9			9	IIIV
lio.	Input offset current	V _O = 1.4 V		25°C		3	25		5	50	пA
lio	input onset current	VO = 1.4 V		Full range			100			250	Ĭ
lin.	Input bias current	Vo - 1.4 V		25°C		-25	-100		-25	-250	пA
lВ	input bias current	$V_0 = 1.4 \text{ V}$		Full range			-300			– 400	IIA
	Common-mode			25°C	0 to V _{CC} -1.5			0 to V _{CC} -1.5			V
VICR	VICR input voltage range‡				0 to V _{CC} -2			0 to V _{CC} -2			V
A _{VD}	Large-signal differential voltage amplification	V_{CC} = 15 V, V_{O} = 1.4 V to 1 $R_{L} \ge$ 15 k Ω to \	,	25°C	50	200		50	200		V/mV
lau	High-level	V _{OH} = 5 V,	V _{ID} = 1 V	25°C		0.1			0.1	50	nA
ЮН	output current	V _{OH} = 30 V,	V _{ID} = 1 V	Full range			1			1	μΑ
\/	Low-level	l 1 m 1	V:- 1.V	25°C		150	400		150	400	mV
VOL	output voltage	$I_{OL} = 4 \text{ mA}, \qquad V_{ID} = -1 \text{ V}$ Full rang	Full range			700			700	IIIV	
loL	Low-level output current	V _{OL} = 1.5 V,	V _{ID} = 1 V	25°C	6			6			mA
	Complete accompany	D.	V _{CC} = 5 V	25°C		0.8	1		8.0	1	A
Icc	Supply current F	RL = ∞	V _{CC} = 30 V	Full range			2.5			2.5	mA

[†] Full range (MIN or MAX) for LM193 is -55°C to 125°C, for LM293 is 25°C to 85°C, and for LM393 is 0°C to 70°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

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[‡] The voltage at either input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V_{CC+} -1.5 V, but either or both inputs can go to 30 V without damage.

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electrical characteristics at specified free-air temperature, $V_{CC} = 5 \text{ V}$ (unless otherwise noted)

PARAMETER		TEST CO	NDITIONS	T _A †		M293A M393A			M2903 I2903Q		UNIT
					MIN	TYP	MAX	MIN	TYP	MAX	
\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	Input offset voltage	V _{CC} = 5 V to 3	0 V,	25°C		1	3		2	7	mV
VIO	input onset voltage	$V_O = 1.4 \text{ V},$ $V_{IC} = V_{IC(min)}$		Full range			4			15	IIIV
lio.	Input offset current	V _O = 1.4 V		25°C		5	50		5	50	пA
10	input onset current	VO = 1.4 V		Full range			150			200	117
lin	Input bias current	V _O = 1.4 V		25°C		-25	-250		-25	-250	пA
IIB	input bias current	V() = 1.4 V		Full range			-4 00			-500	ПА
\\\	Common-mode			25°C	0 to V _{CC} -1.5			0 to V _{CC} -1.5			<
VICR	VICR input voltage range‡				0 to V _{CC} -2			0 to V _{CC} -2			V
A _{VD}	Large-signal differential voltage amplification	V_{CC} = 15 V, V_{O} = 1.4 V to 1 $R_{L} \ge$ 15 k Ω to \	,	25°C	50	200		25	100		V/mV
lau	High-level	V _{OH} = 5 V,	V _{ID} = 1 V	25°C		0.1	50		0.1	50	nA
ІОН	output current	V _{OH} = 30 V,	V _{ID} = 1 V	Full range			1			1	μΑ
V	Low-level	la. 4 m 4	V:- 1.V	25°C		150	400		150	400	mV
VOL	output voltage	$I_{OL} = 4 \text{ mA},$	$V_{ID} = -1 V$	Full range			700			700	IIIV
loL	Low-level output current	V _{OL} = 1.5 V,	V _{ID} = 1 V	25°C	6			6			mA
la a	Supply surrent	B	V _{CC} = 5 V	25°C		0.8	1		8.0	1	mA
ICC	Supply current	R _L = ∞	V _{CC} = 30 V	Full range			2.5			2.5	IIIA

[†] Full range (MIN or MAX) for LM293A is 25°C to 85°C, for LM393A is 0°C to 70°C, and for LM2903 and LM2903Q is -40°C to 125°C. All characteristics are measured with zero common-mode input voltage, unless otherwise specified.

electrical characteristics at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$ (unless otherwise noted)

	PARAMETER	TEST CO	LM393Y			UNIT	
	PARAMETER	l lesi cc	MIN	TYP§	MAX	CIVIT	
V _{IO}	Input offset voltage	.,,,			2	5	mV
Iю	Input offset current	V _{CC} = 5 V to 30 V, V _O = 1.4 V	VIC = VICR(min),		5	50	nA
I _{IB}	Input bias current	10-114			-25	-250	nA
V _{ICR}	Common-mode input voltage range	V _{CC} = 5 V to 30 V		0 to V _{CC} -1.5	5		V
A _{VD}	Large-signal differential voltage amplification	V_{CC} = 15 V, R _L ≥ 15 k Ω to V _{CC}	$V_O = 1.4 \text{ V to } 11.4 \text{ V},$	25	200		V/mV
IOH	High-level output current	V _{OH} = 5 V,	V _{ID} = 1 V		0.1	50	nA
V _{OL}	Low-level output voltage	I _{OL} = 4 mA,	V _{ID} = -1 V		150	400	mV
loL	Low-level output current	V _{OL} = 1.5 V,	V _{ID} = -1 V	6			mA
Icc	Supply current	R _L = ∞,	V _{CC} = 5 V		0.8	1	mA

[§] All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified.



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[‡] The voltage at either input or common-mode should not be allowed to go negative by more than 0.3 V. The upper end of the common-mode voltage range is V_{CC+} -1.5 V, but either or both inputs can go to 30 V without damage.

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switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

PARAMETER	ETER TEST CONDITIONS		LM29 LM290	UNIT		
			MIN	MAX		
Despense time	R_L connected to 5 V through 5.1 k Ω ,	100-mV input step with 5-mV overdrive	1.3			
Response time	C _L = 15 pF [†] , See Note 4	TTL-level input step		0.3	·	μs

†C_L includes probe and jig capacitance.

NOTE 4: The response time specified is the interval between the input step function and the instant when the output crosses 1.4 V.

