

SPECIFICATIONS

DIGITAL TO ANALOG CONVERTER

DA121050

Resolution	12 Binary Bits
Output: Voltage	0 volts to +9.995 volts
Impedance	5000 ohms $\pm 0.01\%$
Zero Offset	Less than -1.0 mv. tempco $\pm 10\mu\text{v}/^\circ\text{C}$
Accuracy	± 2.0 millivolts tempco ± 20 ppm/ $^\circ\text{C}$
Linearity	± 1 part in 10000 deviation from best straight line
Settling Time	12 usec
Current Mode	Less than 0.5 usec to $\pm 0.02\%$ F.S.

Power Requirement:

+15 volts @ 120 ma
-15 volts @ 150 ma

Reference:

Self Contained +10 volts
 $\pm 0.01\%$

Power Supply Sensitivity

Zero Nil
+ full scale less than 1 mv./V

Data Input:

12 Line parallel data +F.S.
zero
Logic "0" < +0.7 volts @ 1.5ma
Logic "1" > 2.5 volts @ 0.1ma

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Temperature:

0 $^\circ\text{C}$ to 50 $^\circ\text{C}$ for all stated specifications
-20 $^\circ\text{C}$ to +65 $^\circ\text{C}$ at reduce performance

Mechanical:

Encapsulated Module
1.5" x 2.4" x 0.8"
terminals on 0.2" grid

