

Re: Q: lpc921 compatibility with 8051?

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Source: <http://coding.derkeiler.com/Archive/General/comp.arch.embedded/2005-08/msg00906.html>

- From: Chris Hills <chris@xxxxxxxxxxxxx>
- Date: Thu, 25 Aug 2005 13:41:15 +0100

In article <dehl61\$sb4\$1@xxxxxxxxxxxxx>, Petar Silic
<shilla@xxxxxxxxxxxxx> writes

>
 >hi,
 >
 >I have a project with philips p89lpc921, and need to use it as standard 8051
 >
 >now, this two chips have different clocks per cycle, as I understood,
 >
 >so delay and timer do not work corectly

The timers DO work correctly.

The standard 51 family use 12 clocks per cycle
 Dalas-Maxim use 4 clocks per cycle
 Infineon, Temic, Atmel, Philips Cygnal, Sylicon labs etc etc etc use 6
 clock per cycle

Some Dallas, Infineon, Philips and others use 2 clocks per cycle for
 some parts.

There are VERY FEW still using the 12 clocks per cycle these days. Many
 like the Philips, Atmel and Infineon let you set the part in either 6
 or 12 cycles but I don't think this wil continue much longer.

BTW I don't think Intel even do a 51 now

RTFM is the answer to most of the questions you have.

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 ~~~~\ Chris Hills Staffs England /~~~~  
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- **References:**

- ◆ **Q: lpc921 compatibility with 8051?**

- ◇ *From:* Petar Silic

- Prev by Date: **FREE Photo Ipod**

- Next by Date: **Re: Lantronix X-Port**

- Previous by thread: **Re: Q: lpc921 compatibility with 8051?**

- Next by thread: **Ecc Size**

- Index(es):

- ◆ **Date**

- ◆ **Thread**

