


MOTOROLA

Crystal Oscillator

The MC12061 is for use with an external crystal to form a crystal controlled oscillator. In addition to the fundamental series mode crystal, two bypass capacitors are required (plus usual power supply pin bypass capacitors). Translators are provided internally for MECL and TTL outputs.

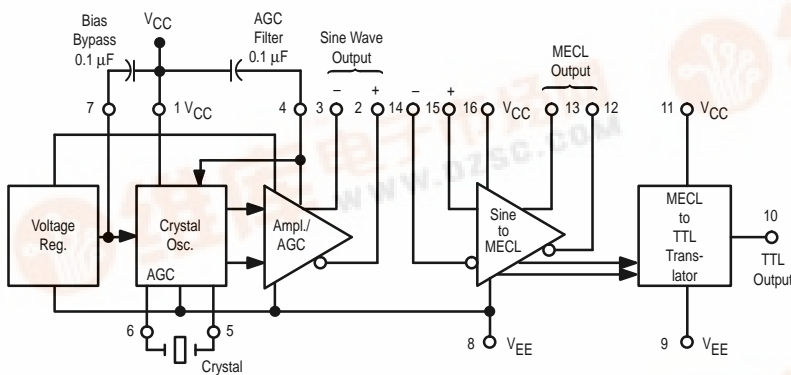
- Frequency Range = 2.0 to 20 MHz
- Temperature Range = 0 to + 70°C
- Single Supply Operation: +5.0 Vdc or -5.2 Vdc
- Three Outputs Available:
 1. Complementary Sine Wave (600 mVpp typ)
 2. Complementary MECL
 3. Single Ended TTL

MC12061

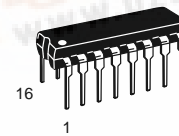
CRYSTAL OSCILLATOR

SEMICONDUCTOR TECHNICAL DATA

Figure 1. Block Diagram



Note: 0.1 μF power supply pin bypass capacitors not shown.



P SUFFIX
PLASTIC PACKAGE
CASE 648

ORDERING INFORMATION

| Device | Operating Temperature Range | Package |
|----------|------------------------------|---------|
| MC12061P | T _A = 0° to +70°C | Plastic |

TYPICAL CIRCUIT CONFIGURATIONS

Note: 0.1 μF power supply pin bypass capacitors not shown.

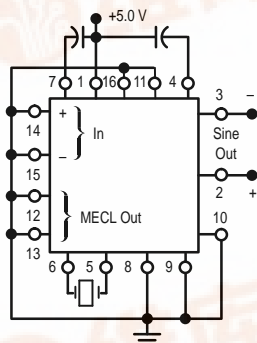


Figure 2. Sine Wave Output

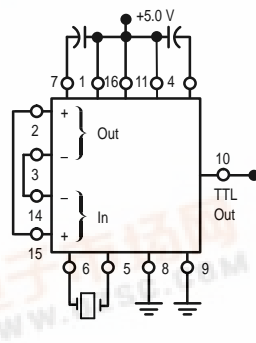


Figure 3. MTTL Output

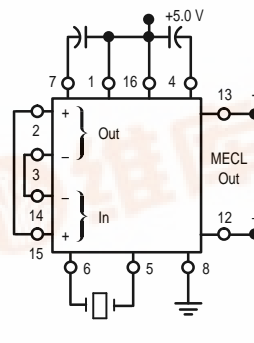


Figure 4. MECL Output
(+5.0 V Supply)

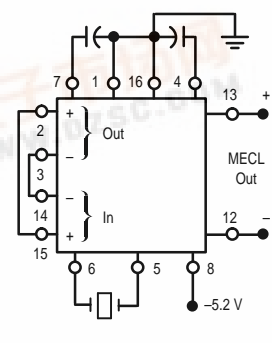


Figure 5. MECL Output
(-5.2 V Supply)

CRYSTAL REQUIREMENTS

Note: Start-up stabilization time is a function of crystal series resistance. The lower the resistance, the faster

| Characteristic | MC12061 |
|--|------------------------------|
| Mode of Operation | Fundamental Series Resonance |
| Frequency Range | 2.0 MHz — 20 MHz |
| Series Resistance, R ₁ | Minimum at Fundamental |
| Maximum Effective Resistance R _{E(max)} | 155 ohms |

MC12061

ELECTRICAL CHARACTERISTICS

| Characteristic | Symbol | Pin Under Test | Test Limits | | | | | | | Unit |
|------------------------------|------------------------------|----------------|-------------|------|-------|-----|------|-------|------|------|
| | | | 0°C | | +25°C | | | +75°C | | |
| | | | Min | Max | Min | Typ | Max | Min | Max | |
| Power Supply Drain Current | I _{CC} | 1 | — | — | 13 | 16 | 19 | — | — | mAdc |
| | | 1 | — | — | 18 | 23 | 28 | — | — | |
| | | 11 | — | — | — | 3.0 | 4.0 | — | — | |
| | | 16 | — | — | 13 | 16 | 19 | — | — | |
| Input Current | I _{inH} | 14 | — | — | — | — | 250 | — | — | μAdc |
| | | 15 | — | — | — | — | 250 | — | — | |
| | I _{inL} | 14 | — | — | — | — | 1.0 | — | — | μAdc |
| | | 15 | — | — | — | — | 1.0 | — | — | |
| Differential Offset Voltage | ΔV | 4 to 7 | — | — | 40 | — | 325 | — | — | mAdc |
| | | 2 to 3 | — | — | −200 | 0 | +200 | — | — | |
| Output Voltage Level | V _{out} | 2 | — | — | — | 3.5 | — | — | — | Vdc |
| | | 3 | — | — | — | 3.5 | — | — | — | |
| Logic '1' Output Voltage | V _{OH1} (Note 1) | 12 | 4.0 | 4.16 | 4.04 | — | 4.19 | 4.1 | 4.28 | Vdc |
| | | 13 | 4.0 | 4.16 | 4.04 | — | 4.19 | 4.1 | 4.28 | |
| | V _{OH2} | 10 | 2.4 | — | 2.4 | — | — | 2.4 | — | |
| Logic '0' Output Voltage | V _{OL1} (Note 1) | 12 | 2.98 | 3.43 | 3.0 | — | 3.44 | 3.02 | 3.47 | Vdc |
| | | 13 | 2.98 | 3.43 | 3.0 | — | 3.44 | 3.02 | 3.47 | |
| | V _{OL2} | 10 | — | 0.5 | — | — | 0.5 | — | 0.5 | |
| | | 10 | — | 0.5 | — | — | 0.5 | — | 0.5 | |
| Logic '1' Threshold Voltage | V _{OHA} | 12 | 3.98 | — | 4.02 | — | — | 4.08 | — | Vdc |
| | | 13 | 3.98 | — | 4.02 | — | — | 4.08 | — | |
| Logic '0' Threshold Voltage | V _{OLA} | 12 | — | 3.45 | — | — | 3.46 | — | 3.49 | Vdc |
| | | 13 | — | 3.45 | — | — | 3.46 | — | 3.49 | |
| Output Short Circuit Current | I _{OS} | 10 | 20 | 60 | 20 | — | 60 | 20 | 60 | mAdc |

NOTE: 1. Devices will meet standard MECL logic levels using $V_{EE} = -5.2$ Vdc and $V_{CC} = 0$.



MC12061

ELECTRICAL CHARACTERISTICS (continued)

| @ Test Temperature | | | TEST VOLTAGE/CURRENT VALUES | | | | | | Gnd | |
|------------------------------|------------------------------|----------------|---|--------------------|---------------------|---------------------|------------------|-------------------|--------|------|
| | | | Volts | | | | | | | |
| | | | V _{IHmax} | V _{ILmin} | V _{IHAmin} | V _{ILAmax} | V _{IHT} | V _{CCCL} | | |
| | | | 0°C | 4.16 | 3.19 | 3.86 | 3.51 | 4.0 | | 4.75 |
| | | | +25°C | 4.19 | 3.21 | 3.90 | 3.52 | 4.0 | | 4.75 |
| +75°C | 4.28 | 3.23 | 3.96 | 3.55 | 4.0 | 4.75 | | | | |
| Characteristic | Symbol | Pin Under Test | TEST VOLTAGE APPLIED TO PINS LISTED BELOW | | | | | | Gnd | |
| | | | V _{IHmax} | V _{ILmin} | V _{IHAmin} | V _{ILAmax} | V _{IHT} | V _{CCCL} | | |
| Power Supply Drain Current | I _{CC} | 1 | – | – | – | – | – | – | 8 | |
| | | 1 | – | – | – | – | – | – | 8 | |
| | | 11 | 14 | 15 | – | – | – | – | 8,9 | |
| | | 16 | – | – | – | – | – | – | 8 | |
| Input Current | I _{inH} | 14 | 14 | 15 | – | – | – | – | 8 | |
| | | 15 | 15 | 14 | – | – | – | – | 8 | |
| | I _{inL} | 14 | 15 | – | – | – | – | – | 8,14 | |
| | | 15 | 14 | – | – | – | – | – | 8,15 | |
| Differential Offset Voltage | ΔV | 4 to 7 | – | – | – | – | 5,6 | – | 8 | |
| | | 2 to 3 | – | – | – | – | 4 | – | – | |
| Output Voltage Level | V _{out} | 2 | – | – | – | – | 4 | – | 8 | |
| | | 3 | – | – | – | – | 4 | – | 8 | |
| Logic '1' Output Voltage | V _{OH1} (Note 1) | 12 | 14 | 15 | – | – | – | – | 8 | |
| | | 13 | 15 | 14 | – | – | – | – | 8 | |
| | V _{OH2} | 10 | 15 | 14 | – | – | – | 11,16 | 8,9 | |
| Logic '0' Output Voltage | V _{OL1} (Note 1) | 12 | 15 | 14 | – | – | – | – | 8 | |
| | | 13 | 14 | 15 | – | – | – | – | 8 | |
| | V _{OL2} | 10 | 14 | 15 | – | – | – | 11,16 | 8,9 | |
| | | 10 | 14 | 15 | – | – | – | – | 8,9 | |
| Logic '1' Threshold Voltage | V _{OHA} | 12 | – | – | 14 | 15 | – | – | 8 | |
| | | 13 | – | – | 15 | 14 | – | – | 8 | |
| Logic '0' Threshold Voltage | V _{OLA} | 12 | – | – | 15 | 14 | – | – | 8 | |
| | | 13 | – | – | 14 | 15 | – | – | 8 | |
| Output Short Circuit Current | I _{OS} | 10 | 15 | 14 | – | – | – | 11,16 | 8,9,10 | |

NOTE: 1. Devices will meet standard MECL logic levels using V_{EE} = –5.2Vdc and V_{CC} = 0.



MC12061

ELECTRICAL CHARACTERISTICS (continued)

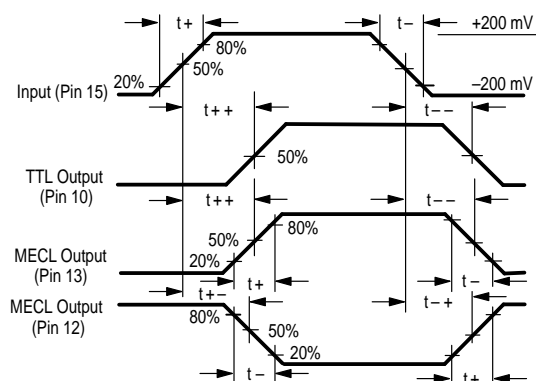
| @ Test Temperature | | | TEST VOLTAGE/CURRENT VALUES | | | | | |
|------------------------------|------------------------------|------------------------------|---|------------------|-----------------|-----------------|-----------------|--------------|
| | | | Volts | | mA | | | |
| | | | V _{CC} | V _{CCH} | I _{OL} | I _{OH} | I _{IL} | |
| | | | 5.0 | 5.25 | 16 | −0.4 | −2.5 | |
| | | | 5.0 | 5.25 | 16 | −0.4 | −2.5 | |
| | | | 5.0 | 5.25 | 16 | −0.4 | −2.5 | |
| Characteristic | Symbol | Pin Under Test | TEST VOLTAGE APPLIED TO PINS LISTED BELOW | | | | | Gnd |
| | | | V _{CC} | V _{CCH} | I _{OL} | I _{OH} | I _{IL} | |
| Power Supply Drain Current | I _{CC} | 1 | 1 | − | − | − | − | 8 |
| | | 1 | 1 | − | − | − | − | 8 |
| | | 11,16 | 11,16 | − | − | − | − | 8,9 8 |
| Input Current | I _{inH} | 14 | 16 | − | − | − | − | 8 |
| | | 15 | 16 | − | − | − | − | 8 |
| | I _{inL} | 14 | 16 | − | − | − | − | 8,14 8,15 |
| Differential Offset Voltage | ΔV | 4 to 7 | 1 | − | − | − | − | 8 |
| | | 2 to 3 | − | − | − | − | − | − |
| Output Voltage Level | V _{out} | 2 | 1 | − | − | − | − | 8 |
| | | 3 | 1 | − | − | − | − | 8 |
| Logic '1' Output Voltage | V _{OH1} (Note 1) | 12 | 16 | − | − | − | 12 | 8 |
| | | 13 | 16 | − | − | − | 13 | 8 |
| | V _{OH2} | 10 | − | − | − | 10 | − | 8,9 |
| | Logic '0' Output Voltage | V _{OL1} (Note 1) | 12 | 16 | − | − | − | 12 |
| 13 | | | 16 | − | − | − | 13 | 8 |
| | V _{OL2} | 10 | − | − | 10 | − | − | 8,9 |
| | | 10 | − | 11,16 | 10 | − | − | 8,9 |
| Logic '1' Threshold Voltage | V _{OHA} | 12 | 16 | − | − | − | 12 | 8 |
| | | 13 | 16 | − | − | − | 13 | 8 |
| Logic '0' Threshold Voltage | V _{OLA} | 12 | 16 | − | − | − | 12 | 8 |
| | | 13 | 16 | − | − | − | 13 | 8 |
| Output Short Circuit Current | I _{OS} | 10 | − | − | − | − | − | 8,9,10 |

NOTE: 1. Devices will meet standard MECL logic levels using V_{EE} = −5.2Vdc and V_{CC} = 0.



MC12061

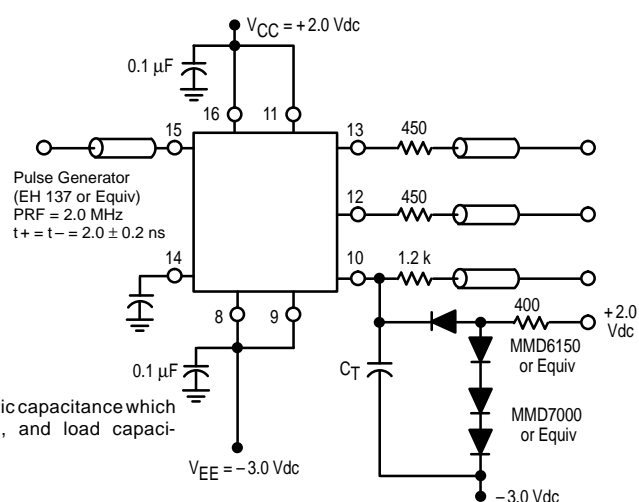
Figure 6. AC Characteristics – MECL and TTL Outputs



All input and output cables to the scope are equal lengths of 50 Ω coaxial cable.

Unused outputs are connected to a $50\ \Omega \pm 1\%$ resistor to ground.

$C_T = 15 \text{ pF}$ = total parasitic capacitance which includes probe, wiring, and load capacitance.



| Characteristic | Symbol | Pin Under Test | Test Limits | | | | | | | TEST VOLTAGES/WAVEFORMS APPLIED TO PINS LISTED BELOW: | | | | | Gnd |
|-------------------|---------------------|----------------|-------------|-----|-------|-----|-----|-------|-----|---|----------|-----------|----------|----------|-----|
| | | | 0°C | | +25°C | | | +75°C | | Unit | Pulse In | Pulse Out | +2.0 Vdc | −3.0 Vdc | |
| | | | Min | Max | Min | Typ | Max | Min | Max | | | | | | |
| Propagation Delay | t ₁₅₊₁₀₊ | 10 | — | 22 | — | 17 | 25 | — | 27 | ns | 15 | 10 | 11,16 | 8,9 | 14 |
| | t _{15−10−} | 10 | — | 19 | — | 12 | 18 | — | 18 | | | | | | |
| | t _{15+12−} | 12 | — | 5.2 | — | 4.3 | 5.5 | — | 5.8 | | | | | | |
| | t _{15−12+} | 12 | — | 5.0 | — | 3.7 | 5.2 | — | 5.2 | | | | | | |
| | t ₁₅₊₁₃₊ | 13 | — | 4.8 | — | 4.0 | 5.0 | — | 5.2 | | | | | | |
| | t _{15−13−} | 13 | — | 5.0 | — | 4.0 | 5.0 | — | 5.1 | | | | | | |
| Rise Time | t ₁₂₊ | 12 | — | 4.0 | — | 3.0 | 4.0 | — | 4.4 | ns | 15 | 12 | 11,16 | 8,9 | 14 |
| | t ₁₃₊ | 13 | — | 4.0 | — | 3.0 | 4.0 | — | 4.4 | ns | 15 | 13 | 11,16 | 8,9 | 14 |
| Fall Time | t _{12−} | 12 | — | 4.0 | — | 3.0 | 4.0 | — | 4.0 | ns | 15 | 12 | 11,16 | 8,9 | 14 |
| | t _{13−} | 13 | — | 4.0 | — | 3.0 | 4.0 | — | 4.0 | ns | 15 | 13 | 11,16 | 8,9 | 14 |

| Characteristic | Pin Under Test | +25°C | | Unit | TEST VOLTAGE APPLIED TO PINS LISTED BELOW | |
|---------------------|----------------|-------|-----|-------|---|----------|
| | | Min | Typ | | +2.0 Vdc | −3.0 Vdc |
| Sine Wave Amplitude | 2 | 650 | 750 | mVp-p | 1 | 8,9 |
| | 3 | 650 | 750 | | | |

Figure 7. AC Test Circuit – Sine Wave Output

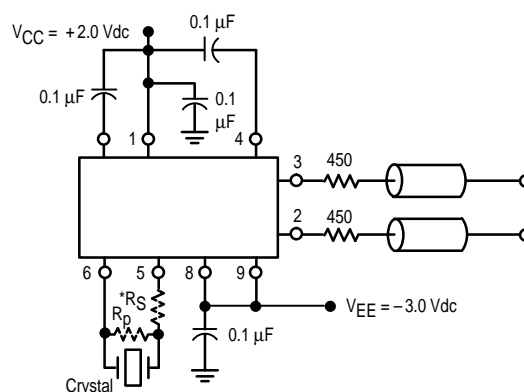
All output cables to the scope are equal lengths of 50 Ω coaxial cable. All unused cables must be terminated with a 50 $\Omega \pm 1\%$ resistor to ground.

450 Ω resistor and the scope termination impedance constitute a 10:1 attenuator probe.

Crystal — Reeves Hoffman Series Mode,
Series Resistance Minimum at Fundamental
 $f = 10 \text{ MHz}$
 $R_F = 5 \Omega$

*R_S = 15 kΩ is inserted only for test purposes. When used with the above specified crystal, it guarantees oscillation with any crystal which has an equivalent series resistance ≤ 155 Ω

R_p: will improve start up problems value: 200–500 Ω



MC12061

OPERATING CHARACTERISTICS

The MC12061 consists of three basic sections: an oscillator with AGC and two translators (NO TAG). Buffered complementary sine wave outputs are available from the oscillator section. The translators convert these sine wave outputs to levels compatible with MECL and/or TTL.

Series mode crystals should be used with the oscillator. If it is necessary or desirable to adjust the crystal frequency, a reactive element can be inserted in series with the crystal — an inductor to lower the frequency or a capacitor to raise it. When such an adjustment is necessary, it is recommended that the crystal be specified slightly lower in frequency and a series trimmer capacitor be added to bring the oscillator back on frequency. As the oscillator frequency is changed from the natural resonance of the crystal, more and more dependence is placed on the external reactance, and temperature drift of the trimming components then affects overall oscillator performance.

The MC12061 is designed to operate from a single supply — either +5.0 Vdc or -5.2 Vdc. Although each translator has separate V_{CC} and V_{EE} supply pins, the circuit is NOT designed to operate from both voltage levels at the same time. The separate V_{EE} pin from the TTL translator helps minimize transient disturbance. If neither translator is being used, all unused pins (9 thru 16) should be connected to V_{EE} (pin 8). With the translators not powered, supply current drain is typically reduced from 42 mA to 23 mA for the MC12061.

Frequency Stability

Output frequency of different oscillator circuits (of a given device type number) will vary somewhat when used with a given test setup. However, the variation should be within approximately $\pm 0.001\%$ from unit to unit.

Frequency variations with temperature (independent of the crystal, which is held at 25°C) are small — about -0.08 ppm/°C for MC12061 operating at 8.0 MHz (see NO TAG).

Signal Characteristics

The sine wave outputs at either pin 2 or pin 3 will typically range from 800 mV_{p-p} (no load) to 500 mV_{p-p} (120 ohm ac load). Approximately 500 mV_{p-p} can be provided across 50 ohms by slightly increasing the dc current in the output buffer by the addition of an external resistor (680 ohms) from pin 2 or 3 to ground, as shown in Figure 9. Frequency drift is typically less than 0.0003% when going from a high-impedance load (1 megohm, 15 pF) to the 50 ohm load of Figure 9. The dc voltage level at pin 2 or 3 is nominally 3.5 Vdc with $V_{CC} = +5.0$ Vdc.

Harmonic distortion content in the sine wave outputs is crystal as well as circuit dependent. The largest harmonic (third) will usually be at least 15 dB down from the fundamental. The harmonic content is approximately load independent except that the higher harmonic levels

(greater than the fifth) are increased when the MECL translator is being driven.

Typically, the MECL outputs (pins 12 and 13) will drive up to five gates, as defined in NO TAG, and the TTL output (pin 10) will drive up to ten gates, as defined in NO TAG.

Noise Characteristics

Noise level evaluation of the sine wave outputs using the circuit of NO TAG, with operation at or 9.0 MHz, indicates the following characteristics:

1. Noise floor (200 kHz from oscillator center frequency) is approximately -122 dB when referenced to a 1.0 Hz bandwidth. Noise floor is not sensitive to load conditions and/or translator operation.
2. Close-in noise (100 Hz from oscillator center frequency) is approximately -88 dB when referenced to a 1.0 Hz bandwidth.

Figure 8. Frequency Variation Due to Temperature

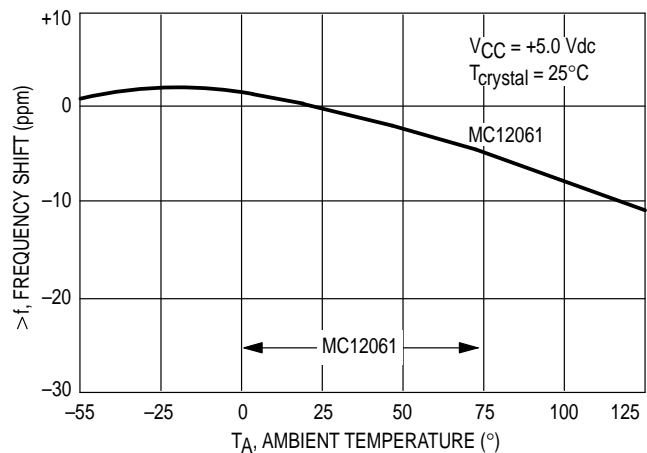
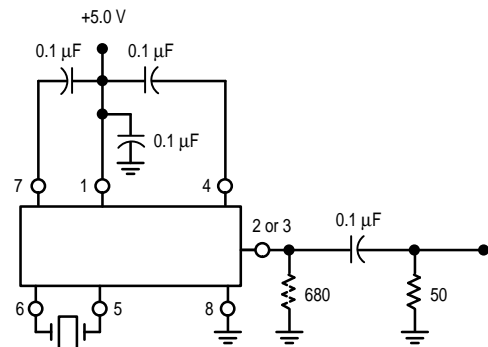


Figure 9. Driving Low Impedance Loads



* See text under signal characteristics.

MC12061

Figure 10. MECL Translator Load Capability

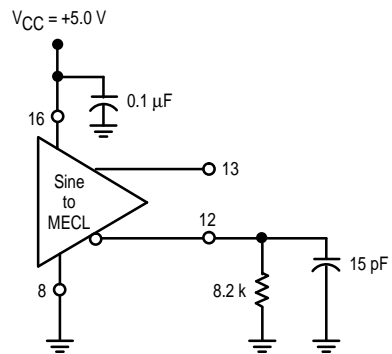


Figure 11. TTL Translator Load Capability

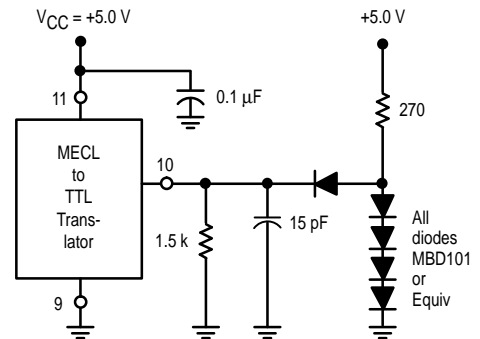
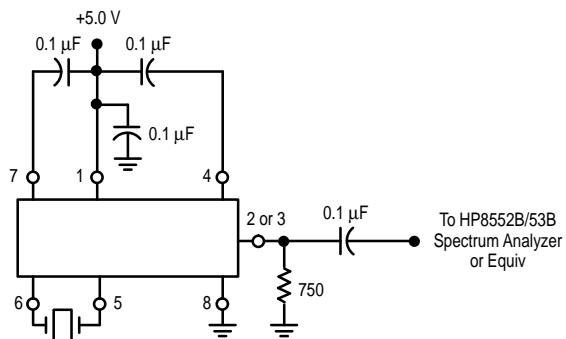


Figure 12. Noise Measurement Test Circuit



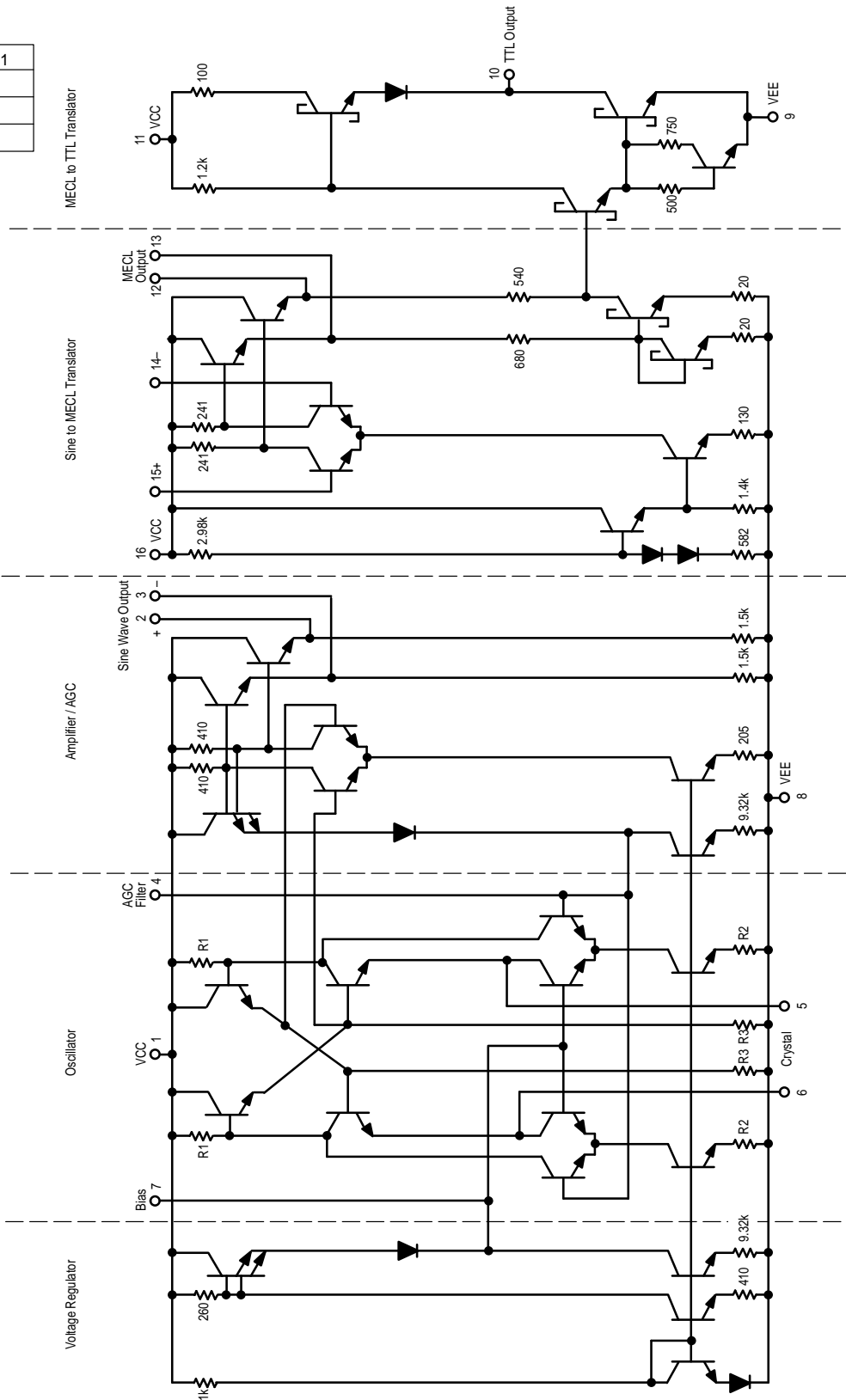
ANALYZER SETTING

| Measurement | Sweep | Bandwidth | Video Filter |
|----------------|------------|-----------|--------------|
| Noise Floor | 50 kHz/div | 10 kHz | 10 Hz |
| Close-In Noise | 20 kHz/div | 10 Hz | 10 Hz |

MC12061

Figure 13. Circuit Schematic

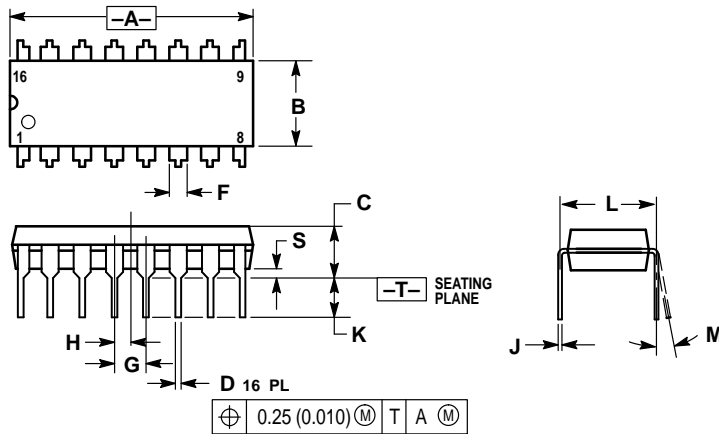
| RESISTOR | MC12061 |
|---------------|---------|
| R1 (2 Places) | 200 Ω |
| R2 (2 Places) | 400 Ω |
| R3 (2 Places) | 2 kΩ |



MC12061

OUTLINE DIMENSIONS


P SUFFIX
PLASTIC PACKAGE
CASE 648-08
ISSUE R



NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.
4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.
5. ROUNDED CORNERS OPTIONAL.

| DIM | INCHES | | MILLIMETERS | |
|-----|-----------|-------|-------------|-------|
| | MIN | MAX | MIN | MAX |
| A | 0.740 | 0.770 | 18.80 | 19.55 |
| B | 0.250 | 0.270 | 6.35 | 6.85 |
| C | 0.145 | 0.175 | 3.69 | 4.44 |
| D | 0.015 | 0.021 | 0.39 | 0.53 |
| F | 0.040 | 0.70 | 1.02 | 1.77 |
| G | 0.100 BSC | | 2.54 BSC | |
| H | 0.050 BSC | | 1.27 BSC | |
| J | 0.008 | 0.015 | 0.21 | 0.38 |
| K | 0.110 | 0.130 | 2.80 | 3.30 |
| L | 0.295 | 0.305 | 7.50 | 7.74 |
| M | 0° | 10° | 0° | 10° |
| S | 0.020 | 0.040 | 0.51 | 1.01 |

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters which may be provided in Motorola data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and  are registered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

Mfax is a trademark of Motorola, Inc.

How to reach us:

USA/EUROPE/Locations Not Listed: Motorola Literature Distribution;
P.O. Box 5405, Denver, Colorado 80217. 1-303-675-2140 or 1-800-441-2447

JAPAN: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 141,
4-32-1 Nishi-Gotanda, Shagawa-ku, Tokyo, Japan. 03-5487-8488

Customer Focus Center: 1-800-521-6274

Mfax™: RMFAX0@email.sps.mot.com – TOUCHTONE 1-602-244-6609
Motorola Fax Back System
– US & Canada ONLY 1-800-774-1848
– <http://sps.motorola.com/mfax/>

ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park,
51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298

HOME PAGE: <http://motorola.com/sps/>