

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π–MOSIII)

2SK2611

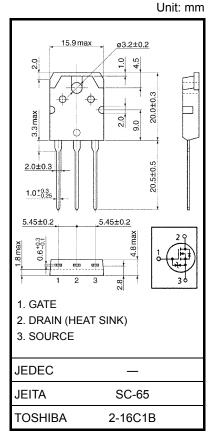
DC-DC Converter, Relay Drive and Motor Drive Applications

• Low drain–source ON resistance : RDS (ON) = 1.1 Ω (typ.) • High forward transfer admittance : $|Y_{fs}| = 7.0 \text{ S (typ.)}$ • Low leakage current : IDSS = 100 μ A (max) (VDS = 720 V)

• Enhancement-mode : $V_{th} = 2.0 \sim 4.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V_{DSS}	900	V	
Drain-gate voltage (R _{GS} = 20 kΩ)		V_{DGR}	900	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	ID	9	Α	
	Pulse (Note 1)	I _{DP}	27	Α	
Drain power dissipation	r (Tc = 25°C)	P _D	150	W	
Single pulse avalanche energy (Note 2)		E _{AS}	663	mJ	
Avalanche current		I _{AR}	9	Α	
Repetitive avalanche energy (Note 3)		E _{AR}	15	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 4.6 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	0.833	°C/W
Thermal resistance, channel to ambient	R _{th (ch-a)}	50	°C/W

Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 15 mH, R_G = 25 Ω , I_{AR} = 9 A

Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device.

Please handle with caution.



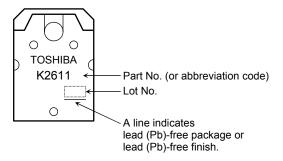
Electrical Characteristics (Ta = 25°C)

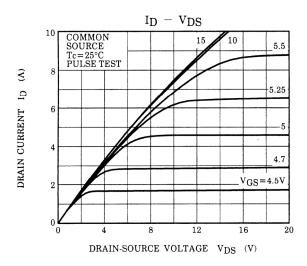
Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	ırrent	I _{GSS}	V _{GS} = ±30 V, V _{DS} = 0 V	_	_	±10	μA
Gate-source bro	eakdown voltage	V (BR) GSS	$I_G = \pm 10 \mu A, V_{DS} = 0 V$	±30	_	_	V
Drain cut-off cu	rrent	I _{DSS}	V _{DS} = 720 V, V _{GS} = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I _D = 10 mA, V _{GS} = 0 V	900	_	_	V
Gate threshold v	/oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	2.0	_	4.0	V
Drain-source O	N resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 4 A	_	1.1	1.4	Ω
Forward transfe	r admittance	Y _{fs}	V _{DS} = 15 V, I _D = 4 A	3.0	7.0	_	S
Input capacitano	ce	C _{iss}		_	2040	_	
Reverse transfe	Reverse transfer capacitance C_{rss} $V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		V _{DS} = 25 V, V _{GS} = 0 V, f = 1 MHz	_	45	_	pF
Output capacitance		Coss		_	190	_	
Switching time	Rise time	t _r	V_{GS} V_{OU} V_{OU} V_{OU} V_{OU} V_{OU} V_{OU} V_{OU} V_{OU}	_	25	_	
	Turn-on time	t _{on}		_	60	_	ns
	Fall time	t _f		l	20		115
	Turn-off time	t _{off}	Duty $\leq 1\%$, $t_W = 10 \mu s$		95	_	
Total gate charge (gate-source plus gate-drain)		Qg	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 9 A		58		nC
Gate-source charge		Q _{gs}		_	32	_	
Gate-drain ("miller") Charge		Q_{gd}			26	_	

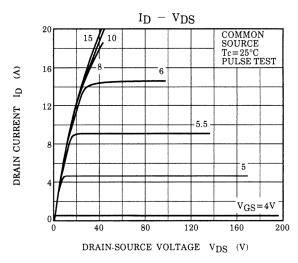
Source-Drain Ratings and Characteristics (Ta = 25°C)

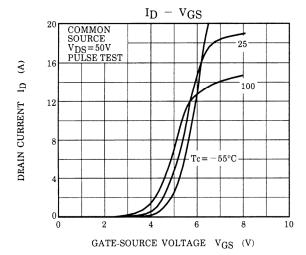
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	_	_	_	9	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	27	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 9 A, V _{GS} = 0 V	_	_	-1.9	V
Reverse recovery time	t _{rr}	I _{DR} = 9 A, V _{GS} = 0 V, dI _{DR} / dt = 100 A / μs	ı	1.6	1	μs
Reverse recovery charge	Qrr	1DR - 9 A, VGS - 0 V, αDR / αt - 100 A / μs		20	_	μC

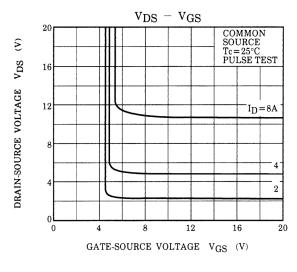
Marking

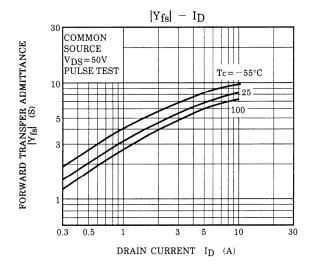


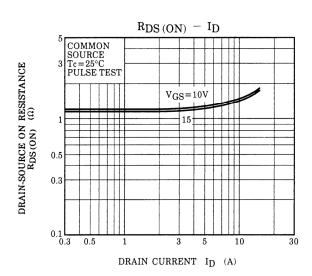




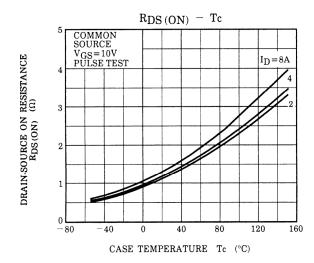


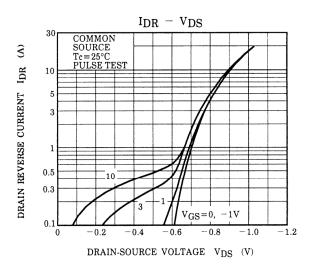


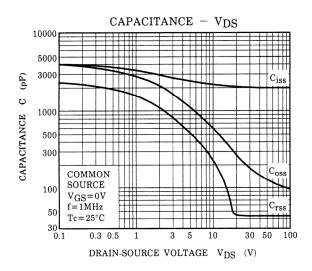


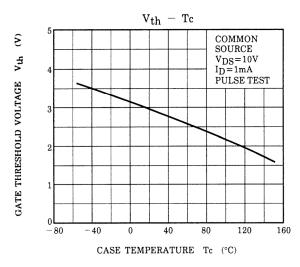


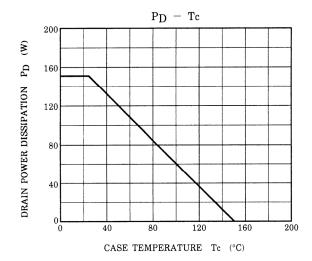


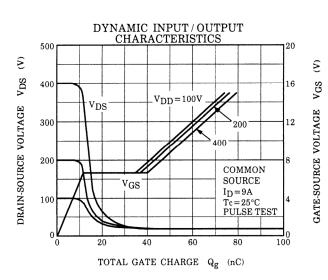


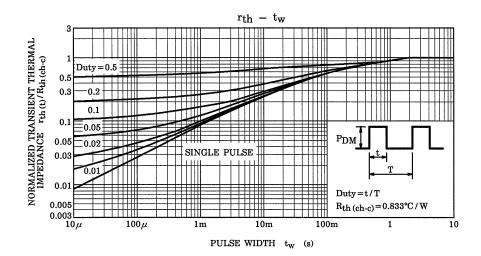


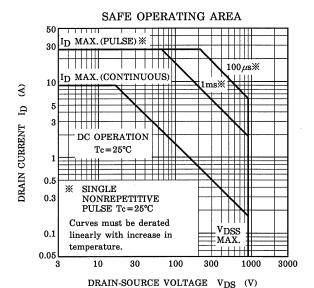


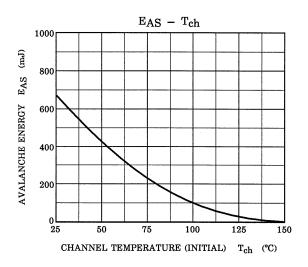


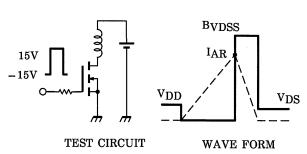












$$R_{G} = 25 \Omega$$

$$V_{DD} = 90 \text{ V, } L = 15 \text{ mH}$$

$$E_{AS} = \frac{1}{2} \cdot L \cdot I^{2} \cdot \left(\frac{BVDSS}{BVDSS - VDD}\right)$$

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20070701-EN

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