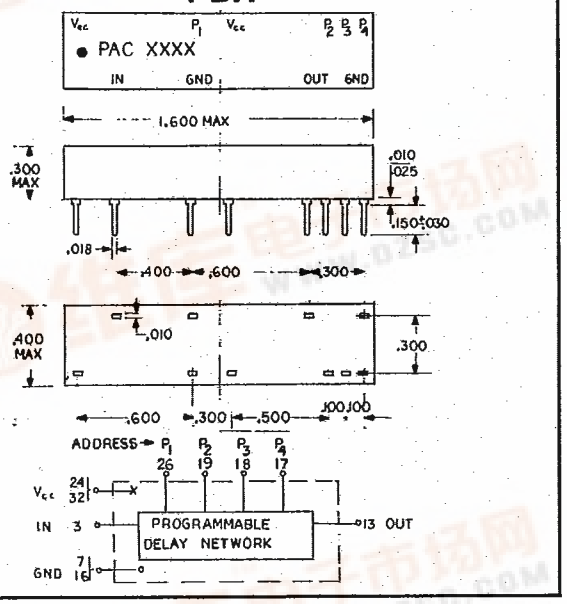
PRINCETON ADVANCED T-47-17

DELAY MODULES, TTL, PROGRAMMABLE

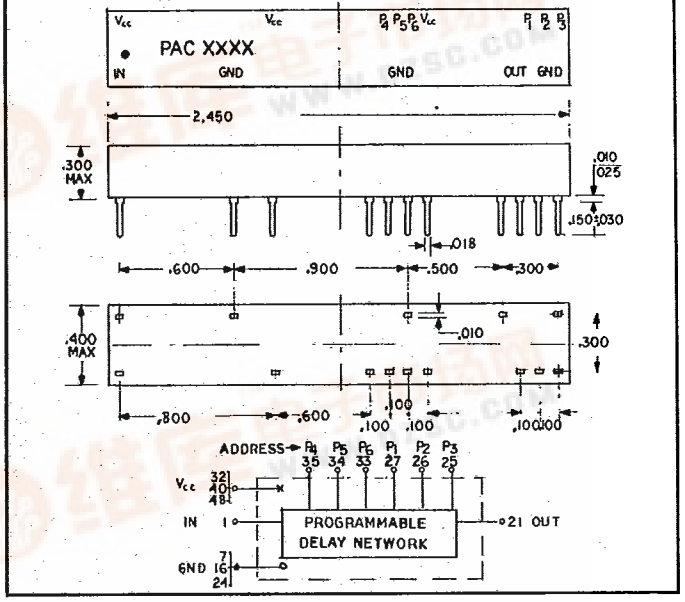
4 BIT and 6 BIT

4 BIT



Part Number	Incremental Delay Step ns	Programmable Delay Range Low ns ± 1	High ns ± 5%
4320	1 ± .3	14	30
4321	2 ± .4	14	45
4322	3 ± .4	14	60
4323	4 ± .5	14	75
4324	5 ± .5	14	90
4325	6 ± .6	14	105
4326	7 ± .7	14	120
4327	8 ± .8	14	135
4328	9 ± .9	14	150
4329	10 ± 1	14	165

6 BIT



Part Number	Incremental Delay Step ns	Programmable Delay Range Low ns ± 1	High ns ± 5%
4330	1 ± .3	14	77
4331	2 ± .4	14	140
4332	3 ± .4	14	203
4333	4 ± .5	14	266
4334	5 ± .5	14	329
4335	6 ± .6	14	392
4336	7 ± .7	14	455
4337	8 ± .8	14	518
4338	9 ± .9	14	581
4339	10 ± 1	14	644

OPERATING SPECIFICATIONS

Vcc supply voltage:	4.75 to 5.25 VDC
Logic 1 input:	
Voltage	2V min: 5.5V max.
Current	2.4V = 50ua max.
	5.5 V = 1 ma max.
Logic 0 input:	
Voltage	.8V max.
Current	−2 ma max.
Logic 1 Voltage out:	2.4V min.
Logic 0 Voltage out:	.4V max.
Operating temperature range:	0° to 70° C.
Storage temperature:	−55 to + 125°C.

TEST CONDITIONS

Input pulse width:	Minimum of 50% of Delay
Input pulse rise time:	3ns
Input pulse voltage:	3.2V
Vcc supply voltage:	5.0 VDC
Icc supply current:	80ma typ. - 4 BIT
	120ma typ. - 6 BIT

All measurements made at 25°C.

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