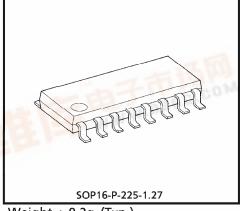
TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

# T A 8 5 1 7 F

### HIGH SPEED DUAL COMPARATOR

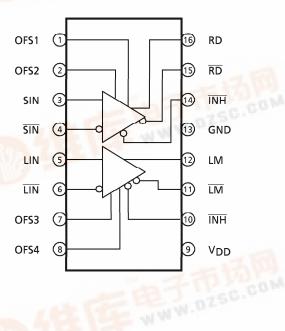
#### **FEATURES**

- Standard + 5V power supply TTL OUT
- TTL OUT
- FLP-16pin
- Inhibit function



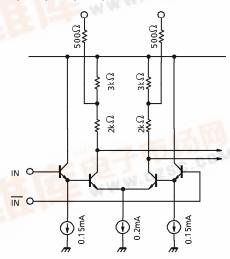
Weight: 0.2g (Typ.)

#### **BLOCK DIAGRAM & PIN CONNECTION (TOP VIEW)**



#### **EQUIVALENT CIRCUIT**

Off set control terminal (OFS1, 2, 3, 4), Input terminal (SIN, SIN, LIN, LIN)



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**TOSHIBA TA8517F** 

### **MAXIMUM RATINGS** (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	$V_{DD}$	7	V
Diffusion Input Voltage	$DV_IN$	±3	٧
Common Mode Input Voltage	CVIN	$-0.3 \sim V_{DD} + 0.3$	٧
Power Dissipation	PD	0.625	W
Operating Temperature	T <sub>opr</sub>	- 20~85	°C
Storage Temperature	$T_{stg}$	- 55~150	Ç
Inhibit Terminal	V <sub>ih</sub>	$-0.3 \sim V_{DD} + 0.3$	>

Recommended Operating Range :  $V_{DD}$  = 5V ± 10%, Ta = -20~70°C (\*) On Glass Epoxy PCD (20 × 20 × 1.6mm)

# **ELECTRICAL CHARACTERISTICS** ( $V_{DD} = 5V$ , Ta = 25°C Unless otherwise noted)

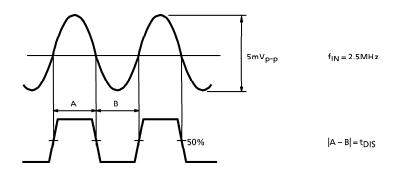
	• 00							
CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION	MIN.	TYP.	МАХ.	UNIT	
Power Supply Current	l <sub>DD</sub>	_	_	_	25	40	mA	
Input Sensitivity	VMIN	_		5	_	_	mV	
Input Off Set Voltage	V <sub>IO</sub>	_	OFS 1, 2, 3, 4 Terminal→OPEN	<b>–</b> 10	_	+ 10	mV	
Input Bias Current	I <sub>IB</sub>	_	<del>_</del>	_	_	6	$\mu$ A	
Input Offset Current	lιο	_	<del>_</del>	_	_	3	$\mu$ A	
Input Resistance	R <sub>I</sub>	_	_	10	_	_	kΩ	
Input Capacitance	Cl	_	_	_	_	3	рF	
Common Mode Input Voltage Range	cv <sub>IN</sub>	_	_	2.0	_	4.3	٧	
Open-Loop Gain	$G_V$	_	_	70	_	_	dB	
Output Voltage	VOH	_	$V_{DD} = 4.5V$ , $I_{OH} = 1mA$	2.4	_	_	V	
	$V_{OL}$		$V_{DD} = 4.5V$ , $I_{OL} = 10mA$		_	0.5	\ \ \	
Inhibit Terminal Input	$V_{IH}$	_	_	2.0	_	_	l <sub>v</sub> l	
Voltage	$V_{IL}$		_	_	_	0.8	·	
Inhibit Propagation Delay	<sup>t</sup> ih	_	_	_	_	30	$\mu$ s	
Propagation Delay	$t_{pLH}$	_	(Note 2)	_	11	20	ns	
	$t_{pHL}$	_	(Note 2)	_	10	22		
Rise Time	t <sub>r</sub>	_	(Note 2)	_	4	_	ns	
Fall Time	t <sub>f</sub>	_	(Note 2)	_	2	_	ns	
Time Distortion	t <sub>DIS</sub>	_	(Note 1) $V_{IN} = 5mV_{p-p}$ , $f_{IN} = 2.5MHz$	_	2	_	ns	

#### **INHIBIT FUNCTION**

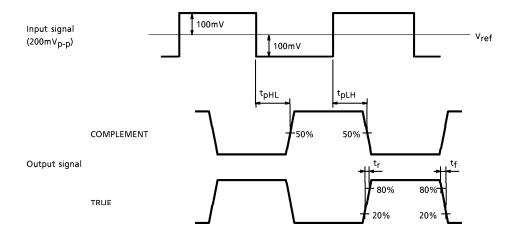
OUTPUT TERMINAL	OUTPUT FUNCTION			
INH TERMINAL	RD, LM	RD, LM		
High	Active	Active		
Low	High	High		

TOSHIBA TA8517F

## (Note 1)



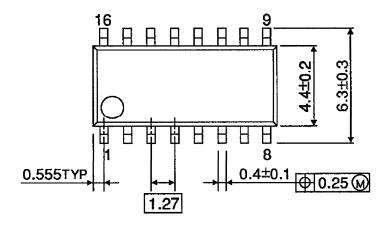
# (Note 2)

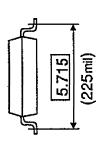


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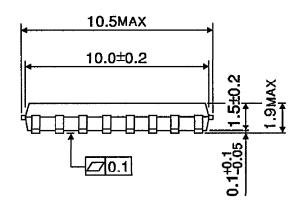
### **OUTLINE DRAWING**

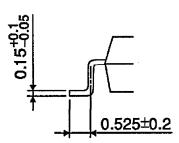
SOP16-P-225-1.27





Unit: mm





Weight: 0.2g (Typ.)