

TOSHIBA**TC74AC574P/F/FW/FT**

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TC74AC574P, TC74AC574F, TC74AC574FW, TC74AC574FT**OCTAL D - TYPE FLIP - FLOP WITH 3 - STATE OUTPUT**

The TC74AC574 is an advanced high speed CMOS OCTAL FLIP - FLOP fabricated with silicon gate and double - layer metal wiring C²MOS technology.

It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation.

These 8 - bit D - type flip - flops are controlled by a clock input (CK) and a output enable input (\overline{OE}).

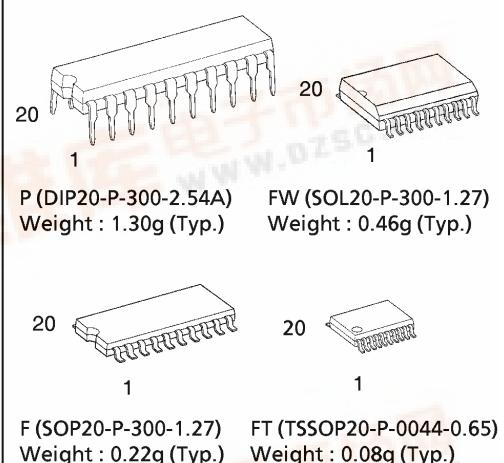
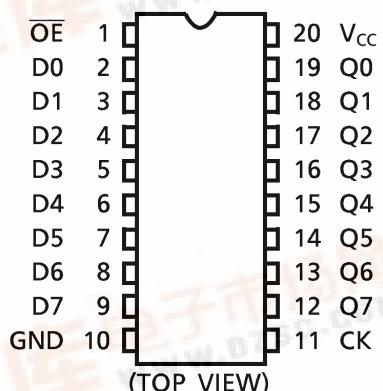
When the \overline{OE} input is high, the eight outputs are in a high impedance state.

All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES :

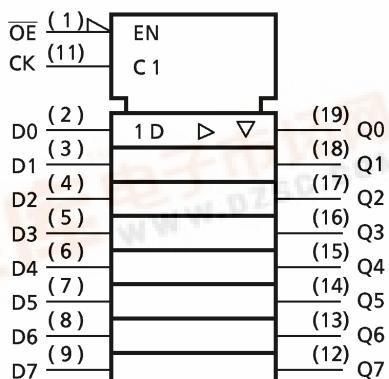
- High Speed..... $f_{MAX} = 180MHz$ (typ.) at $V_{CC} = 5V$
- Low Power Dissipation..... $I_{CC} = 8\mu A$ (Max.) at $T_a = 25^{\circ}C$
- High Noise Immunity..... $V_{NIH} = V_{NIL} = 28\% V_{CC}$ (Min.)
- Symmetrical Output Impedance..... $|I_{OH}| = I_{OL} = 24mA$ (Min.) Capability of driving 50Ω transmission lines.
- Balanced Propagation Delays..... $t_{PLH} \approx t_{PHL}$
- Wide Operating Voltage Range..... V_{CC} (opr) = 2V ~ 5.5V
- Pin and Function Compatible with 74F574

(Note) The JEDEC SOP (FW) is not available in Japan.

**PIN ASSIGNMENT****TRUTH TABLE**

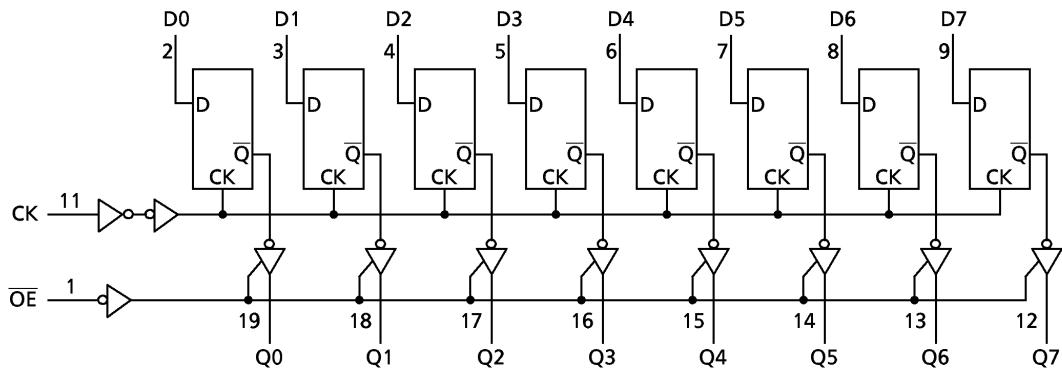
| INPUTS | | | OUTPUTS |
|-----------------|----|---|---------|
| \overline{OE} | CK | D | Q |
| H | X | X | Z |
| L | — | X | Q_n |
| L | — | L | L |
| L | — | H | H |

X : Don't Care
Z : High Impedance
 Q_n : No Change

IEC LOGIC SYMBOL

● TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.

SYSTEM DIAGRAM



ABSOLUTE MAXIMUM RATINGS

| PARAMETER | SYMBOL | VALUE | UNIT |
|-----------------------------|-----------|-----------------------------|------|
| Supply Voltage Range | V_{CC} | -0.5~7.0 | V |
| DC Input Voltage | V_{IN} | -0.5~ $V_{CC} + 0.5$ | V |
| DC Output Voltage | V_{OUT} | -0.5~ $V_{CC} + 0.5$ | V |
| Input Diode Current | I_{IK} | ± 20 | mA |
| Output Diode Current | I_{OK} | ± 50 | mA |
| DC Output Current | I_{OUT} | ± 50 | mA |
| DC V_{CC} /Ground Current | I_{CC} | ± 200 | mA |
| Power Dissipation | P_D | 500 (DIP)*/ 180 (SOP/TSSOP) | mW |
| Storage Temperature | T_{stg} | -65~150 | °C |

*500mW in the range of $T_a = -40^{\circ}\text{C} \sim 65^{\circ}\text{C}$. From $T_a = 65^{\circ}\text{C}$ to 85°C a derating factor of $-10\text{mW}/^{\circ}\text{C}$ should be applied up to 300mW.

RECOMMENDED OPERATING CONDITIONS

| PARAMETER | SYMBOL | VALUE | UNIT |
|--------------------------|-----------|---|------|
| Supply Voltage | V_{CC} | 2.0~5.5 | V |
| Input Voltage | V_{IN} | 0~ V_{CC} | V |
| Output Voltage | V_{OUT} | 0~ V_{CC} | V |
| Operating Temperature | T_{opr} | -40~85 | °C |
| Input Rise and Fall Time | dt/dV | 0~ 100 ($V_{CC} = 3.3 \pm 0.3\text{V}$) 0~ 20 ($V_{CC} = 5 \pm 0.5\text{V}$) | ns/V |

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- The information contained herein is subject to change without notice.

DC ELECTRICAL CHARACTERISTICS

| PARAMETER | SYMBOL | TEST CONDITION | V _{CC} (V) | Ta = 25°C | | | Ta = -40~85°C | | UNIT |
|--------------------------------------|-----------------|---|---|----------------------|-------------------|----------------------|----------------------|----------------------|----------------------|
| | | | | MIN. | TYP. | MAX. | MIN. | MAX. | |
| High - Level Input Voltage | V _{IH} | | 2.0 3.0 5.5 | 1.50 2.10 3.85 | — — — | — — — | 1.50 2.10 3.85 | — — — | V |
| Low - Level Input Voltage | V _{IL} | | 2.0 3.0 5.5 | — — — | — — — | 0.50 0.90 1.65 | — — — | 0.50 0.90 1.65 | V |
| High - Level Output Voltage | V _{OH} | V _{IN} = V _{IH} or V _{IL} | I _{OH} = -50μA | 2.0 3.0 4.5 | 1.9 2.9 4.4 | 2.0 3.0 4.5 | — — — | 1.9 2.9 4.4 | — — — |
| | | | I _{OH} = -4mA I _{OH} = -24mA I _{OH} = -75mA* | 3.0 4.5 5.5 | 2.58 3.94 — | — — — | — — — | 2.48 3.80 3.85 | — — — |
| Low - Level Output Voltage | V _{OL} | V _{IN} = V _{IH} or V _{IL} | I _{OL} = 50μA | 2.0 3.0 4.5 | — — — | 0.0 0.0 0.0 | 0.1 0.1 0.1 | — — — | 0.1 0.1 0.1 |
| | | | I _{OL} = 12mA I _{OL} = 24mA I _{OL} = 75mA* | 3.0 4.5 5.5 | — — — | — — — | 0.36 0.36 — | — — — | 0.44 0.44 1.65 |
| 3 - State Output Off - State Current | I _{OZ} | V _{IN} = V _{IH} or V _{IL} V _{OUT} = V _{CC} or GND | 5.5 | — | — | ± 0.5 | — | ± 5.0 | μA |
| Input Leakage Current | I _{IN} | V _{IN} = V _{CC} or GND | 5.5 | — | — | ± 0.1 | — | ± 1.0 | |
| Quiescent Supply Current | I _{CC} | V _{IN} = V _{CC} or GND | 5.5 | — | — | 8.0 | — | 80.0 | |

* : This spec indicates the capability of driving 50Ω transmission lines.

One output should be tested at a time for a 10ms maximum duration.

TIMING REQUIREMENTS (Input t_r = t_f = 3ns)

| PARAMETER | SYMBOL | TEST CONDITION | V _{CC} (V) | Ta = 25°C | | Ta = -40~85°C | | UNIT |
|----------------------------|--|----------------|------------------------|------------|-------|---------------|-------|------|
| | | | | LIMIT | LIMIT | LIMIT | LIMIT | |
| Minimum Pulse Width (CK) | t _W (H) t _W (L) | | 3.3 ± 0.3 5.0 ± 0.5 | 7.0 5.0 | | 7.0 5.0 | | ns |
| | | | | | | | | |
| Minimum Set - up Time | t _s | | 3.3 ± 0.3 5.0 ± 0.5 | 9.0 4.5 | | 9.0 4.5 | | |
| | | | | | | | | |
| Minimum Hold Time | t _h | | 3.3 ± 0.3 5.0 ± 0.5 | 1.0 1.0 | | 1.0 1.0 | | |
| | | | | | | | | |

AC ELECTRICAL CHARACTERISTICS ($C_L = 50\text{pF}$, $R_L = 500\Omega$, Input $t_r = t_f = 3\text{ns}$)

| PARAMETER | SYMBOL | TEST CONDITION | Ta = 25°C | | | Ta = -40~85°C | | UNIT |
|----------------------------------|------------------------|----------------|--------------------------------|----------|------------|---------------|------------|--------------|
| | | | V _{CC} (V) | MIN. | TYP. | MAX. | MIN. | |
| Propagation Delay Time (CK-Q) | t_{pLH} t_{pHL} | | 3.3 ± 0.3 5.0 ± 0.5 | — — | 9.8 6.1 | 16.7 9.2 | 1.0 1.0 | 19.0 10.5 |
| Output Enable Time | t_{pZL} t_{pZH} | | 3.3 ± 0.3 5.0 ± 0.5 | — — | 9.2 6.1 | 15.8 9.3 | 1.0 1.0 | 18.0 10.6 |
| Output Disable Time | t_{pLZ} t_{pHZ} | | 3.3 ± 0.3 5.0 ± 0.5 | — — | 6.6 5.8 | 11.0 8.8 | 1.0 1.0 | 12.5 10.0 |
| Maximum Clock Frequency | f _{MAX} | | 3.3 ± 0.3 5.0 ± 0.5 | 50 95 | 100 160 | — | 50 95 | — |
| Input Capacitance | C _{IN} | | | — | 5 | 10 | — | 10 |
| Output Capacitance | C _{OUT} | | | — | 10 | — | — | — |
| Power Dissipation Capacitance | C _{PD} (1) | | | — | 36 | — | — | — |

Note(1) C_{PD} is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation :

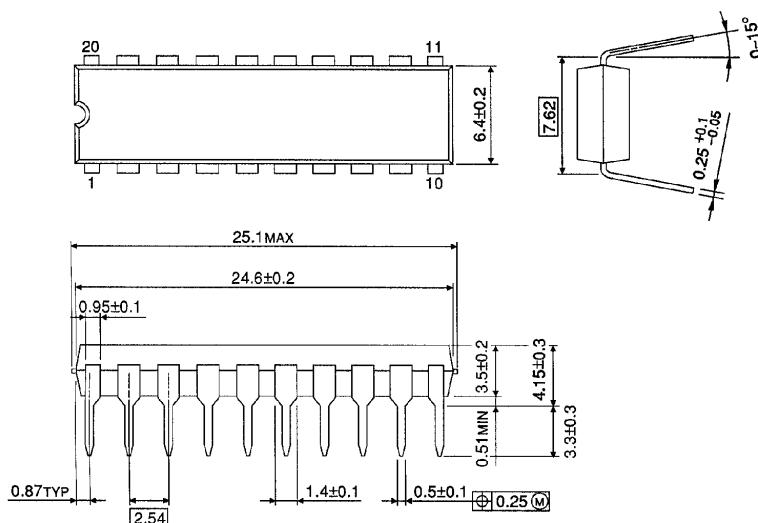
$$I_{CC}(\text{opr.}) = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/8 \text{ (per F/F)}$$

And the total C_{PD} when n pcs. of Latch operate can be gained by the following equation :

$$C_{PD}(\text{total}) = 26 + 10 \cdot n$$

DIP 20PIN OUTLINE DRAWING (DIP20-P-300-2.54A)

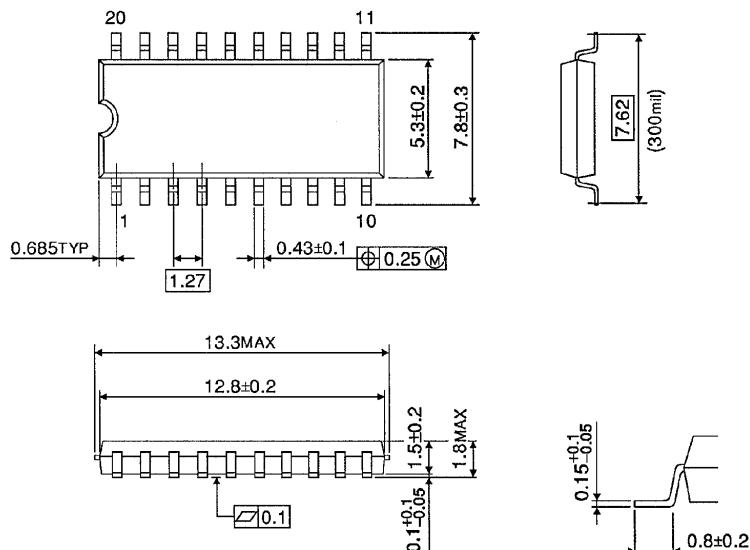
Unit in mm



Weight : 1.30g (Typ.)

SOP 20PIN (200mil BODY) OUTLINE DRAWING (SOP20-P-300-1.27)

Unit in mm

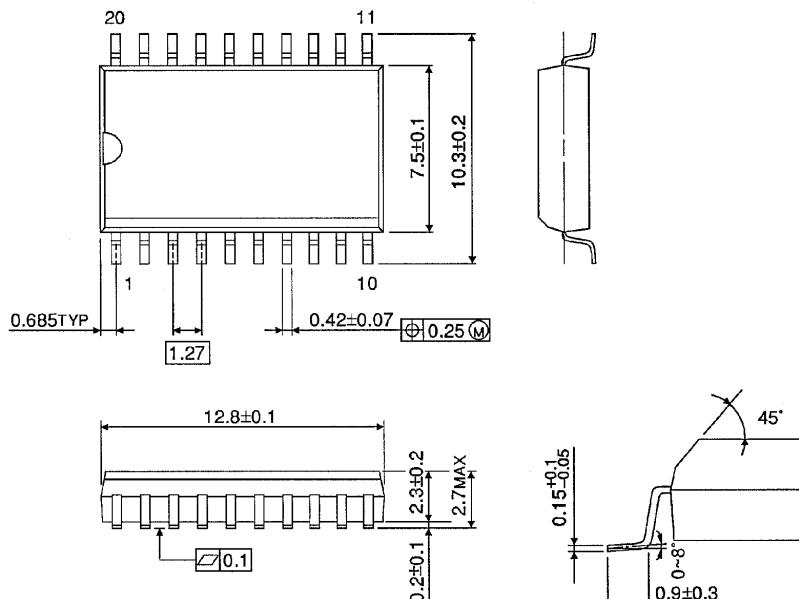


Weight : 0.22g (Typ.)

SOP 20PIN (300mil BODY) OUTLINE DRAWING (SOL20-P-300-1.27)

Unit in mm

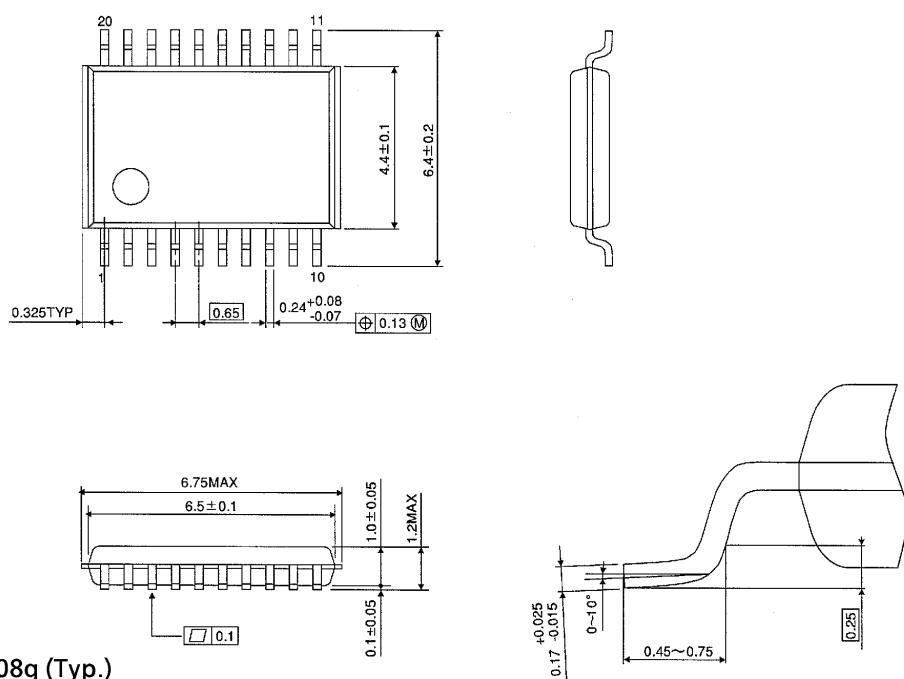
(Note) This package is not available in Japan.



Weight : 0.46g (Typ.)

TSSOP 20PIN OUTLINE DRAWING (TSSOP20-P-0044-0.65)

Unit in mm



Weight : 0.08g (Typ.)