TOSHIBA

MT6L58AT

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

MT6L58AT

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

Unit in mm

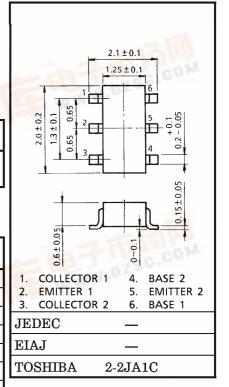
TWO devices are built in to the super-thin and ultra super mini (6 pins) package: TU6

MOUNTED DEVICES

| | Q1 : SSM (TESM) | Q2 : SSM (TESM) |
|----------------------------|-----------------|-----------------|
| Three-pins (SSM/TESM) mold | MT3S06S | MT3S03AS |
| products are corresponded. | (MT3S06T) | (MT3S03AT) |

MAXIMUM RATINGS (Ta = 25°C)

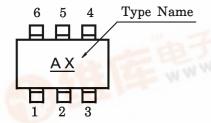
| CHARACTERISTIC | SYMBOL | Q1 | Q2 | UNIT |
|------------------------------|--------------------|---------|----|------|
| Collector-Base Voltage | v_{CBO} | 10 | 10 | V |
| Collector-Emitter Voltage | v_{CEO} | 5 | 5 | V |
| Emitter-Base Voltage | $V_{ m EBO}$ | 1.5 | 2 | V |
| Collector Current | $I_{\mathbf{C}}$ | 15 | 40 | mA |
| Base Current | IB | 7 | 10 | mA |
| Collector Power Dissipation | $P_{\mathbf{C}}$ | 150 | | mW |
| Junction Temperature | T_{j} | 15 | °C | |
| Storage Temperature Range | $ m T_{stg}$ | -55~125 | | °C |

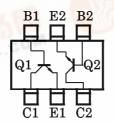


MARKING

df.dzsc.com

PIN ASSIGNMENT (TOP VIEW)





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ELECTRICAL CHARACTERISTICS Q1 (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|---------------------------------|-------------------|----------------------------------------------------------------------------------------------------------------------------|------|------|------|---------|
| Collector Cut-off Current | $I_{ m CBO}$ | $V_{CB} = 5 V, I_{E} = 0$ | _ | _ | 0.1 | μ A |
| Emitter Cut-off Current | ${ m I}_{ m EBO}$ | $V_{EB} = 1 V, I_C = 0$ | _ | _ | 1 | μ A |
| DC Current Gain | ${ m h_{FE}}$ | $V_{CE} = 1 V$, $I_{C} = 5 mA$ | 70 | _ | 140 | |
| Transition Frequency | ${ m f_T}$ | $ m V_{CE} = 3~V,~I_{C} = 5~mA$ | 7 | 10 | _ | GHz |
| Insertion Gain | $ S_{21e} ^2$ (1) | $egin{aligned} \mathrm{V_{CE}} &= 1 \mathrm{V, \ I_{C}} = 5 \mathrm{mA,} \ \mathrm{f} &= 2 \mathrm{GHz} \end{aligned}$ | | 7.5 | _ | dB |
| | $ S_{21e} ^2$ (2) | $egin{aligned} \mathrm{V_{CE}} &= 3 \mathrm{V, \ I_{C}} = 7 \mathrm{mA,} \\ \mathrm{f} &= 2 \mathrm{GHz} \end{aligned}$ | 4.5 | 8 | _ | dB |
| Noise Figure | NF (1) | $egin{aligned} \mathrm{V_{CE}} &= 1 \mathrm{V, \ I_{C}} = 3 \mathrm{mA,} \ \mathrm{f} &= 2 \mathrm{GHz} \end{aligned}$ | _ | 1.7 | 3 | dB |
| | NF (2) | $egin{aligned} \mathrm{V_{CE}} = 3 \mathrm{V, \ I_{C}} = 3 \mathrm{mA,} \ \mathrm{f} = 2 \mathrm{GHz} \end{aligned}$ | _ | 1.6 | 3 | dB |
| Reverse Transfer Capacitance | $\mathrm{C_{re}}$ | $egin{aligned} V_{\mathrm{CB}} &= 1 \mathrm{V, \ I_E} = 0, \\ \mathrm{f} &= 1 \mathrm{MHz \ (Note)} \end{aligned}$ | _ | 0.35 | 0.75 | pF |

ELECTRICAL CHARACTERISTICS Q2 (Ta = 25°C)

| CHARACTERISTIC | SYMBOL | TEST CONDITION | MIN. | TYP. | MAX. | UNIT |
|---------------------------------|--------------------|-----------------------------------------------------------------------------------------------------------------|------|------|------|----------------|
| Collector Cut-off Current | I_{CBO} | $V_{CB} = 5 \text{ V}, I_{E} = 0$ | _ | _ | 0.1 | μ A |
| Emitter Cut-off Current | $I_{ m EBO}$ | $V_{EB} = 1 V, I_{C} = 0$ | _ | _ | 1 | μ A |
| DC Current Gain | ${ m h_{FE}}$ | $V_{CE} = 1 V$, $I_{C} = 5 mA$ | 80 | _ | 160 | _ |
| Transition Frequency | f _T (1) | $ m V_{CE} = 1 V, I_{C} = 5 mA$ | 3 | 5 | _ | GHz |
| | f _T (2) | $ m V_{CE} = 3~V,~I_{C} = 10~mA$ | 7 | 10 | _ | GHz |
| Insertion Gain | $ S_{21e} ^2$ (1) | $ m V_{CE}=1~V,~I_{C}=5~mA,~f=2~GHz$ | _ | 5 | _ | dB |
| | $ S_{21e} ^2$ (2) | $V_{CE} = 3 \text{ V}, I_{C} = 20 \text{ mA}, f = 2 \text{ GHz}$ | 3 | 6.5 | _ | dB |
| I Noise Kigure — | NF (1) | $V_{CE} = 1 \text{ V}, I_{C} = 5 \text{ mA}, f = 2 \text{ GHz}$ | _ | 1.7 | 3 | dB |
| | NF (2) | $ m V_{CE}=3~V,~I_{C}=7~mA,~f=2~GHz$ | _ | 1.4 | 2.2 | dB |
| Reverse Transfer Capacitance | $\mathrm{C_{re}}$ | $egin{aligned} V_{	ext{CB}} &= 1 	ext{V}, I_{	ext{E}} &= 0, \ f &= 1 	ext{MHz} (ext{Note}) \end{aligned}$ | _ | 0.8 | 1.15 | pF |

(Note): C_{re} is measured by 3 terminal method with capacitance bridge.

HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.