

**TOSHIBA**

**MT6L50AT**

TOSHIBA TRANSISTOR SILICON NPN EPITAXIAL PLANAR TYPE

# MT6L50AT

VHF~UHF BAND LOW NOISE AMPLIFIER APPLICATIONS

Unit in mm

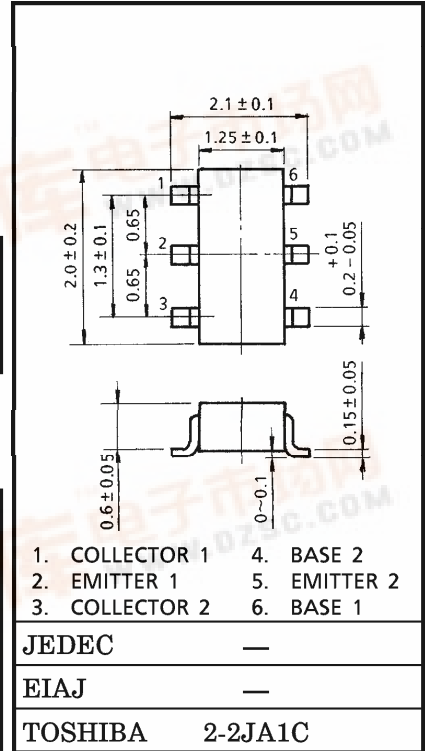
- TWO devices are built in to the super-thin and ultra super mini (6 pins) package : TU6

**MOUNTED DEVICES**

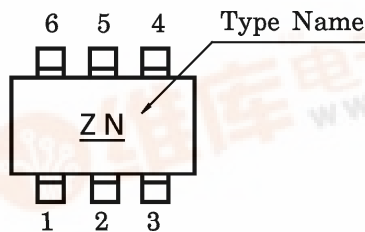
	Q1 : SSM (TESM)	Q2 : SSM (TESM)
Three-pins (SSM/TESM) mold products are corresponded.	2SC5256 (5256FT)	MT3S04AS (MT3S04AT)

**MAXIMUM RATINGS (Ta = 25°C)**

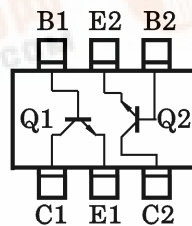
CHARACTERISTIC	SYMBOL	Q1	Q2	UNIT
Collector-Base Voltage	V <sub>CBO</sub>	15	10	V
Collector-Emitter Voltage	V <sub>CEO</sub>	7	5	V
Emitter-Base Voltage	V <sub>EBO</sub>	1.5	2	V
Collector Current	I <sub>C</sub>	40	40	mA
Base Current	I <sub>B</sub>	20	10	mA
Collector Power Dissipation	P <sub>C</sub>	200		mW
Junction Temperature	T <sub>j</sub>	125		°C
Storage Temperature Range	T <sub>stg</sub>	-55~125		°C



**MARKING**



**PIN ASSIGNMENT (TOP VIEW)**



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## ELECTRICAL CHARACTERISTICS Q1 (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	$I_{CBO}$	$V_{CB} = 10\text{ V}, I_E = 0$	—	—	1	$\mu\text{A}$
Emitter Cut-off Current	$I_{EBO}$	$V_{EB} = 1\text{ V}, I_C = 0$	—	—	1	$\mu\text{A}$
DC Current Gain	$h_{FE}$	$V_{CE} = 5\text{ V}, I_C = 20\text{ mA}$	50	—	160	—
Transition Frequency	$f_T$	$V_{CE} = 5\text{ V}, I_C = 20\text{ mA}$	10	12	—	GHz
Insertion Gain	$ S_{21e} ^2$	$V_{CE} = 5\text{ V}, I_C = 20\text{ mA},$ $f = 2000\text{ MHz}$	5	7.8	—	dB
Noise Figure	NF	$V_{CE} = 5\text{ V}, I_C = 5\text{ mA},$ $f = 2000\text{ MHz}$	—	1.5	3	dB
Reverse Transfer Capacitance	$C_{re}$	$V_{CB} = 5\text{ V}, I_E = 0,$ $f = 1\text{ MHz (Note)}$	—	0.5	0.95	pF

## ELECTRICAL CHARACTERISTICS Q2 (Ta = 25°C)

CHARACTERISTIC	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Collector Cut-off Current	$I_{CBO}$	$V_{CB} = 5\text{ V}, I_E = 0$	—	—	0.1	$\mu\text{A}$
Emitter Cut-off Current	$I_{EBO}$	$V_{EB} = 1\text{ V}, I_C = 0$	—	—	1	$\mu\text{A}$
DC Current Gain	$h_{FE}$	$V_{CE} = 1\text{ V}, I_C = 5\text{ mA}$	80	—	160	—
Transition Frequency	$f_T$ (1)	$V_{CE} = 1\text{ V}, I_C = 5\text{ mA}$	2	4.5	—	GHz
	$f_T$ (2)	$V_{CE} = 3\text{ V}, I_C = 7\text{ mA}$	5	7	—	GHz
Insertion Gain	$ S_{21e} ^2$ (1)	$V_{CE} = 1\text{ V}, I_C = 5\text{ mA}, f = 1\text{ GHz}$	—	8.5	—	dB
	$ S_{21e} ^2$ (2)	$V_{CE} = 3\text{ V}, I_C = 20\text{ mA}, f = 1\text{ GHz}$	7.5	11	—	dB
Noise Figure	NF (1)	$V_{CE} = 1\text{ V}, I_C = 5\text{ mA}, f = 1\text{ GHz}$	—	1.3	2.2	dB
	NF (2)	$V_{CE} = 3\text{ V}, I_C = 7\text{ mA}, f = 1\text{ GHz}$	—	1.2	2	dB
Reverse Transfer Capacitance	$C_{re}$	$V_{CB} = 1\text{ V}, I_E = 0,$ $f = 1\text{ MHz (Note)}$	—	0.9	1.25	pF

(Note) :  $C_{re}$  is measured by 3 terminal method with capacitance bridge.

## HANDLING PRECAUTION

When handling individual devices (which are not yet mounted on a circuit board), be sure that the environment is protected against electrostatic electricity. Operators should wear anti-static clothing, and containers and other objects that come into direct contact with devices should be made of anti-static materials.