# **Silicon Controlled Rectifiers**

### **Reverse Blocking Triode Thyristors**

... designed primarily for half-wave ac control applications, such as motor controls, heating controls and power supplies.

- Glass Passivated Junctions with Center Gate Geometry for Greater Parameter Uniformity and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability

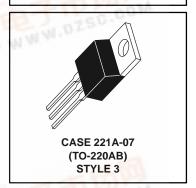
  Blocking Voltage to 800 Volts

## 2N6394 thru 2N6399

Motorola preferred devices

**SCRs** 12 AMPERES RMS 50 thru 800 VOLTS





#### \*MAXIMUM RATINGS (T.J = 25°C unless otherwise noted.)

Rat	ing	Symbol	Value	Unit
Peak Repetitive Forward and Reverse Blo (Gate Open, T <sub>J</sub> = -40 to 125°C)	cking Voltage(1) 2N6394 2N6395 2N6397 2N6398 2N6399	V <sub>DRM</sub> , V <sub>RRM</sub>	50 100 400 600 800	Volts
RMS On-State Current (T <sub>C</sub> = 90°C) (All Conduction Angles)		IT(RMS)	12	Amps
Peak Non-Repetitive Surge Current (1/2 Cycle, Sine Wave, 60 Hz, T <sub>J</sub> = 125	°C)	ITSM	100	Amps
Circuit Fusing (t = 8.3 ms)		l <sup>2</sup> t	40	A <sup>2</sup> s
Forward Peak Power		PGM	20	Watts
Forward Average Gate Power		P <sub>G(AV)</sub>	0.5	Watt
Forward Peak Gate Current	- Fil 6	I <sub>GM</sub>	2	Amps
Operating Junction Temperature Range	五切門 电	TJ	-40 to +125	°C
Storage Temperature Range	-48C.COm	T <sub>stg</sub>	-40 to +150	°C

### THERMAL CHARACTERISTICS

	Characteristic	Symbol	Max	Unit
I	Thermal Resistance, Junction to Case	$R_{ heta JC}$	2	°C/W

<sup>\*</sup>Indicates JEDEC Registered Data.

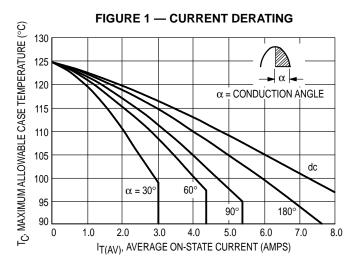
 $<sup>1.\ \</sup> V_{DRM}\ and\ V_{RRM}\ for\ all\ types\ can\ be\ applied\ on\ a\ continuous\ basis.\ Ratings\ apply\ for\ zero\ or\ negative\ gate\ voltage;\ however,\ positive\ gate\ properties and\ properties apply\ for\ properties apply\ for\ properties apply\ for\ properties\ propertie$ voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.

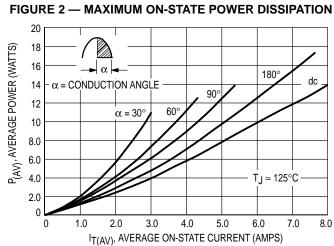
#### 2N6394 thru 2N6399

### **ELECTRICAL CHARACTERISTICS** ( $T_C = 25^{\circ}C$ unless otherwise noted.)

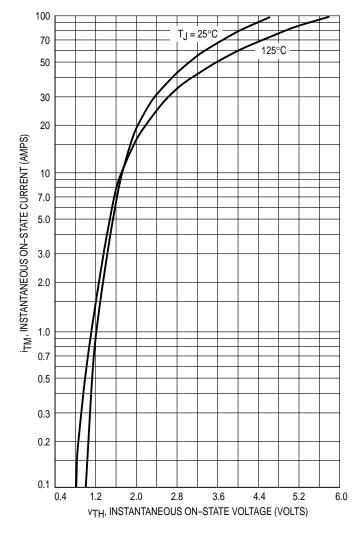
Characteristic	Symbol	Min	Тур	Max	Unit
* Peak Repetitive Forward or Reverse Blocking Current ( $V_{AK}$ = Rated $V_{DRM}$ or $V_{RRM}$ , Gate Open) $T_J = 25^{\circ}C$ $T_J = 125^{\circ}C$	IDRM, IRRM	_	_	10 2	μA mA
* Forward "On" Voltage (I <sub>TM</sub> = 24 A Peak)	VTM	_	1.7	2.2	Volts
*Gate Trigger Current (Continuous dc) (V <sub>D</sub> = 12 Vdc, R <sub>L</sub> = 100 Ohms)	lGТ	_	5	30	mA
*Gate Trigger Voltage (Continuous dc) (V <sub>D</sub> = 12 Vdc, R <sub>L</sub> = 100 Ohms) (V <sub>D</sub> = Rated V <sub>DRM</sub> , R <sub>L</sub> = 100 Ohms, T <sub>J</sub> = 125°C)	V <sub>GT</sub> V <sub>GD</sub>	_ 0.2	0.7 —	1.5 —	Volts
*Holding Current (V <sub>D</sub> = 12 Vdc, Gate Open)	lн		6	40	mA
Turn-On Time $(I_{TM} = 12 \text{ A}, I_{GT} = 40 \text{ mAdc}, V_D = \text{Rated V}_{DRM})$	tgt	_	1	2	μs
Turn-Off Time ( $V_D$ = Rated $V_{DRM}$ ) ( $I_{TM}$ = 12 A, $I_R$ = 12 A) ( $I_{TM}$ = 12 A, $I_R$ = 12 A, $T_J$ = 125°C)	tq	_	15 35	_	μs
Critical Rate–of–Rise of Off-State Voltage Exponential (V <sub>D</sub> = Rated V <sub>DRM</sub> , T <sub>J</sub> = 125°C)	dv/dt	_	50	_	V/µs

<sup>\*</sup>Indicates JEDEC Registered Data.

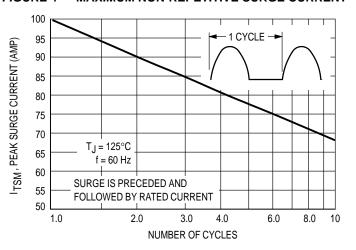




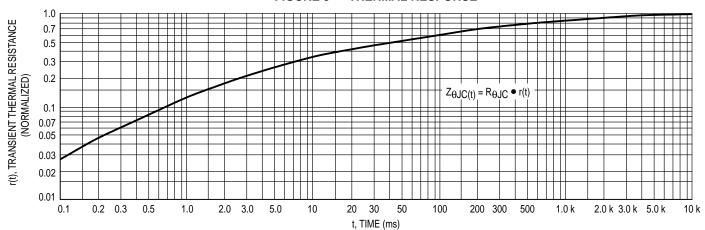




#### FIGURE 4 — MAXIMUM NON-REPETITIVE SURGE CURRENT



#### FIGURE 5 — THERMAL RESPONSE



#### **TYPICAL CHARACTERISTICS**

FIGURE 6 — PULSE TRIGGER CURRENT

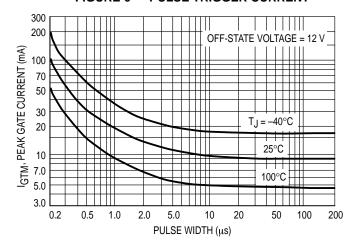


FIGURE 7 — GATE TRIGGER CURRENT

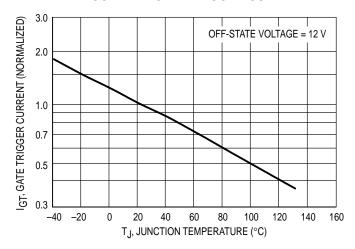
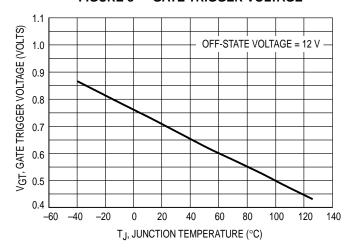
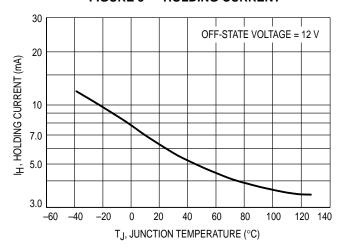


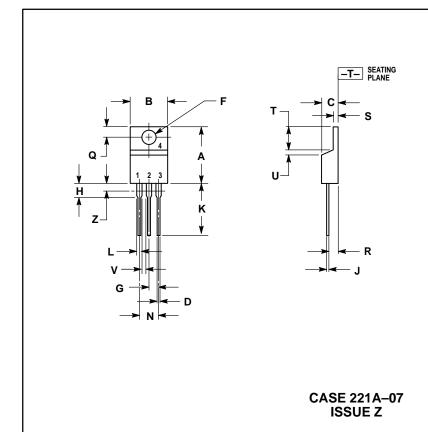
FIGURE 8 — GATE TRIGGER VOLTAGE



#### FIGURE 9 — HOLDING CURRENT



#### **PACKAGE DIMENSIONS**



- NOTES:
  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES MILLIMETER			IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.147	3.61	3.73
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.014	0.022	0.36	0.55
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
Т	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

STYLE 3:
PIN 1. CATHODE
2. ANODE
3. GATE
4. ANODE

#### 2N6394 thru 2N6399

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