Surface Mount Schottky Power Rectifier

... employing the Schottky Barrier principle in a large area metal-to-silicon power diode. State-of-the-art geometry features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency rectification, or as free wheeling and polarity protection diodes in surface mount applications where compact size and weight are critical to the system.

- Small Compact Surface Mountable Package with J–Bend Leads
- Rectangular Package for Automated Handling
- Highly Stable Oxide Passivated Junction
- Very Low Forward Voltage Drop (0.55 Volts Max @ 1.0 A, T_J = 25°C)
- Excellent Ability to Withstand Reverse Avalanche Energy Transients
- Guardring for Stress Protection

Mechanical Characteristics:

- Case: Epoxy, Molded
- Weight: 95 mg (approximately)
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Shipped in 12 mm Tape and Reel, 2500 units per reel
- Cathode Polarity Band
- Marking: B14

MBRS140T3

Motorola Preferred Device

SCHOTTKY BARRIER RECTIFIERS 1.0 AMPERE 40 VOLTS



MAXIMUM RATINGS

Rating		Symbol	Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	47.4	VRRM VRWM VR	40	Volts
Average Rectified Forward Current	T _L = 115°C	lF(AV)	1.0	Amps
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		IFSM	40	Amps
Operating Junction Temperature		TJ	- 65 to +125	°C

THERMAL CHARACTERISTICS

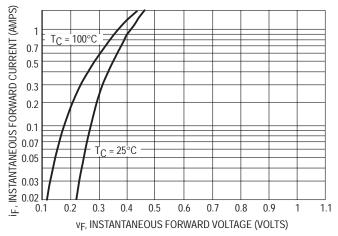
Thermal Resistance — Junction to Lead	R ₀ JL	12	°C/W
$(T_L = 25^{\circ}C)$		- C.C.C.	

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (1) (i _F = 1.0 A, T _J = 25°C)	VF	0.6	Volts
Maximum Instantaneous Reverse Current (1) (Rated dc Voltage, T _J = 25°C) (Rated dc Voltage, T _J = 100°C)	İR	1.0 10	mA

(1) Pulse Test: Pulse Width = 300 µs, Duty Cycle ≤ 2.0%.

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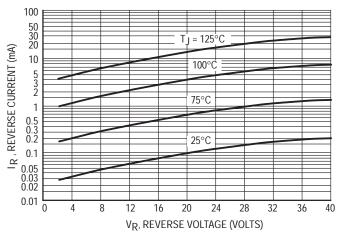


Figure 2. Typical Reverse Current

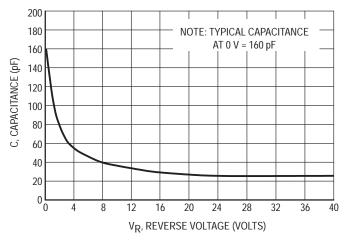


Figure 3. Typical Capacitance

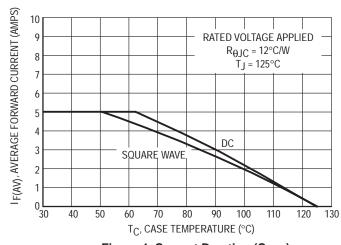


Figure 4. Current Derating (Case)

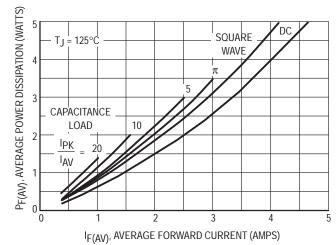
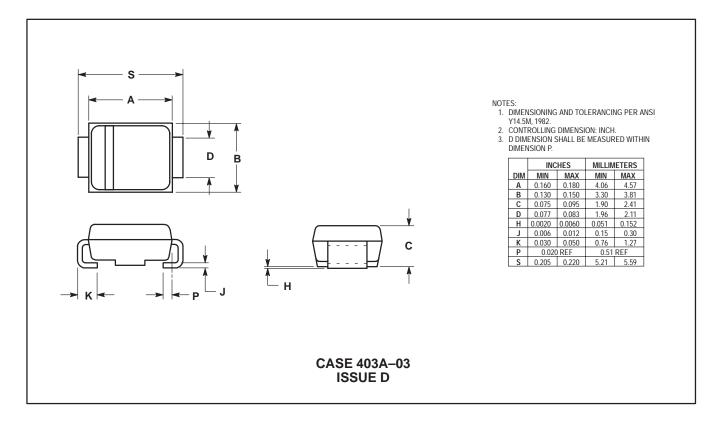


Figure 5. Power Dissipation

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PACKAGE DIMENSIONS



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