MOTOROBRA 540T3/D供应商 SEMICONDUCTOR TECHNICAL DATA

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by MBRS1540T3/D

Advance Information Surface Mount

Schottky Power Rectifier

SMB Power Surface Mount Package

... employing the Schottky Barrier principle in a metal-to-silicon power rectifier. Features epitaxial construction with oxide passivation and metal overlay contact. Ideally suited for low voltage, high frequency switching power supplies; free wheeling diodes and polarity protection diodes.

- Compact Package with J–Bend Leads Ideal for Automated Handling
- Highly Stable Oxide Passivated Junction •
- Guardring for Over-Voltage Protection
- Low Forward Voltage Drop •

Mechanical Characteristics:

- Case: Molded Epoxy
- Epoxy Meets UL94, VO at 1/8"
- Weight: 95 mg (approximately)
- Polarity: Notch in Plastic Body Indicates Cathode Lead
- Lead and Mounting Surface Temperature for Soldering Purposes: 260°C Max. for 10 Seconds
- Available in 12 mm Tape, 2500 Units per 13" Reel, Add "T3" Suffix to Part Number
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Marking: BGJ .

MAXIMUM RATINGS

Rating	Symbol	Value	Volts
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	VRRM VRWM VR	40 02554	
Average Rectified Forward Current (At Rated V _R , T _C = 100°C)	IO	1.5	Amps
Peak Repetitive Forward Current (At Rated V _R , Square Wave, 100 kHz, T _C = 105°C)	IFRM	3.0	Amps
Non-Repetitive Peak Surge Current (Surge applied at rated load conditions, halfwave, single phase, 60 Hz)	IFSM	40	Amps
Storage / Operating Case Temperature	Tstg, T _C	– 55 to +150	°C
Operating Junction Temperature	Тј	– 55 to +125	°C
Voltage Rate of Change (Rated V _R , $T_J = 25^{\circ}C$)	dv/dt	10,000	V/µs
HERMAL CHARACTERISTICS	144	NWW.	

°C/W 24 Thermal Resistance — Junction-to-Lead (2) R₀JL Thermal Resistance — Junction-to-Ambient (2) 80 R_{0JA}

ELECTRICAL CHARACTERISTICS

Maximum Instantaneous Forward Voltage (1)		٧ _F	TJ = 25°C	TJ = 125°C	Volts
see Figure 2	(I _F = 1.5 A) (I _F = 3.0 A)		0.46 0.54	0.39 0.54	
Maximum Instantaneous Reverse Current		IR	Tj = 25°C	TJ = 100°C	mA
see Figure 4	(V _R = 40 V) (V _R = 20 V)		0.8 0.1	5.7 1.6	

This document contains information on a new product. Specifications and information herein are subject to change without notice.

(1) Pulse Test: Pulse Width $\leq 250 \mu s$, Duty Cycle $\leq 2.0\%$.

2 Mounted with minimum recommended pad size, PC Board FR4.

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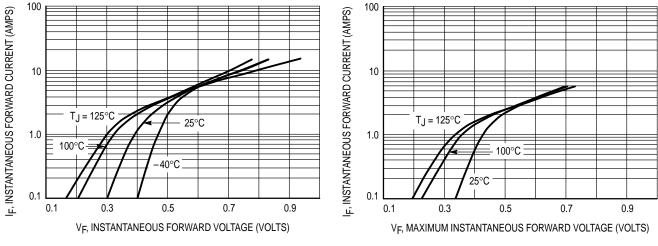
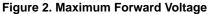


Figure 1. Typical Forward Voltage



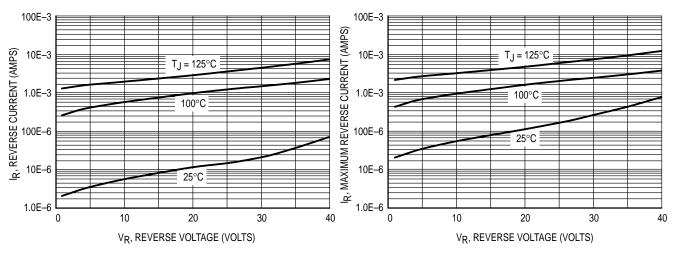
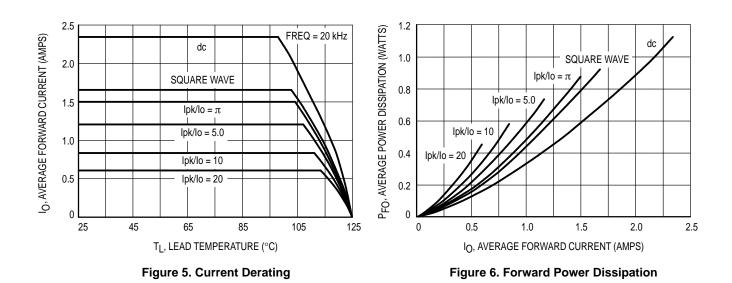
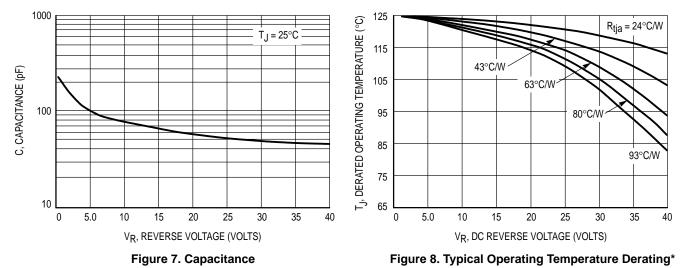




Figure 4. Maximum Reverse Current



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* Reverse power dissipation and the possibility of thermal runaway must be considered when operating this device under any reverse voltage conditions. Calculations of T_J therefore must include forward and reverse power effects. The allowable operating T_J may be calculated from the equation: $T_J = T_{Jmax} - r(t)(Pf + Pr)$ where

r(t) = thermal impedance under given conditions,

Pf = forward power dissipation, and

Pr = reverse power dissipation

This graph displays the derated allowable T_J due to reverse bias under DC conditions only and is calculated as $T_J = T_{Jmax} - r(t)Pr$, where r(t) = Rthja. For other power applications further calculations must be performed.

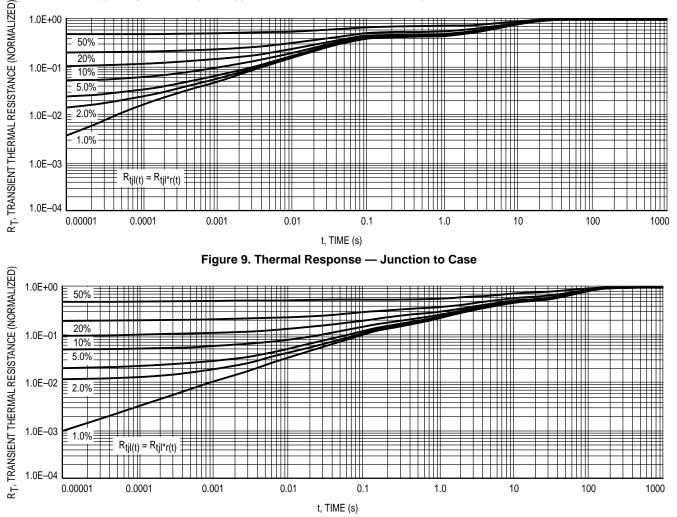
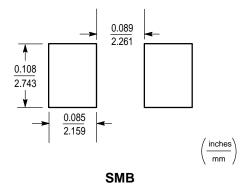
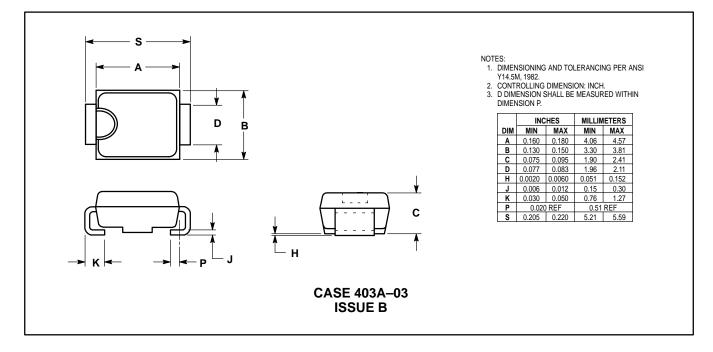


Figure 10. Thermal Response — Junction to Ambient

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