# MOTOR<sup>使復MG10ELT21供应商</sup> SEMICONDUCTOR TECHNICAL DATA

# **Differential PECL to TTL Translator**

The MC10ELT/100ELT21 is a differential PECL to TTL translator. Because PECL (Positive ECL) levels are used only +5V and ground are required. The small outline 8-lead SOIC package and the single gate of the ELT21 makes it ideal for those applications where space, performance and low power are at a premium. Because the mature MOSAIC 1.5 process is used, low cost can be added to the list of features.

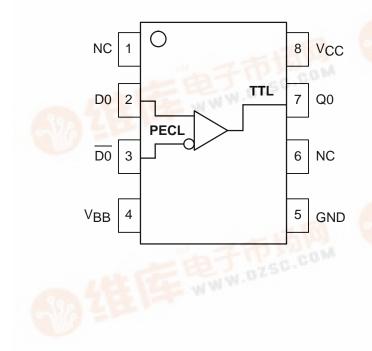
The VBB output allows the ELT21 to also be used in a single-ended input mode. In this mode the VBB output is tied to the IN input for a non-inverting buffer or the IN input for an inverting buffer. If used the VBB pin should be bypassed to ground via a 0.01µF capacitor.

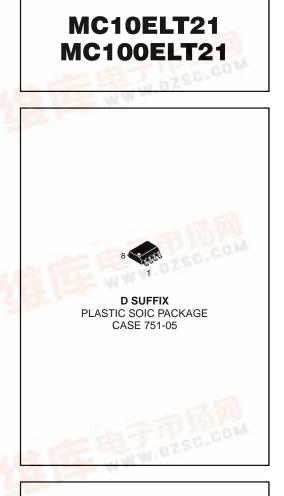
The ELT21 is available in both ECL standards: the 10ELT is compatible with positive MECL 10H logic levels while the 100ELT is compatible with positive ECL 100K logic levels.

- Small Outline SOIC Package
  24mA TTL Output 3.5ns Typical Propagation Delay

- Flow Through Pinouts

#### LOGIC DIAGRAM AND PINOUT ASSIGNMENT





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PIN	FUNCTION
Q	TTL Output
D	Diff PECL Inputs
VCC	+5.0V Supply
VBB	Reference Output
GND	Ground





### MC10ELT21 MC100ELT21

#### MAXIMUM RATINGS\*

Symbol	Parameter	Value	Unit
VCC	DC Supply Voltage (Referenced to GND)	7.0	V
T <sub>A</sub>	Operating Temperature Range (In Free-Air)	-40 to 85	°C
T <sub>STG</sub>	Storage Temperature Range	–55 to +150	°C

\* Maximum Ratings are those values beyond which damage to the device may occur. Functional operation should be restricted to the Recommended Operating Conditions.

### TTL OUTPUT DC CHARACTERISTICS (V\_{CC} = 4.75V to 5.25V; T\_A = -40^{\circ}C to 85°C)

Symbol	Characteristic	Min	Тур	Max	Unit	Condition
VOH	Output HIGH Voltage	2.4			V	I <sub>OH</sub> = -3.0mA
V <sub>OL</sub>	Output LOW Voltage			0.5	V	I <sub>OL</sub> = 24mA
ІССН	Power Supply Current		20	29	mA	
ICCL	Power Supply Current		22	32	mA	
los	Output Short Circuit Current	-150		-60	mA	

## **PECL INPUT DC CHARACTERISTICS** (V<sub>CC</sub> = 4.75V to 5.25V; T<sub>A</sub> = $-40^{\circ}$ C to 85°C)

			-40	)°C	0°C		25°C			85°C			
Symbol	Characteristic	;	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
IIН	Input HIGH Curren	nt		150		150			150		150	μA	
۱ <sub>IL</sub>	Input LOW Curren	ıt	0.5		0.5		0.5			0.5		μA	
VCMR	Common Mode Ra	ange	2.2	VCC	2.2	VCC	2.2		VCC	2.2	VCC	V	
V <sub>PP</sub>	Minimum Peak-to-Peak Inpu	<sub>ut</sub> 1	200		200		200			200		mV	
VIH		0ELT 0ELT	3.770 3.835	4.110 4.120	3.830 3.835	4.16 4.12	3.870 3.835		4.19 4.12	3.930 3.835	4.265 4.120	V	V <sub>CC</sub> = 5.0V
VIL		0ELT 0ELT	3.05 3.19	3.500 3.525	3.05 3.19	3.520 3.525	3.05 3.19		3.520 3.525	3.05 3.19	3.550 3.525	V	V <sub>CC</sub> = 5.0V
V <sub>BB</sub>		0ELT 0ELT	3.57 3.62	3.70 3.74	3.62 3.62	3.73 3.74	3.65 3.62		3.75 3.74	3.69 3.62	3.81 3.75	V	V <sub>CC</sub> = 5.0V

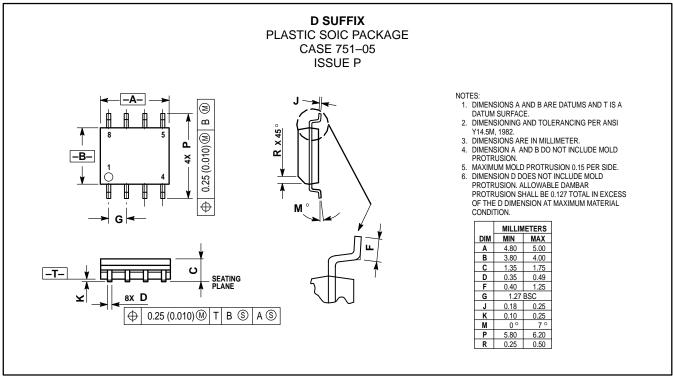
1. 200mV input guarantees full logic swing at the output.

# AC CHARACTERISTICS (V<sub>CC</sub> = 4.75V to 5.25V; T<sub>A</sub> = $-40^{\circ}$ C to $85^{\circ}$ C)

		–40°C		0°C		25°C			85°C			
Symbol	Characteristic	Min	Max	Min	Max	Min	Тур	Max	Min	Max	Unit	Condition
<sup>t</sup> PLH	Propagation Delay <sup>1</sup>	2.0	5.5	2.0	5.5	2.0		5.5	2.0	5.5	ns	$C_L = 20 pF$
<sup>t</sup> PHL	Propagation Delay <sup>1</sup>	2.0	5.5	2.0	5.5	2.0		5.5	2.0	5.5	ns	$C_L = 20 pF$

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