

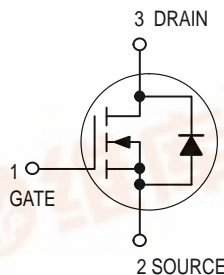


# Low $r_{DS(on)}$ Small-Signal MOSFETs TMOS Single N-Channel Field Effect Transistors

Part of the GreenLine™ Portfolio of devices with energy-conserving traits.

These miniature surface mount MOSFETs utilize Motorola's High Cell Density, HDTMOS process. Low  $r_{DS(on)}$  assures minimal power loss and conserves energy, making this device ideal for use in small power management circuitry. Typical applications are dc-dc converters, power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

- Low  $r_{DS(on)}$  Provides Higher Efficiency and Extends Battery Life
- Miniature SC-70/SOT-323 Surface Mount Package Saves Board Space



## MMBF2201NT1

Motorola Preferred Device

**N-CHANNEL  
 ENHANCEMENT-MODE  
 TMOS MOSFET**  
 $r_{DS(on)} = 1.0 \text{ OHM}$



**CASE 419-02, Style 7  
 SC-70/SOT-323**

### MAXIMUM RATINGS ( $T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Drain-to-Source Voltage	$V_{DSS}$	20	Vdc
Gate-to-Source Voltage — Continuous	$V_{GS}$	$\pm 20$	Vdc
Drain Current — Continuous @ $T_A = 25^\circ\text{C}$ — Continuous @ $T_A = 70^\circ\text{C}$ — Pulsed Drain Current ( $t_p \leq 10 \mu\text{s}$ )	$I_D$ $I_D$ $I_{DM}$	300 240 750	mAdc
Total Power Dissipation @ $T_A = 25^\circ\text{C}$ (1) Derate above $25^\circ\text{C}$	$P_D$	150 1.2	mW mW/°C
Operating and Storage Temperature Range	$T_J, T_{stg}$	-55 to 150	°C
Thermal Resistance — Junction-to-Ambient	$R_{\theta JA}$	833	°C/W
Maximum Lead Temperature for Soldering Purposes, for 10 seconds	$T_L$	260	°C

### DEVICE MARKING

N1
----

(1) Mounted on G10/FR4 glass epoxy board using minimum recommended footprint.

### ORDERING INFORMATION

Device	Reel Size	Tape Width	Quantity
MMBF2201NT1	7"	8 mm embossed tape	3000
MMBF2201NT3	13"	8 mm embossed tape	10,000

GreenLine is a trademark of Motorola, Inc.  
 HDTMOS is a trademark of Motorola, Inc. TMOS is a registered trademark of Motorola, Inc.  
 Thermal Clad is a registered trademark of the Berquist Company.

Preferred devices are Motorola recommended choices for future use and best overall value.



# MMBF2201NT1

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
----------------	--------	-----	-----	-----	------

### OFF CHARACTERISTICS

Drain-to-Source Breakdown Voltage (V <sub>GS</sub> = 0 Vdc, I <sub>D</sub> = 10 μA)	V <sub>(BR)DSS</sub>	20	—	—	Vdc
Zero Gate Voltage Drain Current (V <sub>DS</sub> = 16 Vdc, V <sub>GS</sub> = 0 Vdc) (V <sub>DS</sub> = 16 Vdc, V <sub>GS</sub> = 0 Vdc, T <sub>J</sub> = 125°C)	I <sub>DSS</sub>	—	—	1.0 10	μAdc
Gate-Body Leakage Current (V <sub>GS</sub> = ± 20 Vdc, V <sub>DS</sub> = 0)	I <sub>GSS</sub>	—	—	±100	nAdc

### ON CHARACTERISTICS(1)

Gate Threshold Voltage (V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μAdc)	V <sub>GS(th)</sub>	1.0	1.7	2.4	Vdc
Static Drain-to-Source On-Resistance (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 300 mAdc) (V <sub>GS</sub> = 4.5 Vdc, I <sub>D</sub> = 100 mAdc)	r <sub>DS(on)</sub>	—	0.75 1.0	1.0 1.4	Ohms
Forward Transconductance (V <sub>DS</sub> = 10 Vdc, I <sub>D</sub> = 200 mAdc)	g <sub>FS</sub>	—	450	—	mMhos

### DYNAMIC CHARACTERISTICS

Input Capacitance	(V <sub>DS</sub> = 5.0 V)	C <sub>iss</sub>	—	45	—	pF
Output Capacitance	(V <sub>DS</sub> = 5.0 V)	C <sub>oss</sub>	—	25	—	
Transfer Capacitance	(V <sub>DG</sub> = 5.0 V)	C <sub>rss</sub>	—	5.0	—	

### SWITCHING CHARACTERISTICS(2)

Turn-On Delay Time	(V <sub>DD</sub> = 15 Vdc, I <sub>D</sub> = 300 mAdc, R <sub>L</sub> = 50 Ω)	t <sub>d(on)</sub>	—	2.5	—	ns
Rise Time		t <sub>r</sub>	—	2.5	—	
Turn-Off Delay Time		t <sub>d(off)</sub>	—	15	—	
Fall Time		t <sub>f</sub>	—	0.8	—	
Gate Charge (See Figure 5)		Q <sub>T</sub>	—	1400	—	pC

### SOURCE-DRAIN DIODE CHARACTERISTICS

Continuous Current	I <sub>S</sub>	—	—	0.3	A
Pulsed Current	I <sub>SM</sub>	—	—	0.75	
Forward Voltage(2)	V <sub>SD</sub>	—	0.85	—	V

(1) Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2%.

(2) Switching characteristics are independent of operating junction temperature.

## TYPICAL CHARACTERISTICS

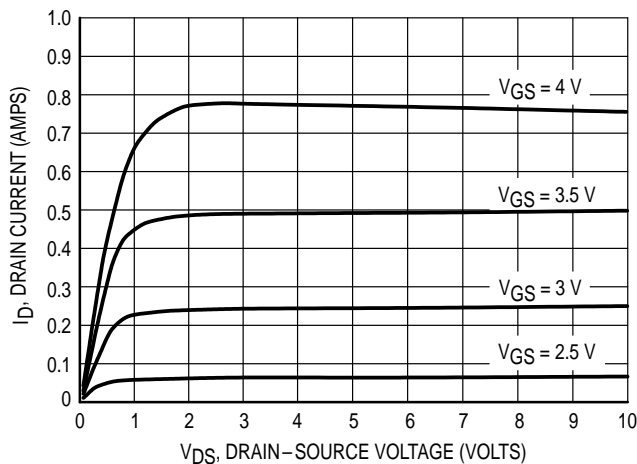


Figure 1. Typical Drain Characteristics

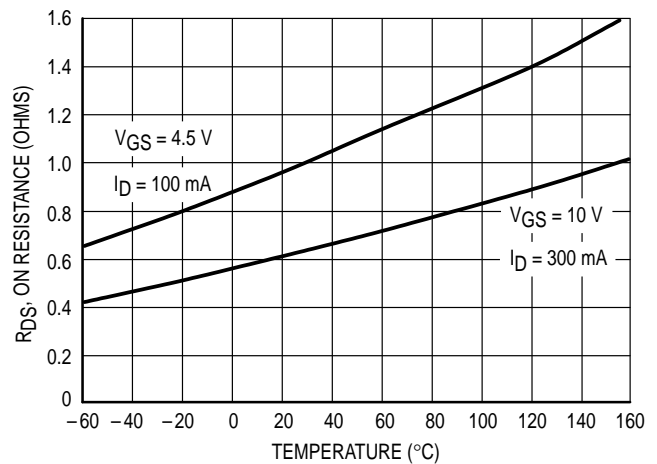


Figure 2. On Resistance versus Temperature

TYPICAL CHARACTERISTICS

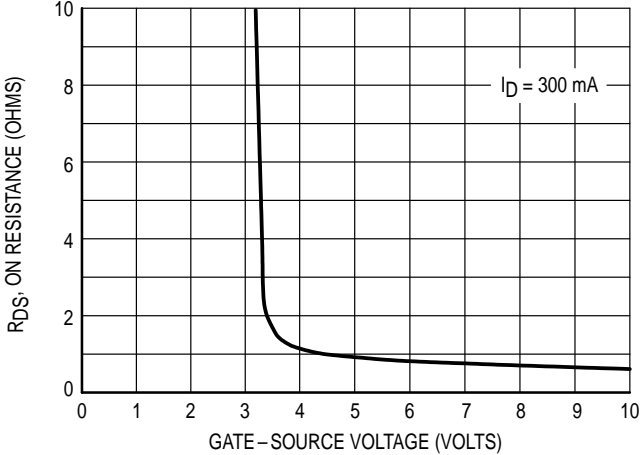


Figure 3. On Resistance versus Gate-Source Voltage

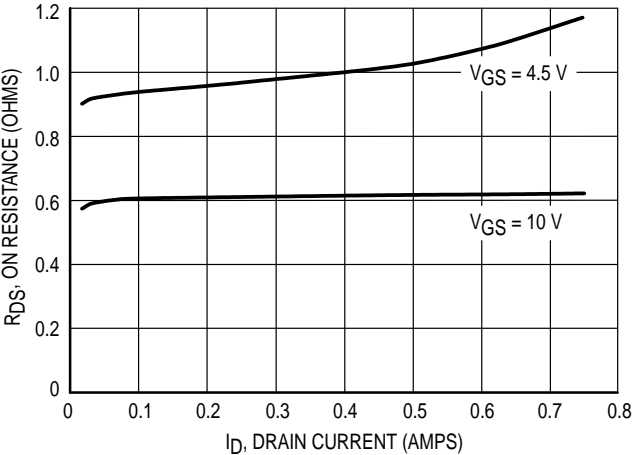


Figure 4. On Resistance versus Drain Current

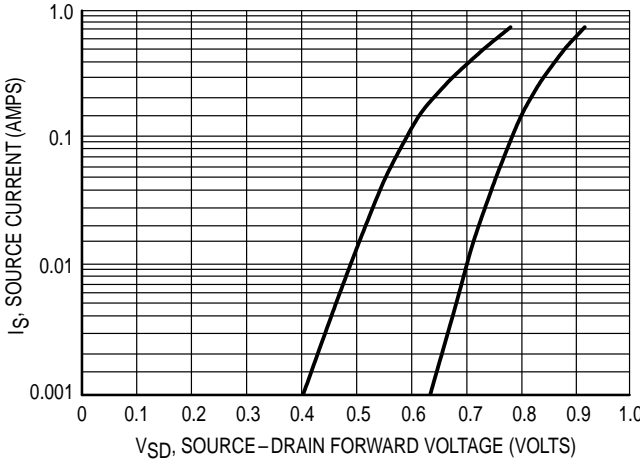


Figure 5. Source-Drain Forward Voltage

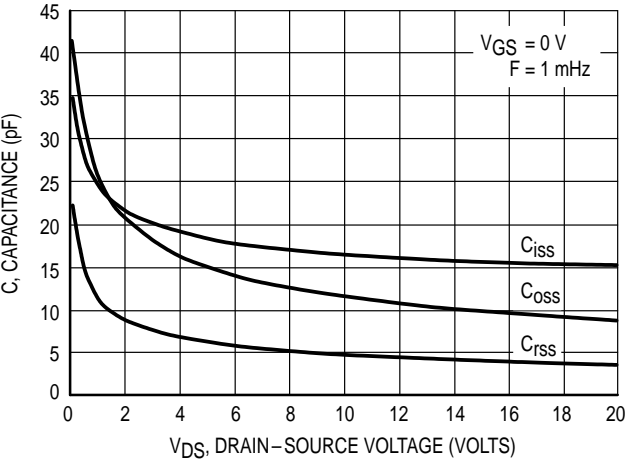


Figure 6. Capacitance Variation

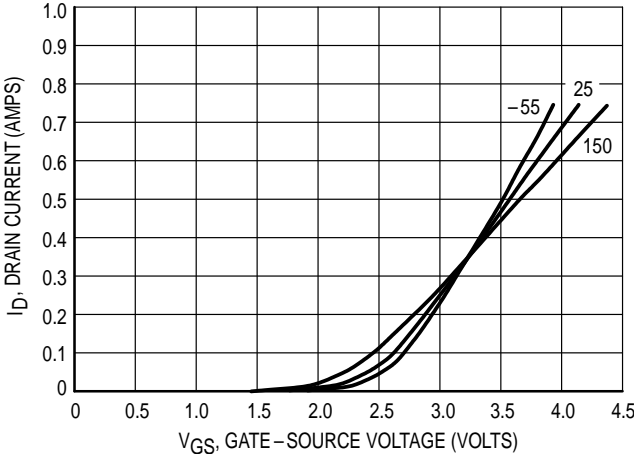


Figure 7. Transfer Characteristics

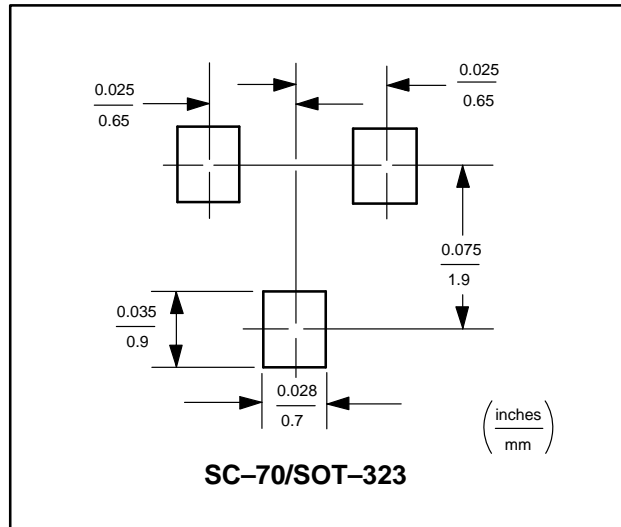
## MMBF2201NT1

# INFORMATION FOR USING THE SC-70/SOT-323 SURFACE MOUNT PACKAGE

### MINIMUM RECOMMENDED FOOTPRINT FOR SURFACE MOUNTED APPLICATIONS

Surface mount board layout is a critical portion of the total design. The footprint for the semiconductor packages must be the correct size to insure proper solder connection

interface between the board and the package. With the correct pad geometry, the packages will self align when subjected to a solder reflow process.



### SC-70/SOT-323 POWER DISSIPATION

The power dissipation of the SC-70/SOT-323 is a function of the drain pad size. This can vary from the minimum pad size for soldering to a pad size given for maximum power dissipation. Power dissipation for a surface mount device is determined by  $T_{J(max)}$ , the maximum rated junction temperature of the die,  $R_{\theta JA}$ , the thermal resistance from the device junction to ambient, and the operating temperature,  $T_A$ . Using the values provided on the data sheet for the SC-70 package,  $P_D$  can be calculated as follows:

$$P_D = \frac{T_{J(max)} - T_A}{R_{\theta JA}}$$

The values for the equation are found in the maximum ratings table on the data sheet. Substituting these values into the equation for an ambient temperature  $T_A$  of 25°C, one can calculate the power dissipation of the device which in this case is 150 milliwatts.

$$P_D = \frac{150^\circ\text{C} - 25^\circ\text{C}}{833^\circ\text{C/W}} = 150 \text{ milliwatts}$$

The 833°C/W for the SC-70/SOT-323 package assumes the use of the recommended footprint on a glass epoxy printed circuit board to achieve a power dissipation of 150 milliwatts. There are other alternatives to achieving higher power dissipation from the SC-70/SOT-323 package. Another alternative would be to use a ceramic substrate or an aluminum core board such as Thermal Clad™. Using a board material such as Thermal Clad, an aluminum core board, the power dissipation can be doubled using the same footprint.

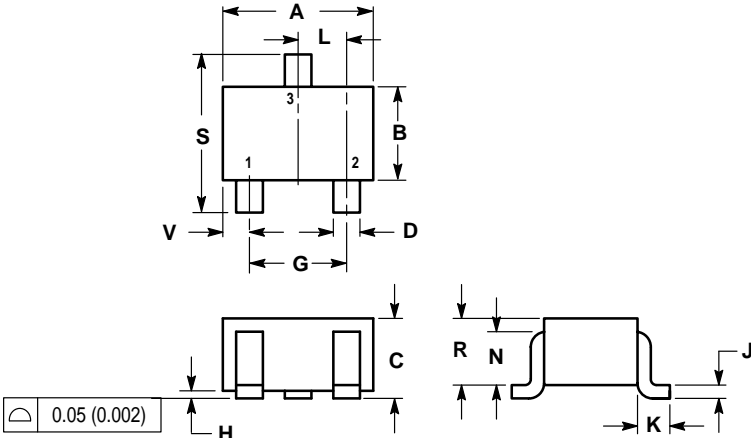
### SOLDERING PRECAUTIONS

The melting temperature of solder is higher than the rated temperature of the device. When the entire device is heated to a high temperature, failure to complete soldering within a short time could result in device failure. Therefore, the following items should always be observed in order to minimize the thermal stress to which the devices are subjected.

- Always preheat the device.
- The delta temperature between the preheat and soldering should be 100°C or less.\*
- When preheating and soldering, the temperature of the leads and the case must not exceed the maximum temperature ratings as shown on the data sheet. When using infrared heating with the reflow soldering method, the difference shall be a maximum of 10°C.
- The soldering temperature and time shall not exceed 260°C for more than 10 seconds.
- When shifting from preheating to soldering, the maximum temperature gradient shall be 5°C or less.
- After soldering has been completed, the device should be allowed to cool naturally for at least three minutes. Gradual cooling should be used as the use of forced cooling will increase the temperature gradient and result in latent failure due to mechanical stress.
- Mechanical stress or shock should not be applied during cooling.

\* Soldering a device without preheating can cause excessive thermal shock and stress which can result in damage to the device.

PACKAGE DIMENSIONS




- NOTES:  
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.  
 2. CONTROLLING DIMENSION: INCH.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.071	0.087	1.80	2.20
B	0.045	0.053	1.15	1.35
C	0.035	0.049	0.90	1.25
D	0.012	0.016	0.30	0.40
G	0.047	0.055	1.20	1.40
H	0.000	0.004	0.00	0.10
J	0.004	0.010	0.10	0.25
K	0.017 REF		0.425 REF	
L	0.026 BSC		0.650 BSC	
N	0.028 REF		0.700 REF	
R	0.031	0.039	0.80	1.00
S	0.079	0.087	2.00	2.20
V	0.012	0.016	0.30	0.40

- STYLE 7:  
 PIN 1. DRAIN  
 2. GATE  
 3. COLLECTOR

CASE 419-02  
 SC-70/SOT-323  
 ISSUE E

## MMBF2201NT1

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Motorola was negligent regarding the design or manufacture of the part. Motorola and  are registered trademarks of Motorola, Inc. Motorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

**How to reach us:**

**USA / EUROPE:** Motorola Literature Distribution;  
P.O. Box 20912; Phoenix, Arizona 85036. 1-800-441-2447

**MFAX:** RMFAX0@email.sps.mot.com - TOUCHTONE (602) 244-6609  
**INTERNET:** <http://Design-NET.com>

**JAPAN:** Nippon Motorola Ltd.; Tatsumi-SPD-JLDC, Toshikatsu Otsuki,  
6F Seibu-Butsuryu-Center, 3-14-2 Tatsumi Koto-Ku, Tokyo 135, Japan. 03-3521-8315

**HONG KONG:** Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park,  
51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298

