MOTORO D302/D供应商 SEMICONDUCTOR TECHNICAL DATA

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by MTD3302/D

MTD3302

SINGLE TMOS

POWER MOSFET 30 VOLTS $R_{DS(on)} = 10 \text{ m}\Omega$

CASE 369A-13, Style 2

DPAK

Product Preview

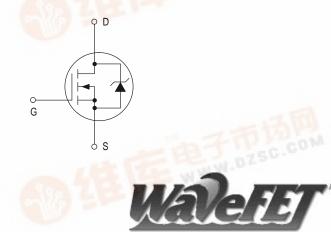
WaveFETTM **Power Surface Mount Products HDTMOS Single N-Channel Field Effect Transistor**



WaveFET[™] devices are an advanced series of power MOSFETs which utilize Motorola's latest MOSFET technology process to achieve the lowest possible on-resistance per silicon area. They are capable of withstanding high energy in the avalanche and commutation modes and the drain-to-source diode has a very low reverse recovery time. WaveFET™ devices are designed for use in low voltage, high speed switching applications where power efficiency is important. Typical applications are dc-dc converters, and power management in portable and battery powered products such as computers, printers, cellular and cordless phones. They can also be used for low voltage motor controls in mass storage products such as disk drives and tape drives. The avalanche energy is specified to eliminate the guesswork in designs where inductive loads are switched and offer additional safety margin against unexpected voltage transients.

DZSC.COM

- Characterized Over a Wide Range of Power Ratings
- Ultralow RDS(on) Provides Higher Efficiency and Extends Battery Life in Portable Applications
- Logic Level Gate Drive Can Be Driven by Logic ICs
- Diode Is Characterized for Use In Bridge Circuits
- Diode Exhibits High Speed, With Soft Recovery
- IDSS Specified at Elevated Temperature •
- Avalanche Energy Specified
- Industry Standard DPAK Surface Mount Package



MAXIMUM RATINGS (T_J = 25°C unless otherwise specified)

| Parameter | | Value | Unit |
|--|----------|------------|------|
| Drain-to-Source Voltage | VDSS | 30 | Vdc |
| Drain-to-Gate Voltage | VDGR | 30 | Vdc |
| Gate-to-Source Voltage | VGS | ±20 | Vdc |
| Operating and Storage Temperature Range | TJ, Tstg | -55 to 150 | °C |
| Single Pulse Drain–to–Source Avalanche Energy — Starting T _J = 25°C (V _{DD} = 25 Vdc, V _{GS} = 10 Vdc, L = 126 mH, I _{L(pk)} = 3.0 A, V _{DS} = 30 Vdc) | EAS | 500 | mJ |

DEVICE MARKING ORDERING INFORMATION Device **Reel Size** Tape Width Quantity D3302 2500 MTD3302T4 13″ 12 mm embossed tape

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MTD3302

| Parameter | | Symbol | Value | Unit |
|--|--|-------------------|----------------|-------------------|
| $ \begin{array}{l} \text{Drain Current} \text{Continuous} @ T_A = 25^\circ C \\ \text{Continuous} @ T_A = 100^\circ C \\ \text{Single Pulse (tp \le 10 \ \mu s)} \end{array} $ | Mounted on heat sink T _{Case} = 25°C | ID ID MD | 30 30 70 | Adc Adc Adc |
| Total Power Dissipation @ T _A = 25°C Linear Derating Factor | V _{GS} = 10 Vdc | PD | 96 769 | Watts mW/°C |
| Thermal Resistance — Junction-to-Case | Steady State | R _θ JC | 1.3 | °C/W |
| Continuous Source Current (Diode Conduction) | | ١ _S | 2.0 | Adc |

POWER RATINGS ($T_J = 25^{\circ}C$ unless otherwise specified)

| Parameter | | Symbol | Value | Unit |
|--|---|---|-------------------|-------------------|
| $ \begin{array}{l} \text{Drain Current} \longrightarrow \text{Continuous} @ T_A = 25^\circ C \\ \longrightarrow \text{Continuous} @ T_A = 100^\circ C \\ \longrightarrow \text{Single Pulse (tp \le 10 \ \mu s)} \end{array} $ | Mounted on 1 inch square FR–4 or G10 board | I _D I _D I _{DM} | 10.8 6.6 70 | Adc Adc Adc |
| Total Power Dissipation @ T _A = 25°C Linear Derating Factor | V _{GS} = 10 Vdc | PD | 1.8 14 | Watts mW/°C |
| Thermal Resistance — Junction-to-Ambient | Steady State | R _{θJA} | 71.4 | °C/W |
| Continuous Source Current (Diode Conduction) | | ۱ _S | 2.0 | Adc |

| Parameter | | Symbol | Value | Unit |
|--|---|-----------------|------------------|-------------------|
| Drain Current — Continuous @ $T_A = 25^{\circ}C$ — Continuous @ $T_A = 100^{\circ}C$ — Single Pulse (tp $\leq 10 \ \mu$ s) | Mounted on minimum recommended FR–4 or G10 board | ID ID IDM | 8.3 5.2 60 | Adc Adc Adc |
| Total Power Dissipation @ T _A = 25°C Linear Derating Factor | V _{GS} = 10 Vdc | PD | 1.0 8.3 | Watts mW/°C |
| Thermal Resistance — Junction-to-Ambient | Steady State | $R_{\theta JA}$ | 120 | °C/W |
| Continuous Source Current (Diode Conduction) |] , | ١ _S | 2.0 | Adc |

MTD3302

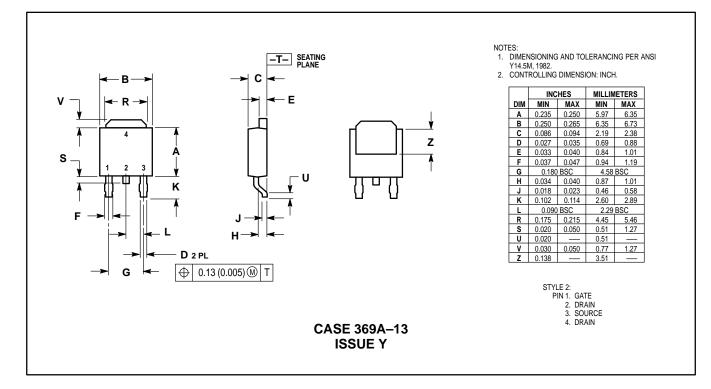
ELECTRICAL CHARACTERISTICS (T = 25°C unless otherwise specified)

| Characteristic | | Symbol | Min | Тур | Max | Unit |
|--|---|-----------------------|---------|--------------|-----------|--------------|
| OFF CHARACTERISTICS | | | | | | • |
| Drain-to-Source Breakdown Voltage ($V_{GS} = 0 \text{ Vdc}, I_D = 250 \ \mu\text{Adc}$) Temperature Coefficient (Positive) | | V _(BR) DSS | 30 — | 33 23 | _ | Vdc mV/°C |
| Zero Gate Voltage Drain Current $(V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc})$ $(V_{DS} = 30 \text{ Vdc}, V_{GS} = 0 \text{ Vdc}, T_{J} = 125^{\circ}\text{C})$ | | IDSS | | 0.02 0.5 | 1.0 10 | μAdc |
| Gate-Body Leakage Current (VGS = | \pm 20 Vdc, V _{DS} = 0 Vdc) | IGSS | _ | - | ±100 | nAdc |
| ON CHARACTERISTICS ⁽¹⁾ | | | | | | |
| Gate Threshold Voltage $(V_{DS} = V_{GS}, I_D = 250 \mu Adc)$ Threshold Temperature Coefficient (Negative) | | VGS(th) | 1.0 | 1.9 4.7 | _ | Vdc mV/°C |
| Static Drain-to-Source On-Resistance $(V_{GS} = 10 \text{ Vdc}, I_D = 10 \text{ Adc})$ $(V_{GS} = 4.5 \text{ Vdc}, I_D = 5.0 \text{ Adc})$ | | R _{DS(on)} | _ | 8.9 13 | 10 16 | mΩ |
| Forward Transconductance ($V_{DS} = 1$ | 5 Vdc, I _D = 10 Adc) | 9FS | 5 | 13 | | Mhos |
| DYNAMIC CHARACTERISTICS | | | | | | |
| Input Capacitance | | C _{iss} | _ | 1810 | — | pF |
| Output Capacitance | (V _{DS} = 24 Vdc, V _{GS} = 0 Vdc, f = 1.0 MHz) | C _{oss} | — | 165 | _ | |
| Transfer Capacitance | | C _{rss} | _ | 595 | _ | |
| SWITCHING CHARACTERISTICS ⁽²⁾ | | | | | | • |
| Turn–On Delay Time | | ^t d(on) | — | 9 | — | ns |
| Rise Time | $(V_{DD} = 25 \text{ Vdc}, I_D = 1.0 \text{ Adc},$ | t _r | _ | 10 | _ | - |
| Turn–Off Delay Time | V _{GS} = 10 Vdc, R _G = 6.0 Ω) | ^t d(off) | _ | 60 | — | |
| Fall Time | C | tf | _ | 43 | _ | |
| Turn–On Delay Time | | ^t d(on) | _ | 18 | | ns |
| Rise Time | $(V_{DD} = 25 \text{ Vdc}, I_D = 1.0 \text{ Adc},$ | tr | _ | 32 | | |
| Turn–Off Delay Time | V _{GS} = 4.5 Vdc, R _G = 6.0 Ω) | ^t d(off) | _ | 42 | _ | |
| Fall Time | | t _f | _ | 44 | _ | |
| Gate Charge | | QT | _ | 46 | 60 | nC |
| - | $(V_{DS} = 15 \text{ Vdc}, I_D = 2.0 \text{ Adc}, V_{GS} = 10 \text{ Vdc})$ | Q ₁ | | 5.3 | | |
| | | Q ₂ | _ | 10.7 | | |
| | | Q ₃ | _ | 10.3 | | |
| SOURCE-DRAIN DIODE CHARACTE | RISTICS | Ŭ Ŭ | | 1 | | I |
| Forward On–Voltage ⁽¹⁾ | $(I_{S} = 2.3 \text{ Adc}, V_{GS} = 0 \text{ Vdc})$ $(I_{S} = 2.3 \text{ Adc}, V_{GS} = 0 \text{ Vdc}, T_{J} = 125^{\circ}\text{C})$ | V _{SD} | _ | 0.75 0.58 | 1.1 | Vdc |
| | | | | | | 1 |

| | (I _S = 2.3 Adc, V _{GS} = 0 Vdc) (I _S = 2.3 Adc, V _{GS} = 0 Vdc, T _J = 125°C) | | _ | 0.75 0.58 | 1.1 | |
|--------------------------------|--|-----------------|---|--------------|-----|----|
| Reverse Recovery Time | | t _{rr} | _ | 36 | | ns |
| | (I _S = 2.3 Adc, V _{GS} = 0 Vdc, | ^t a | — | 21 | | |
| | dl _S /dt = 100 A/µs) | t _b | _ | 15 | | |
| Reverse Recovery Stored Charge | | Q _{RR} | _ | 0.041 | | μC |

Pulse Test: Pulse Width ≤ 300 µs, Duty Cycle ≤ 2%.
 Switching characteristics are independent of operating junction temperatures.

PACKAGE DIMENSIONS



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