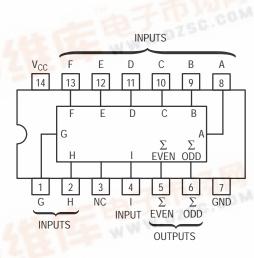
# 9-Bit Odd/Even Parity Generators/Checkers

The SN74LS280 is a Universal 9-Bit Parity Generator/Checker. It features odd/even outputs to facilitate either odd or even parity. By cascading, the word length is easily expanded.

The LS280 is designed without the expander input implementation, but the corresponding function is provided by an input at Pin 4 and the absence of any connection at Pin 3. This design permits the LS280 to be substituted for the LS180 which results in improved performance. The LS280 has buffered inputs to lower the drive requirements to one LS unit load.

- Generates Either Odd or Even Parity for Nine Data Lines
- Typical Data-to-Output Delay of only 33 ns
- Cascadable for n-Bits
- Can Be Used To Upgrade Systems Using MSI Parity Circuits
- Typical Power Dissipation = 80 mW



## **FUNCTION TABLE**

NUMBER OF INPUTS A	OUTPUTS		
THRU 1 THAT ARE HIGH	$\Sigma$ EVEN	ΣODD	
0, 2, 4, 6, 8	Н	L	
1, 3, 5, 7, 9	L	Н	

H = HIGH Level, L = LOW Level

## **GUARANTEED OPERATING RANGES**

Symbol	Parameter Parameter	Min	Тур	Max	Unit
V <sub>CC</sub>	Supply Voltage	4.75	5.0	5.25	V
T <sub>A</sub>	Operating Ambient Temperature Range	0	25	70	°C
I <sub>OH</sub>	Output Current – High			-0.4	mA
9bDE	Output Current – Low			8.0	mA



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LOW POWER SCHOTTKY



PLASTIC N SUFFIX CASE 646

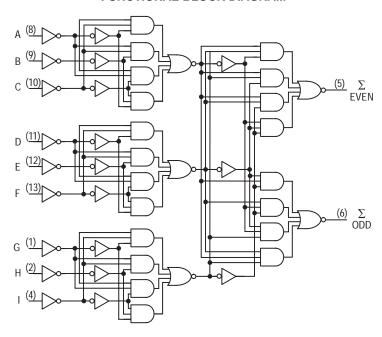


SOIC D SUFFIX CASE 751A

### **ORDERING INFORMATION**

Device Package		Shipping
SN74LS280N	14 Pin DIP	2000 Units/Box
SN74LS280D	14 Pin	2500/Tape & Reel

## **FUNCTIONAL BLOCK DIAGRAM**



## DC CHARACTERISTICS OVER OPERATING TEMPERATURE RANGE (unless otherwise specified)

		Limits					
Symbol	Parameter	Min	Тур	Max	Unit	Tes	t Conditions
V <sub>IH</sub>	Input HIGH Voltage	2.0			V	Guaranteed Input HIGH Voltage for All Inputs	
V <sub>IL</sub>	Input LOW Voltage			0.8	V	Guaranteed Input LOW Voltage for All Inputs	
V <sub>IK</sub>	Input Clamp Diode Voltage		-0.65	-1.5	V	V <sub>CC</sub> = MIN, I <sub>IN</sub> = -18 mA	
V <sub>OH</sub>	Output HIGH Voltage	2.7	3.5		V	$V_{CC}$ = MIN, $I_{OH}$ = MAX, $V_{IN}$ = $V_{IH}$ or $V_{IL}$ per Truth Table	
			0.25	0.4	V	I <sub>OL</sub> = 4.0 mA	$V_{CC} = V_{CC} MIN,$
V <sub>OL</sub>	Output LOW Voltage		0.35	0.5	V	I <sub>OL</sub> = 8.0 mA	V <sub>IN</sub> = V <sub>IL</sub> or V <sub>IH</sub> per Truth Table
1	lament I II O I I Command			20	μΑ	$V_{CC} = MAX, V_{IN} = 2.7 V$	
l <sub>IH</sub>	Input HIGH Current			0.1	mA	V <sub>CC</sub> = MAX, V <sub>IN</sub> = 7.0 V	
I <sub>IL</sub>	Input LOW Current			-0.4	mA	$V_{CC} = MAX$ , $V_{IN} = 0.4 V$	
Ios	Short Circuit Current (Note 1)	-20		-100	mA	V <sub>CC</sub> = MAX	
Icc	Power Supply Current			27	mA	V <sub>CC</sub> = MAX	

Note 1: Not more than one output should be shorted at a time, nor for more than 1 second.

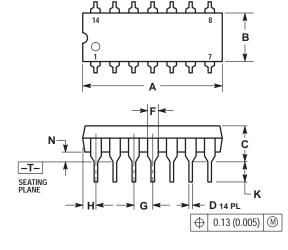
## AC CHARACTERISTICS (T<sub>A</sub> = $25^{\circ}$ C, V<sub>CC</sub> = 5.0 V)

		Limits					
Symbol	Parameter	Min	Тур	Max	Unit	Test Conditions	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, Data to Output ΣEVEN		33 29	50 45	ns	C <sub>L</sub> = 15 pF	
t <sub>PLH</sub> t <sub>PHL</sub>	Propagation Delay, Data to Output ΣΟDD		23 31	35 50	ns		

## **PACKAGE DIMENSIONS**

## **N SUFFIX**

PLASTIC PACKAGE CASE 646-06 ISSUE M





#### NOTES:

- NOTES:

  1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

  2. CONTROLLING DIMENSION: INCH.

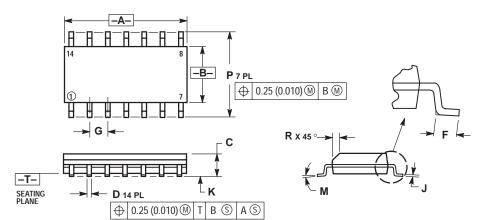
  3. DIMENSION L TO CENTER OF LEADS WHEN FORMED PARALLEL.

  4. DIMENSION B DOES NOT INCLUDE MOLD FLASH.

  5. ROUNDED CORNERS OPTIONAL.

	INC	HES	MILLIN	IETERS	
DIM	MIN	MAX	MIN	MAX	
Α	0.715	0.770	18.16	18.80	
В	0.240	0.260	6.10	6.60	
С	0.145	0.185	3.69	4.69	
D	0.015	0.021	0.38	0.53	
F	0.040	0.070	1.02	1.78	
G	0.100	BSC	2.54 BSC		
Н	0.052	0.095	1.32	2.41	
J	0.008	0.015	0.20	0.38	
K	0.115	0.135	2.92	3.43	
L	0.290	0.310	7.37	7.87	
M		10°		10°	
N	0.015	0.039	0.38	1.01	

## **D SUFFIX** PLASTIC SOIC PACKAGE CASE 751A-03 ISSUE F



#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.

- Y14.5M, 1982.

  2. CONTROLLING DIMENSION: MILLIMETER.

  3. DIMENSIONS A AND B DO NOT INCLUDE MOLD PROTRUSION.

  4. MAXIMUM MOLD PROTRUSION 0.15 (0.006) PER SIDE.

  5. DIMENSION D DOES NOT INCLUDE DAMBAR PROTRUSION. ALLOWABLE DAMBAR PROTRUSION SHALL BE 0.127 (0.005) TOTAL IN EXCESS OF THE D DIMENSION AT MAXIMUM MATERIAL CONDITION.

	MILLIN	IETERS	INCHES		
DIM	MIN	MAX	MIN	MAX	
Α	8.55	8.75	0.337	0.344	
В	3.80	4.00	0.150	0.157	
С	1.35	1.75	0.054	0.068	
D	0.35	0.49	0.014	0.019	
F	0.40	1.25	0.016	0.049	
G	1.27	BSC	0.050 BSC		
J	0.19	0.25	0.008	0.009	
K	0.10	0.25	0.004	0.009	
M	0 °	7°	0 °	7 °	
Р	5.80	6.20	0.228	0.244	
R	0.25	0.50	0.010	0.019	

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