SN5425253 USN546253, SN74125253, SN745253 **DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS**

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- Three-State Version of SN54/74LS153, SN54/74S153
- Schottky-Diode-Clamped Transistors
- Permits Multiplexing from N Lines to 1 Line
- Performs Parallel-to Serial Conversion
- **Fully Compatible with Most TTL Circuits**
- **Low Power Dissipation** 'LS253 . . . 35 mW Typical 'S253 . . . 225 mW Typical

description

Each of these Schottky-clamped data selectors/multiplexers contains inverters and drivers to supply fully complementary, on-chip, binary decoding data selection to the AND-OR gates. Separate output control inputs are provided for each of the two four-line sections.

The three-state outputs can interface with and drive data lines of bus-organized systems. With all but one of the common outputs disabled (at a high-impedance state) the low-impedance of the single enabled output will drive the bus line to a high or low logic level. SN54LS253, SN54S253 . . . J OR W PACKAGE SN74LS253, SN74S253 . . . D OR N PACKAGE (TOP VIEW)

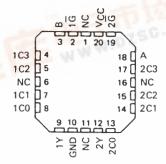
1G 1 U16 VCC BI 15 2G 14 A 1C3 [2C3 □ 2C2 2C1 1C0 [

1Y ∏₇

GND □8

SN54LS253, SN54S253 . . . FK PACKAGE (TOP VIEW)

10 2C0



NC-No internal connection

FUNCTION TABLE

	ELECT	- 1			INPUTS		OUTPUT	
Е	A		CO	C1	C2	C3	G	Υ
×	X		X	X	X	X	Н	Z
Ł	L	c. F	L	X	X	X	L	L
L	L		Н	×	X	×	L	Н
L	. н		X	L	X	X	L	L
L	. Н		X	Н	X	X	L	Н
Н	L		X	×	L	X	L	L
۲	ł L		X	X	Н	X	L	Н
H	Н		X	×	X	L	L	L
H	l H		Х	X	X	Н	L	Н

Address inputs A and B are common to both sections. H = high level, L = low level, X = irrelevant, Z = high impedance (off)

absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

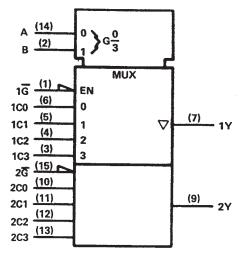
Supply voltage, VCC (see Note 1)	s@::	 	7 V
Input voltage: 'LS253		 	7 V
'S253		 	5.5 V
Off-state output voltage			
Operating free-air temperature range:	SN54LS253, SN54S253	 	- 55°C to 125°C
	SN74LS253, SN74S253	 	0°C to 70°C
Storage temperature range			- 65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.

SN54LS253, SN54S253, SN74LS253, SN74S253 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

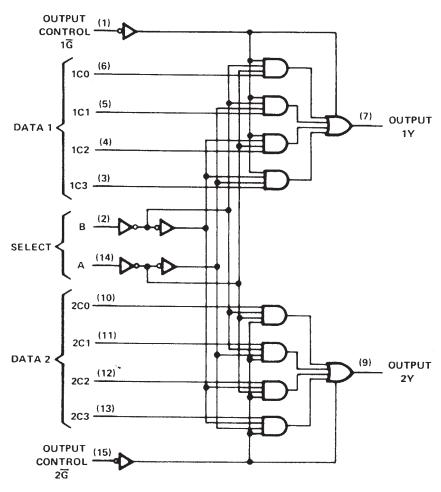
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logic symbol†



[†] This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram (positive logic)

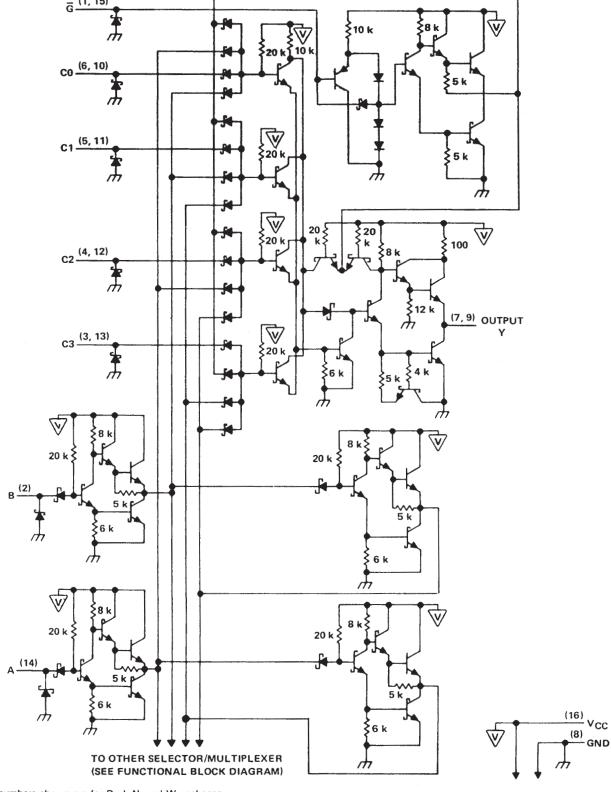


Pin numbers shown are for D, J, N, and W packages.



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schematic (each selector/multiplexer, and the common select section)



Pin numbers shown are for D, J, N, and W packages.

SN54LS253, SN54S253, SN74LS253, SN74S253 DUAL 4-LINE TO 1-LINE DATA SELECTORS/MULTIPLEXERS WITH 3-STATE OUTPUTS

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recommended operating conditions

		s	N54LS2	53	SN74LS253 MIN NOM MAX 4.75 5 5.25		UNIT	
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
Vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			V
VIL	Low-level input voltage			0.7			0.8	V
Іон	High-level output current			- 1			2.6	mA
IOL	Low-level output current			4			8	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER		TEST CONDITIONS [†]						SN74LS253				
TANAMETER		TEST CONDITT	ONS		MIN	TYP ‡	MAX	MIN	TYP‡	MAX	UNIT	
VIK	VCC = MIN,	$I_1 = -18 \text{ mA}$					- 1.5			- 1.5	V	
VOH	V _{CC} = MIN,	V _{IH} = 2 V,	VIL = MAX,	1 _{OH} = MAX	2.4	3.4		2.4	3.1		٧	
VOL	V _{CC} = MIN,	V = 2 V	V MAY	10L = 4 mA		0.25	0.4		0.25	0.4	V	
VOL	ACC - MILIA	$V_{IH} = 2 V$,	VIL = MAX	I _{OL} = 8 mA					0.25	0.5		
loz	V _{CC} = MAX,	V _{IH} = 2 V		V _O = 2.7 V			20			20		
102		VIH - Z V		V _O = 0.4 V			- 20			20	μΑ	
l _I	V _{CC} = MAX,	V ₁ = 7 V					0.1			0.1	mΑ	
IН	V _{CC} = MAX,	V _I = 2.7 V					20			20	μΑ	
1	V _{CC} = MAX,	V. = 0.4.V	Ğ				- 0.2			- 0.2		
116	ACC - MIXY	V ₁ = 0.4 V		All other			- 0.4			- 0.4	, mA	
los§	V _{CC} = MAX				- 30		- 130	- 30		- 130	mA	
loo	V _{CC} = MAX,	See Note 2		Condition A		7	12		7	12		
¹cc	VCC - MAA,	See Note 2		Condition B		8.5	14		8.5	14	mA	

[†] For conditions shown as MIN or MAX, use the appropriate value spcified under recommended operating conditions.

NOTE 2: I_{CC} is measured with the outputs open under the following conditions:

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP M	иах	UNIT
tPLH	Date	_			17	25	
tPHL	Data	'			13	20	ns
^t PLH	Select	_	$C_L = 15 pF$, $R_L = 2 k\Omega$,	$C_{L} = 15 \text{ pF}, \qquad R_{L} = 2 \text{ k}\Omega,$	30	45	
tPHL	Select	'	See Note 3		21	32	ns
^t PZH	Output	_			15	28	
^t PZL	Control	'			15	23	ns
^t PHZ	Output	v	$C_L = 5 pF$, $R_L = 2 k\Omega$,		27	41	
^t PLZ	Control	'	See Note 3		18	27	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ} \text{C}$.

[§] Not more than one output should be shorted at a time, and duration for the short-circuit should exceed one second.

A. All inputs grounded.

B. Output control at 4.5 V, all inputs grounded.

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recommended operating conditions

			N54S2	53	SN74S253			
		MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC	Supply voltage	4.5	5	5.5	4.75	5	5.25	V
VIH	High-level input voltage	2			2			V
NIT	Low-level input voltage			0.8			0.8	V
ЮН	High-level output current			-2			- 6.5	mA
IOL	Low-level output current			20			20	mA
TA	Operating free-air temperature	- 55		125	0		70	°C

electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

PARAMETER			TEST CONDIT	TIONS†		MIN	TYP‡	MAX	UNIT
VIK	VCC = MIN,	= MIN, I _I = - 18 mA						- 1.2	V
Voн	VCC = MIN,	V _{1H} = 2 V,	V _{1L} = 0.8 V,	IOH = MAX	Series 54S	2.5	3.4		\
* OH	VCC - WITH,	VIH - 2 V,	V1[- 0.8 V,	10H = MAX	Series 74S	2.7	3.4		V V V μA mA μA
VOL	VCC = MIN,	VIH = 2 V,	VIL = 0.8 V,	IOL = 20 mA				0.5	V
loz	V _{CC} = MAX,	VIH = 2 V			V _O = 2.4 V			50	
	VCC - MAX,	VIH - 2 V			V _O = 0.5 V			- 50	
11	V _{CC} = MAX,	V1 = 5.5 V						1	mA
IН	V _{CC} = MAX,	V _I = 2.7 V						50	μΑ
IIL	VCC = MAX,	VI = 0.5 V			G = 0.8 V			- 2	
	100	*, 0.0 *			G = 2 V			- 0.25] "
los§	V _{CC} = MAX					- 40		- 100	mA
lcc	V _{CC} = MAX,	See Note 2			Condition A		45	70	
	TOC MAX,	300 NOTE 2			Condition B		65	85	mA

[†] For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

Not more than one output should be shorted at a time and duration of short-circuit should not exceed one second.

NOTE 2: $I_{\mbox{\footnotesize{CC}}}$ is measured with the outputs open under the following conditions:

A. All inputs grounded.

switching characteristics, $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ}\text{C}$

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN TYP	MAX	UNIT	
^t PLH	Data	~			6	9 9 18	
^t PHL	Select	'			6		ns
^t PLH		V	$R_L = 280 \Omega$,	C _L = 15 pF	11.5		
^t PHL	Sciect	'	See Note 3	OL 10 Pi	12	18	ns
^t PZH	Output Control		000 11010 0		11	16.5	ns
^t PZL		'			12	18	
^t PHZ	Output		R _L = 280 Ω,	C _L = 5 pF	6.5	9.5	
^t PLZ	Control	,	See Note 3		10	15	ns

NOTE 3: Load circuits and voltage waveforms are shown in Section 1.



[‡] All typical values are at $V_{CC} = 5 \text{ V}$, $T_A = 25^{\circ} \text{C}$.

B. Output control at 4.5 V, all inputs grounded.

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