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OUT_B ω

4 SHT

H₁ Ø

SUPPLY 등

OUTA E

BRAKE 5

DIRECTION 6

2936

3-PHASE BRUSHLESS DC MOTOR CONTROLLER/DRIVERS

Combining logic and power, the UDN2936W and UDN2936W-120 provide commutation and drive for three-phase brushless dc motors. Each of the three outputs are rated at 45 V and ± 2 A (± 3 A peak), and include internal ground clamp and flyback diodes. These drivers also feature internal commutation logic, PWM current control, and thermal shutdown protection.

The UDN2936W and UDN2936W-120 are compatible with single-ended digital or linear Hall effect sensors. The commutating logic is programmed for 60° (UDN2936W) or 120° (UDN2936W-120) electrical separation. Current control is accomplished by sensing current through an external sense resistor and pulse-width modulating the source drivers. Voltage thresholds and hysteresis can be externally set by the user. If desired, internal threshold and hysteresis defaults (300 mV, 7.5 percent) can be used. The UDN2936W/W-120 also include braking and direction control. Internal protection circuitry prevents crossover current when braking or changing direction.

Both devices are also available for operation between -40°C and +85°C. To order, change the prefix from 'UDN' to 'UDQ'.

For maximum power-handling capability, the UDN2936W and UDN2936W-120 are supplied in 12-pin single in-line power-tab packages. An external heat sink may be required for high-current applications. The tab is at ground potential and needs no insulation.

ABSOLUTE MAXIMUM RATINGS at T_{.1} ≤ +150°C

COMMUTATION LOGIC

Dwg. No W-188

| Supply Voltage, V _{BB} 45 V |
|---|
| Output Current, I _{OUT} |
| (continuous) ±2.0 A |
| (peak) ±3.0 A |
| Input Voltage Range, V_{IN} 0.3 V to 15 V |
| Threshold Voltage, V _{THS} 15 V |
| Package Power Dissipation, |
| P _D See Graph |
| Operating Temperature Range, |
| T _A 20°C to +85°C |
| Storage Temperature Range, |
| T _S 55°C to +150°C |

Note: Output current rating may be limited by duty cycle, ambient temperature, and heat sinking. Under any set of conditions, do not exceed the specified peak current and a junction temperature of +150°C.

f.dzsc.com

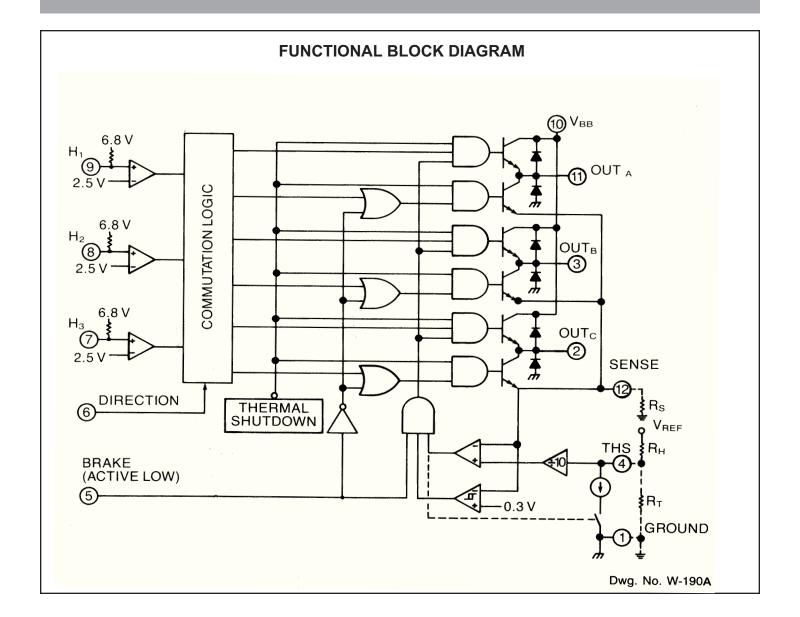
FEATURES

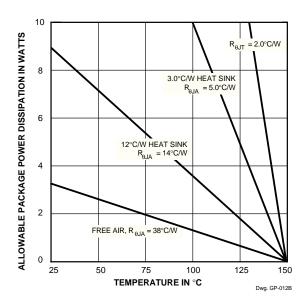
- 10 V to 45 V Operation
- ±3 A Peak Output Current
- Internal Clamp Diodes
- Internal PWM Current Control
- 60° or 120° Commutation Decoding Logic
- Thermal Shutdown Protection
- Compatible with Single-Ended or Differential Hall-Effect Sensors
- Braking and Direction Control

Always order by complete part number:

| | • |
|--------------|-------------------------------|
| Part Number | Sensor Inputs |
| UDN2936W | Single-Ended, 60° Separation |
| UDN2936W-120 | Single-Ended, 120° Separation |







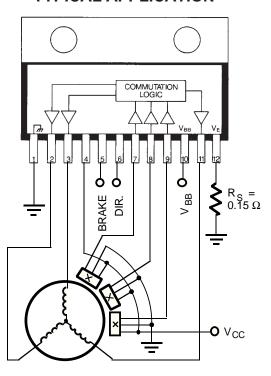
COMMUTATION TRUTH TABLE UDN2936W

| Hall S | Hall Sensor Inputs | | | | Outputs | <u> </u> | |
|----------------|--------------------|----------------|-----------|-------|---------|----------|------------------|
| H ₁ | H_2 | H ₃ | DIRECTION | BRAKE | OUT | OUT_B | OUT _C |
| High | High | High | Low | High | Z | Low | High |
| High | High | Low | Low | High | High | Low | Z |
| High | Low | Low | Low | High | High | Z | Low |
| Low | Low | Low | Low | High | Z | High | Low |
| Low | Low | High | Low | High | Low | High | Z |
| Low | High | High | Low | High | Low | Z | High |
| High | High | High | High | High | Z | High | Low |
| High | High | Low | High | High | Low | High | Z |
| High | Low | Low | High | High | Low | Z | High |
| Low | Low | Low | High | High | Z | Low | High |
| Low | Low | High | High | High | High | Low | Z |
| Low | High | High | High | High | High | Z | Low |
| X | X | X | X | Low | Low | Low | Low |

X= Irrelevant

Z = High Impedance

TYPICAL APPLICATION



COMMUTATION TRUTH TABLE UDN2936W-120

| Hall Sensor Inputs | | | | | | Outputs | |
|--------------------|----------------|----------------|-----------|-------|------|---------|------------------|
| H ₁ | H ₂ | H ₃ | DIRECTION | BRAKE | OUT | OUTB | OUT _c |
| High | Low | High | Low | High | Z | Low | High |
| High | Low | Low | Low | High | High | Low | Z |
| High | High | Low | Low | High | High | Z | Low |
| Low | High | Low | Low | High | Z | High | Low |
| Low | High | High | Low | High | Low | High | Z |
| Low | Low | High | Low | High | Low | Z | High |
| High | Low | High | High | High | Z | High | Low |
| High | Low | Low | High | High | Low | High | Z |
| High | High | Low | High | High | Low | Z | High |
| Low | High | Low | High | High | Z | Low | High |
| Low | High | High | High | High | High | Low | Z |
| Low | Low | High | High | High | High | Z | Low |
| Х | Х | Х | Х | Low | Low | Low | Low |

X= Irrelevant

Z = High Impedance

Dwg. EP-033

ELECTRICAL CHARACTERISTICS at T $_{\rm A}$ = +25 $^{\circ}$ C, T $_{\rm J}$ \leq +150 $^{\circ}$ C, V $_{\rm BB}$ = 45 V

| | | | Limits | | | |
|-----------------------------|-----------------|----------------------------|--------|------|------|-------|
| Characteristic | Symbol | Test Conditions | Min. | Тур. | Max. | Units |
| Supply Voltage Range | V_{BB} | Operating | 10 | _ | 45 | V |
| Supply Current | I _{BB} | Outputs Open | _ | 32 | 40 | mA |
| | | V _{BRAKE} = 0.8 V | _ | 42 | 50 | mA |
| Thermal Shutdown Temp. | T_J | | _ | 165 | _ | °C |
| Thermal Shutdown Hysteresis | ΔT_J | | _ | 25 | _ | °C |

Output Drivers

| Output Leakage Current | I _{CEX} | $V_{OUT} = V_{BB}$ | - | _ | 50 | μΑ |
|-----------------------------|----------------------|---|-----|------|-----|----|
| | | V _{OUT} = 0 V | | _ | -50 | μА |
| Output Saturation Voltage | V _{CE(SAT)} | I _{OUT} = -1 A | _ | 1.7 | 1.9 | V |
| | | I _{OUT} = +1 A | - | 1.1 | 1.3 | V |
| | | I _{OUT} = -2 A | _ | 1.9 | 2.1 | V |
| | | I _{OUT} = +2 A | - | 1.4 | 1.6 | V |
| Output Sustaining Voltage | V _{CE(sus)} | $I_{OUT} = \pm 2 \text{ A, L} = 2 \text{ mH}$ | 45 | _ | _ | V |
| Clamp Diode Forward Voltage | V _F | I _F = 2 A | - | 1.8 | 2.0 | V |
| Clamp Diode Leakage Current | I _R | V _R = 45 V | _ | _ | 50 | μА |
| Output Switching Time | t _r | I _{OUT} = ±2 A, Resistive Load | - | 2.0 | _ | μs |
| | t _f | I _{OUT} = ±2 A, Resistive Load | _ | 2.0 | _ | μs |
| Turn-ON Delay | t _{on} | Source Drivers, 0 to -2 A | T — | 1.25 | _ | μs |
| (Resistive Load) | | Sink Drivers, 0 to +2 A | _ | 1.9 | _ | μs |
| Turn-OFF Delay | t _{off} | Source Drivers, -2 A to 0 | - | 1.7 | _ | μs |
| (Resistive Load) | | Sink Drivers, +2 A to 0 | - | 0.9 | _ | μs |
| | | | | | | |

Continued next page...

ELECTRICAL CHARACTERISTICS at T $_{\! A}$ = +25°C, T $_{\! J}$ \leq +150°C, V $_{\! BB}$ = 45 V continued

| | | | Limits | | | |
|--------------------------------|--------------------|--|--------|-------|------|-------|
| Characteristic | Symbol | Test Conditions | Min. | Тур. | Max. | Units |
| Control Logic | | | | | | |
| Logic Input Voltage | V _{IN(1)} | V_{DIR} or V_{BRAKE} | 2.0 | _ | _ | V |
| | V _{IN(0)} | V_{DIR} or V_{BRAKE} | _ | _ | 8.0 | V |
| Sensor Input Voltage Threshold | V _{IN} | H ₁ , H ₂ , or H ₃ | _ | 2.5 | _ | V |
| Input Current | I _{IN(1)} | V _{DIR} = 2 V | _ | 150 | 200 | μΑ |
| | | V _{BRAKE} = 2 V | _ | <1.0 | 5.0 | μΑ |
| | | V _H = 5 V | _ | -190 | -220 | μΑ |
| | I _{IN(0)} | V _{DIR} = 0.8 V | _ | 35 | 50 | μΑ |
| | | V _{BRAKE} = 0.8 V | _ | -5.0 | -20 | μΑ |
| | | V _H = 0.8 V | _ | -0.64 | -1.0 | mA |
| | I _{THS} | $V_{THS} \ge 3.0 \text{ V}$ | _ | -8.0 | -15 | μΑ |
| | | $V_{THS} < 3.0 \text{ V}, V_{SENSE} < V_{THS}/10.5$ | _ | -15 | -30 | μΑ |
| | | $V_{THS} < 3.0 \text{ V}, V_{SENSE} > V_{THS}/9.5$ | 190 | 250 | 310 | μΑ |
| Current Limit Threshold | _ | V_{THS}/V_{SENSE} at trip point, $V_{THS} < 3.0 \text{ V}$ | 9.5 | 10 | 10.5 | _ |
| Default Sense Trip Voltage | V _{SENSE} | V _{THS} ≥ 3.0 V | 270 | 300 | 330 | mV |
| Default Hysteresis | _ | V _{THS} ≥ 3.0 V | _ | 7.5 | _ | % |
| Deadtime | t _d | BRAKE or DIRECTION | _ | 2.0 | _ | μs |

APPLICATIONS INFORMATION

The UDN2936W and UDN2936W-120 power drivers provide commutation logic and power outputs to drive three-phase brushless dc motors.

The UDN2936W and UDN2936W-120 are designed to interface with single-ended linear or digital Hall-effect devices (HEDs). Internal pull-up resistors allow for direct use with open-collector digital HEDs. The $H_{\rm N}$ inputs have 2.5 V thresholds.

The commutation logic provides decoding for HEDs with 60° (UDN2936W) or 120° (UDN2936W-120) electrical separation. At any one step in the logic sequencing, one half-bridge driver is sourcing current, one driver is sinking current, and one driver is in a high-impedance state (see Truth Table).

A logic low on the BRAKE pin turns ON the three sink drivers and turns OFF the three source drivers, essentially shorting the motor windings to ground. During braking, the back-electromotive force generated by the motor produces a current that dynamically brakes the motor. Depending upon the rotational velocity of the motor, this current can approach the locked rotor current level (which is limited only by the motor winding resistance). During braking, the output current-limiting circuitry is disabled and care should be taken to ensure that the back-EMF generated brake current does not exceed the maximum rating (3 A peak) of the sink drivers and ground clamp diodes.

Changing the logic level of the DIRECTION pin inverts the output states, thus reversing the direction of the motor. Changing the direction of a rotating motor produces a back-EMF current similar to when braking the motor. The load current should not be allowed to exceed the maximum rating (±3 A peak) of the drivers.

An internally generated dead time (t_d) of approximately 2 μs prevents potentially destructive crossover currents that can occur when changing direction or braking.

Motor current is internally controlled by pulse-width modulating the source drivers with a preset hysteresis format. Load current through an external sense resistor (R_S) is constantly monitored. When the current reaches the set trip point (determined by an external reference voltage or internal default), the source driver is disabled. Current recirculates through the ground clamp diode,

motor winding, and sink driver. An internal constantcurrent sink reduces the trip point (hysteresis). When the decaying current reaches this lower threshold, the source driver is enabled again and the cycle repeats.

Thresholds and hysteresis can be set with external resistors, or internal defaults can be used. With $V_{THS} > 3.0 \text{ V}$, the trip point is internally set at 300 mV with 7.5% hysteresis. Load current is then determined by the equation:

$$I_{TRIP} = 0.3/R_{S}$$

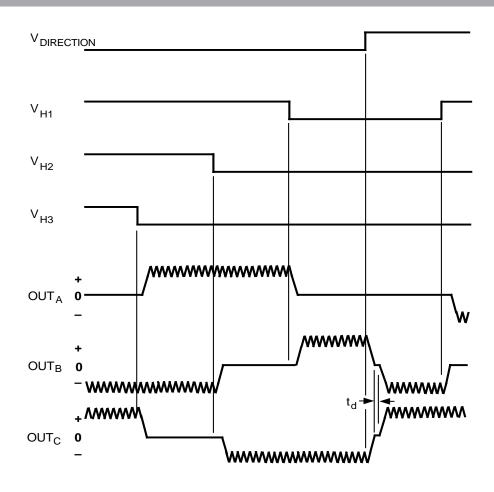
With V_{THS} < 3.0 V, the threshold, hysteresis percentage, and peak current are set with external resistors according to the equations:

threshold voltage (
$$V_{THS}$$
) = $V_{REF} \cdot R_T / (R_H + R_T)$
hysteresis percentage = $R_H / 50 V_{REF}$
load trip current (I_{TRIP}) = $V_{THS} / 10 R_S$

Percentage hysteresis is a fixed value independent of load current. The chopping frequency is a function of circuit parameters including load inductance, load resistance, supply voltage, hysteresis, and switching speed of the drivers.

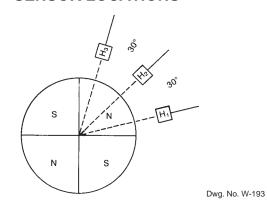
The UDN2936W and UDN2936W-120 outputs are rated for normal operating currents of up to ± 2 A and startup currents to ± 3 A (see cautions above regarding braking and changing of motor direction). Internal power ground-clamp and flyback diodes protect the outputs from the voltage transients that occur when switching inductive loads. All devices also feature thermal protection circuitry. If the junction temperature reaches +165°C, the thermal shutdown circuitry turns OFF all output drivers. The outputs are re-enabled when the junction cools down to approximately +140°C. This protection is only intended to protect the device from failures due to excessive junction temperature or loss of heat sinking and should not imply that output short circuits are permitted.

As with all high-power integrated circuits, the printed wiring board should utilize a heavy ground plane. For optimum performance, the drivers should be soldered directly into the board. The power supply should be decoupled with an electrolytic capacitor (>10 μ F) as close as possible to the device supply pin (V_{BB}).

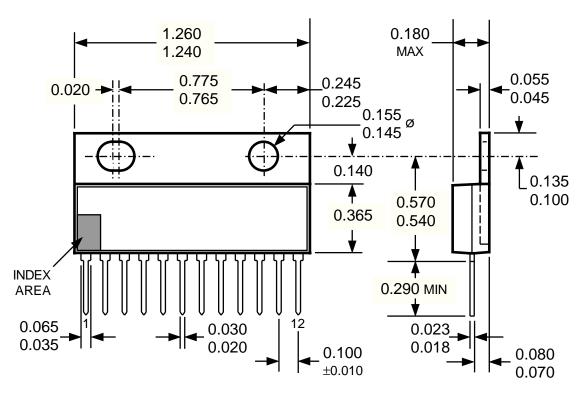


Dwg. WM-002-1

TYPICAL HALL EFFECT SENSOR LOCATIONS



Dimensions in Inches (controlling dimensions)

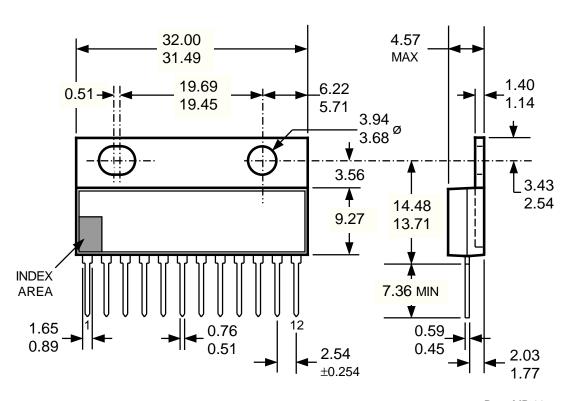


Dwg. MP-007 in

NOTES: 1. Lead thickness is measured at seating plane or below.

- Lead spacing tolerance is non-cumulative
 Exact body and lead configuration at vendor's option within limits shown.
- 4. Lead gauge plane is 0.030" below seating plane.

Dimensions in Millimeters (for reference only)



Dwg. MP-007 mm

NOTES: 1. Lead thickness is measured at seating plane or below.

- 2. Lead spacing tolerance is non-cumulative
- 3. Exact body and lead configuration at vendor's option within limits shown.
- 4. Lead gauge plane is 0.762 mm below seating plane.

MOTOR DRIVERS SELECTION GUIDE

| Function | Output R | atings * | Part Number † | | | | |
|---|--------------|------------|-------------------|--|--|--|--|
| INTEGRATED CIRCUITS FOR BRUSHLESS DC MOTORS | | | | | | | |
| 3-Phase Controller/Drivers | ±2.0 A | 45 V | 2936 and 2936-120 | | | | |
| Hall-Effect Latched Sensors | 10 mA | 24 V | 3175 and 3177 | | | | |
| 2-Phase Hall-Effect Sensor/Controller | 20 mA | 25 V | 3235 | | | | |
| Hall-Effect Complementary-Output Sensor | 20 mA | 25 V | 3275 | | | | |
| 2-Phase Hall-Effect Sensor/Driver | 900 mA | 14 V | 3625 | | | | |
| 2-Phase Hall-Effect Sensor/Driver | 400 mA | 26 V | 3626 | | | | |
| Hall-Effect Complementary-Output Sensor/Driver | 300 mA | 60 V | 5275 | | | | |
| 3-Phase Back-EMF Controller/Driver | ±900 mA | 14 V | 8902–A | | | | |
| 3-Phase Controller/DMOS Driver | ±4.0 A | 14 V | 8925 | | | | |
| 3-Phase Back-EMF Controller/Driver | ±1.0 A | 7 V | 8984 | | | | |
| INTEGRATED BRIDGE DRIVERS | FOR DC AND | BIPOLAR ST | TEPPER MOTORS | | | | |
| PWM Current-Controlled Dual Full Bridge | ±750 mA | 45 V | 2916 | | | | |
| PWM Current-Controlled Dual Full Bridges | ±1.5 A | 45 V | 2917 and 2918 | | | | |
| PWM Current-Controlled Dual Full Bridge | ±750 mA | 45 V | 2919 | | | | |
| Dual Full-Bridge Driver | ±2.0 A | 50 V | 2998 | | | | |
| PWM Current-Controlled Full Bridge | ±2.0 A | 50 V | 3952 | | | | |
| PWM Current-Controlled Full Bridge | ±1.3 A | 50 V | 3953 | | | | |
| PWM Current-Controlled Microstepping Full Bridges | ±1.5 A | 50 V | 3955 and 3957 | | | | |
| PWM Current-Controlled Dual Full Bridge | ±800 mA | 33 V | 3964 | | | | |
| PWM Current-Controlled Dual Full Bridge | ±650 mA | 30 V | 3966 and 3968 | | | | |
| PWM Current-Controlled Dual Full Bridge | ±750 mA | 45 V | 6219 | | | | |
| OTHER INTEGRATED CIRCU | JIT & PMCM I | MOTOR DRIV | ERS | | | | |
| Unipolar Stepper-Motor Quad Driver | 1.8 A | 50 V | 2544 | | | | |
| Unipolar Stepper-Motor Translator/Driver | 1.25 A | 50 V | 5804 | | | | |
| Unipolar Stepper-Motor Quad Drivers | 1 A | 46 V | 7024 and 7029 | | | | |
| Unipolar Microstepper-Motor Quad Driver | 1.2 A | 46 V | 7042 | | | | |
| Voice-Coil Motor Driver | ±500 mA | 6 V | 8932–A | | | | |
| Voice-Coil Motor Driver | ±800 mA | 16 V | 8958 | | | | |
| Voice-Coil (and Spindle) Motor Driver | ±350 mA | 7 V | 8984 | | | | |

^{*} Current is maximum specified test condition, voltage is maximum rating. See specification for sustaining voltage limits or over-current protection voltage limits. Negative current is defined as coming out of (sourcing) the output.

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[†] Complete part number includes additional characters to indicate operating temperature range and package style.