

# MOS FIELD EFFECT TRANSISTOR 2SK3225

## SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

### DESCRIPTION

This product is N-Channel MOS Field Effect Transistor designed for high current switching applications.

### FEATURES

- Low On-State Resistance  $R_{DS(on)1} = 18 \text{ m}\Omega \text{ MAX.}$  (VGs = 10 V, ID = 17 A)  $R_{DS(on)2} = 27 \text{ m}\Omega \text{ MAX.}$  (VGs = 4.0 V, ID = 17 A)
- Low Ciss : Ciss = 2100 pF TYP.
- Built-in Gate Protection Diode
- TO-251/TO-252 package

### ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

Vdss	60	V
VGSS(AC)	±20	V
VGSS(DC)	+20, -10	V
D(DC)	±34	А
D(pulse)	±136	А
P⊤	40	W
Pτ	2.0	W
Tch	150	°C
Tstg	-55 to +150	°C
	VGSS(AC) VGSS(DC) ID(DC) ID(pulse) PT PT Tch	VGSS(AC)         ±20           VGSS(DC)         +20, -10           ID(DC)         ±34           ID(pulse)         ±136           PT         40           PT         2.0           Tch         150

**Note** PW  $\leq$  10  $\mu$ s, Duty cycle  $\leq$  1 %

# THERMAL RESISTANCE

Channel to Case	Rth(ch-C)	3.13	°C/W
Channel to Ambient	Rth(ch-A)	125	°C/W

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## ORDERING INFORMATION

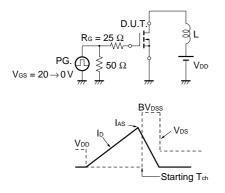
PART NUMBER	PACKAGE
2SK3225	TO-251
2SK3225-Z	TO-252

NEC

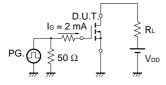
### ELECTRICAL CHARACTERISTICS (TA = 25 °C)

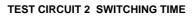
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, Id = 17 A		13	18	mΩ
	RDS(on)2	Vgs = 4.0 V, Id = 17 A		18	27	mΩ
Gate to Source Cut-off Voltage	V <sub>GS(off)</sub>	Vds = 10 V, Id = 1 mA	1.0	1.5	2.0	V
Forward Transfer Admittance	y <sub>fs</sub>	Vds = 10 V, Id = 17 A	13	27		S
Drain Leakage Current	loss	Vds = 60 V, Vgs = 0 V			10	μA
Gate to Source Leakage Current	lgss	$V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$			±10	μA
Input Capacitance	Ciss	V <sub>DS</sub> = 10 V		2100		pF
Output Capacitance	Coss	Vgs = 0 V		550		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		220		pF
Turn-on Delay Time	td(on)	ID = 17 A		32		ns
Rise Time	tr	$V_{GS(on)} = 10 V$		300		ns
Turn-off Delay Time	td(off)	Vdd = 30 V		110		ns
Fall Time	tr	R <sub>G</sub> = 10 Ω		140		ns
Total Gate Charge	Q <sub>G</sub>	ID = 34 A		45		nC
Gate to Source Charge	QGS	V <sub>DD</sub> = 48 V		7		nC
Gate to Drain Charge	Qgd	$V_{GS(on)} = 10 V$		13		nC
Body Diode Forward Voltage	VF(S-D)	IF = 34 A, Vgs = 0 V		0.94		V
Reverse Recovery Time	trr	If = 34 A, V <sub>GS</sub> = 0 V		60		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/µs		95		nC

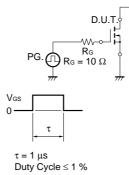
#### TEST CIRCUIT 1 AVALANCHE CAPABILITY

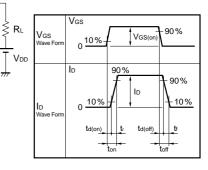


#### TEST CIRCUIT 3 GATE CHARGE



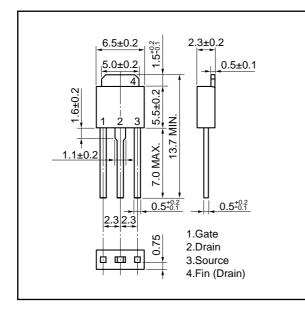


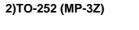


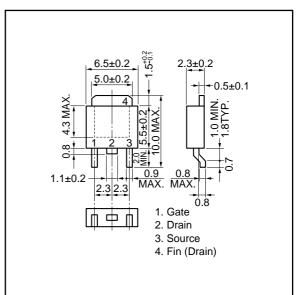


### PACKAGE DRAWINGS (Unit : mm)

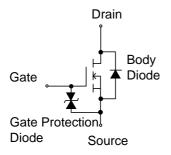
#### 1)TO-251 (MP-3)







#### EQUIVALENT CIRCUIT



**Remark** The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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