

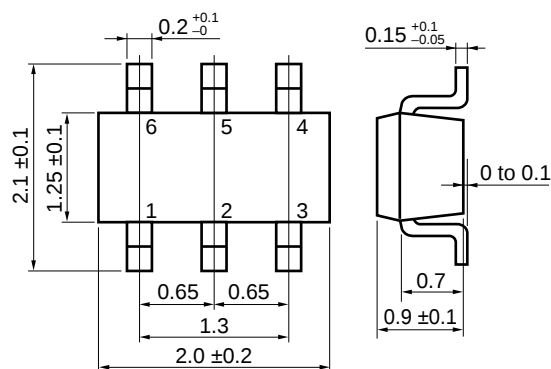
N-CHANNEL MOS FET ARRAY
FOR SWITCHING

The μ PA672T is a super-mini-mold device provided with two MOS FET elements. It achieves high-density mounting and saves mounting costs.

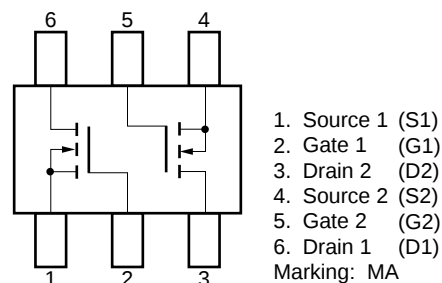
FEATURES

- Two MOS FET circuits in package the same size as SC-70
- Automatic mounting supported

PACKAGE DIMENSIONS (in millimeters)



PIN CONNECTION

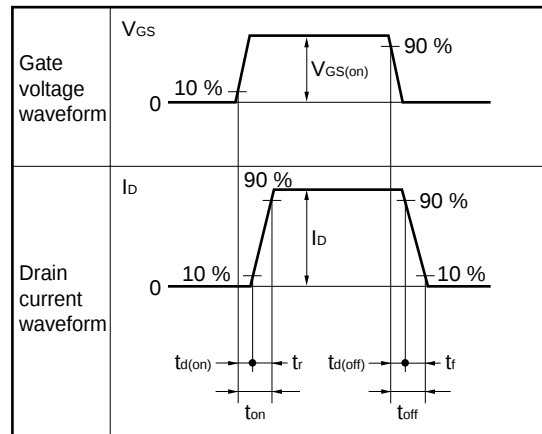
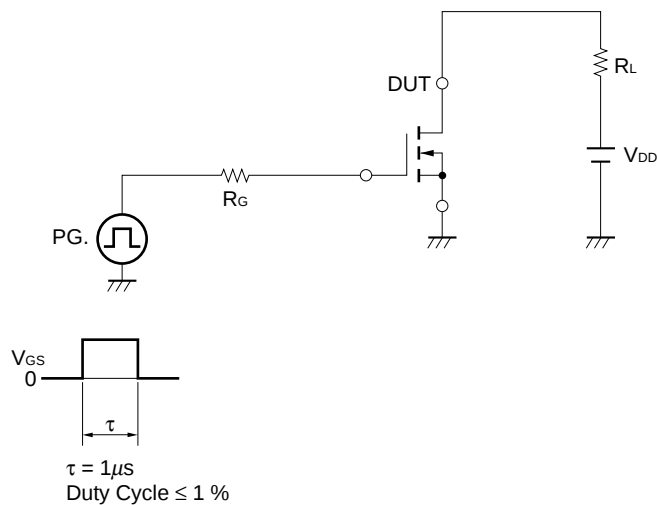


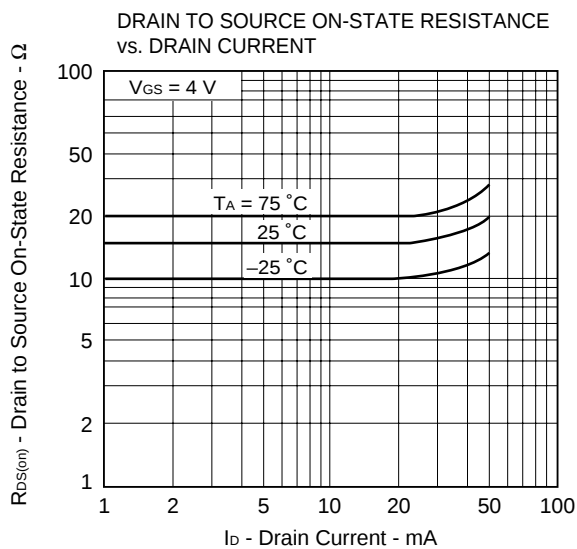
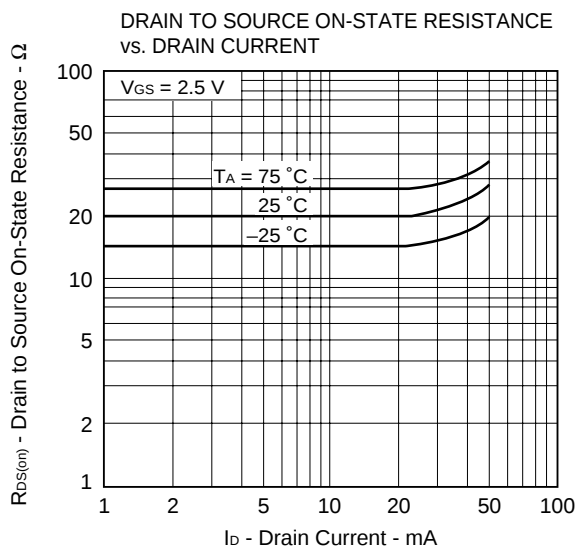
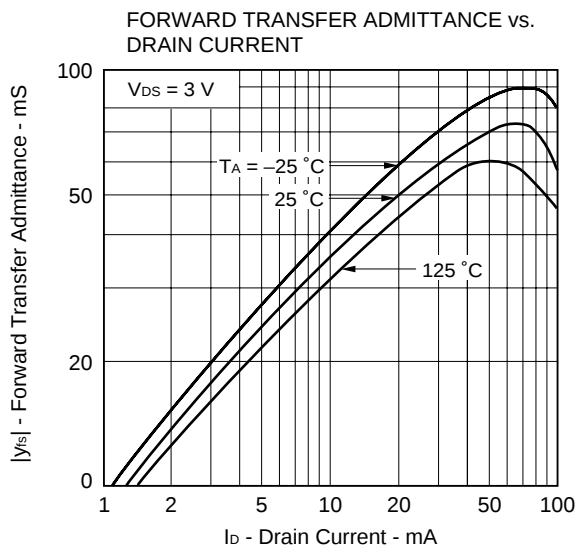
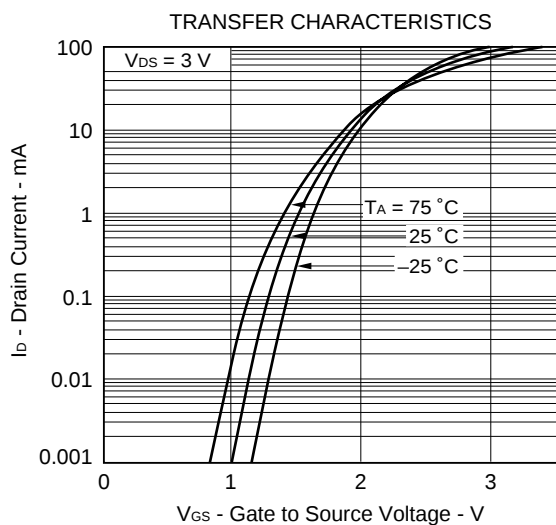
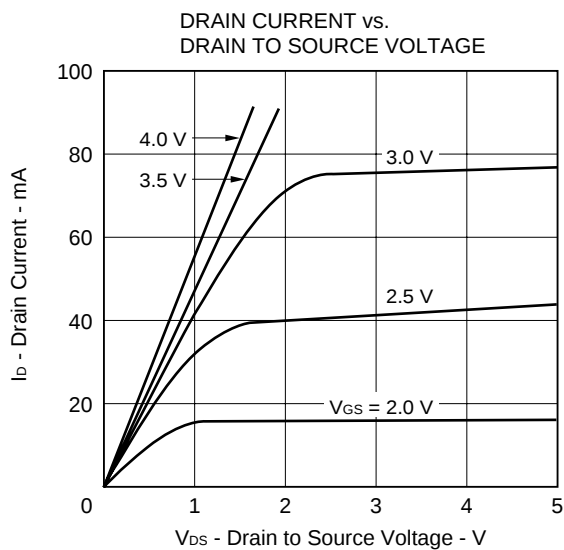
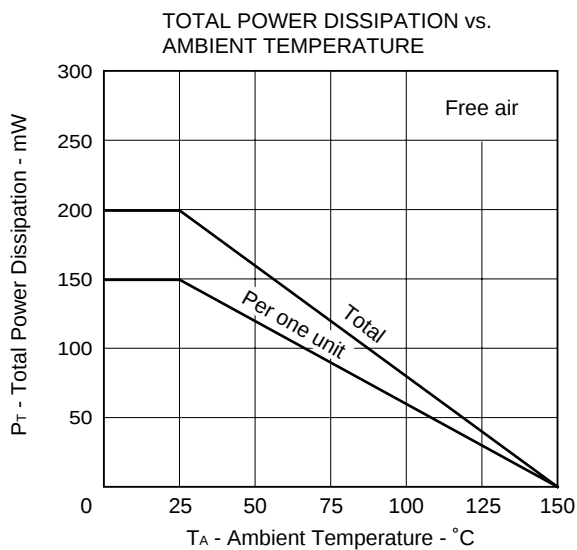
ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

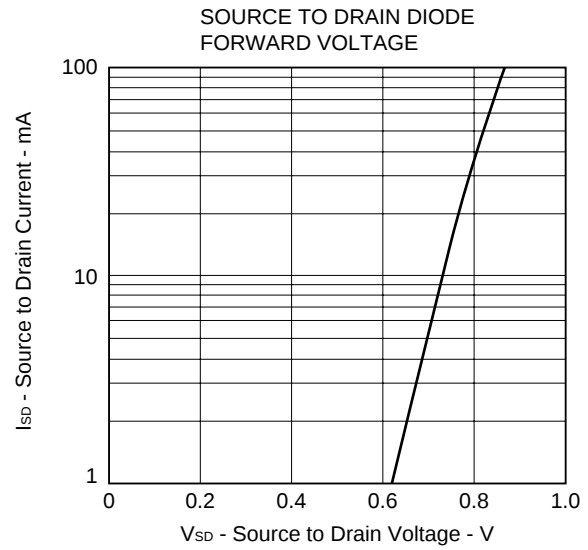
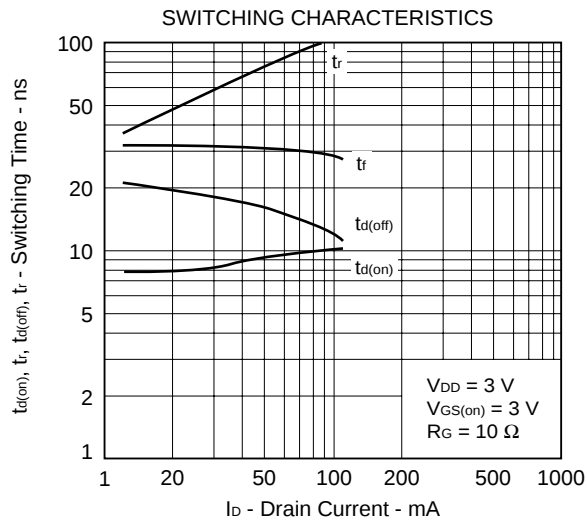
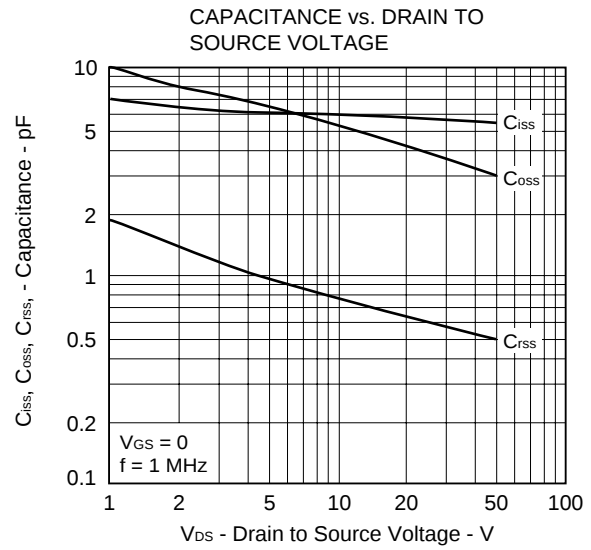
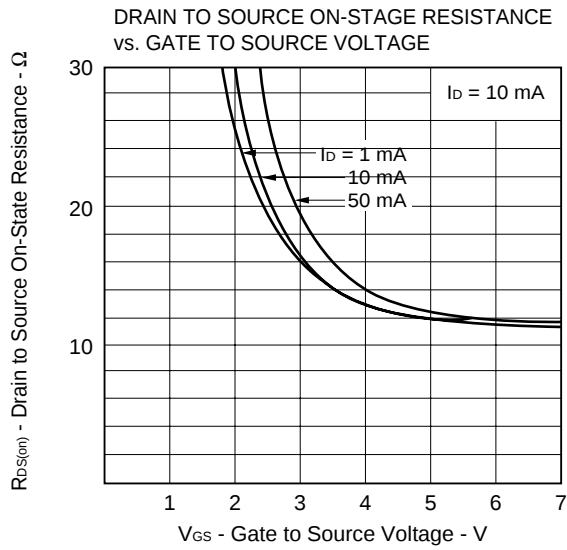
PARAMETER	SYMBOL	TEST CONDITIONS	RATINGS	UNIT
Drain to Source Voltage	V_{DS}		50	V
Gate to Source Voltage	V_{GS}		± 7.0	V
Drain Current (DC)	$I_{D(PC)}$		100	mA
Drain Current (pulse)	$I_{D(pulse)}$	$PW \leq 10 \text{ ms}$, Duty Cycle $\leq 50 \%$	200	mA
Total Power Dissipation	P_T		200 (Total)	mW
Channel Temperature	T_{ch}		150	$^\circ\text{C}$
Storage Temperature	T_{stg}		-55 to $+150$	$^\circ\text{C}$

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)

PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Cut-off Current	I_{DSS}	$V_{DS} = 50\text{ V}$, $V_{GS} = 0$			10	μA
Gate Leakage Current	I_{GSS}	$V_{GS} = \pm 7.0\text{ V}$, $V_{DS} = 0$			± 5.0	μA
Gate Cut-off Voltage	$V_{GS(off)}$	$V_{DS} = 3.0\text{ V}$, $I_D = 1.0\text{ }\mu\text{A}$	0.7	1.0	1.5	V
Forward Transfer Admittance	$ y_{fs} $	$V_{DS} = 3.0\text{ V}$, $I_D = 10\text{ mA}$	20			mS
Drain to Source On-State Resistance	$R_{DS(on)1}$	$V_{GS} = 2.5\text{ V}$, $I_D = 10\text{ mA}$		20	40	Ω
Drain to Source On-State Resistance	$R_{DS(on)2}$	$V_{GS} = 4.0\text{ V}$, $I_D = 10\text{ mA}$		15	20	Ω
Input Capacitance	C_{iss}	$V_{DS} = 3.0\text{ V}$, $V_{GS} = 0$, $f = 1.0\text{ MHz}$		6		pF
Output Capacitance	C_{oss}			8		pF
Reverse Transfer Capacitance	C_{rss}			1.2		pF
Turn-On Delay Time	$t_{d(on)}$	$V_{DD} = 3\text{ V}$, $I_D = 20\text{ mA}$, $V_{GS(on)} = 3\text{ V}$, $R_G = 10\text{ }\Omega$, $R_L = 120\text{ }\Omega$		9		ns
Rise Time	t_r			50		ns
Turn-Off Delay Time	$t_{d(off)}$			20		ns
Fall Time	t_f			40		ns

SWITCHING TIME MEASUREMENT CIRCUIT AND CONDITIONS

TYPICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$)



REFERENCE

Document Name	Document No.
NEC semiconductor device reliability/quality control system	TEI-1202
Quality grade on NEC semiconductor devices	IEI-1209
Semiconductor device mounting technology manual	C10535E
Guide to quality assurance for semiconductor devices	MEI-1202
Semiconductor selection guide	X10679E

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Special: Transportation equipment (automobiles, trains, ships, etc.), traffic control systems, anti-disaster systems, anti-crime systems, safety equipment and medical equipment (not specifically designed for life support)

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Anti-radioactive design is not implemented in this product.