

M52015FP

NTSC VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION

The M52015FP is a semiconductor integrated circuit for processing video signals in an NTSC system color LCD TV. It contains ACC, color signal demodulator, picture quality control, APC, VCXO, RGB matrix amplifier, tint and killer circuits.

FEATURES

- Low supply voltage, low power dissipation IC
- Picture soft/sharp-adjustable via picture quality control circuit
- Primary color contrast control
- 24-pin flat package

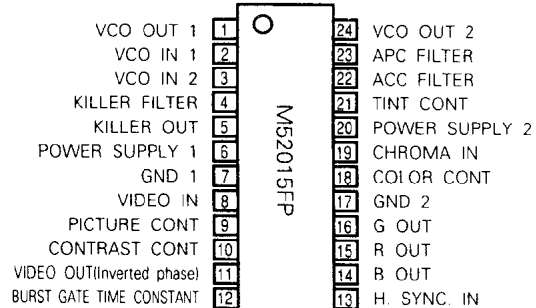
APPLICATION

LCD TV

RECOMMENDED OPERATING CONDITION

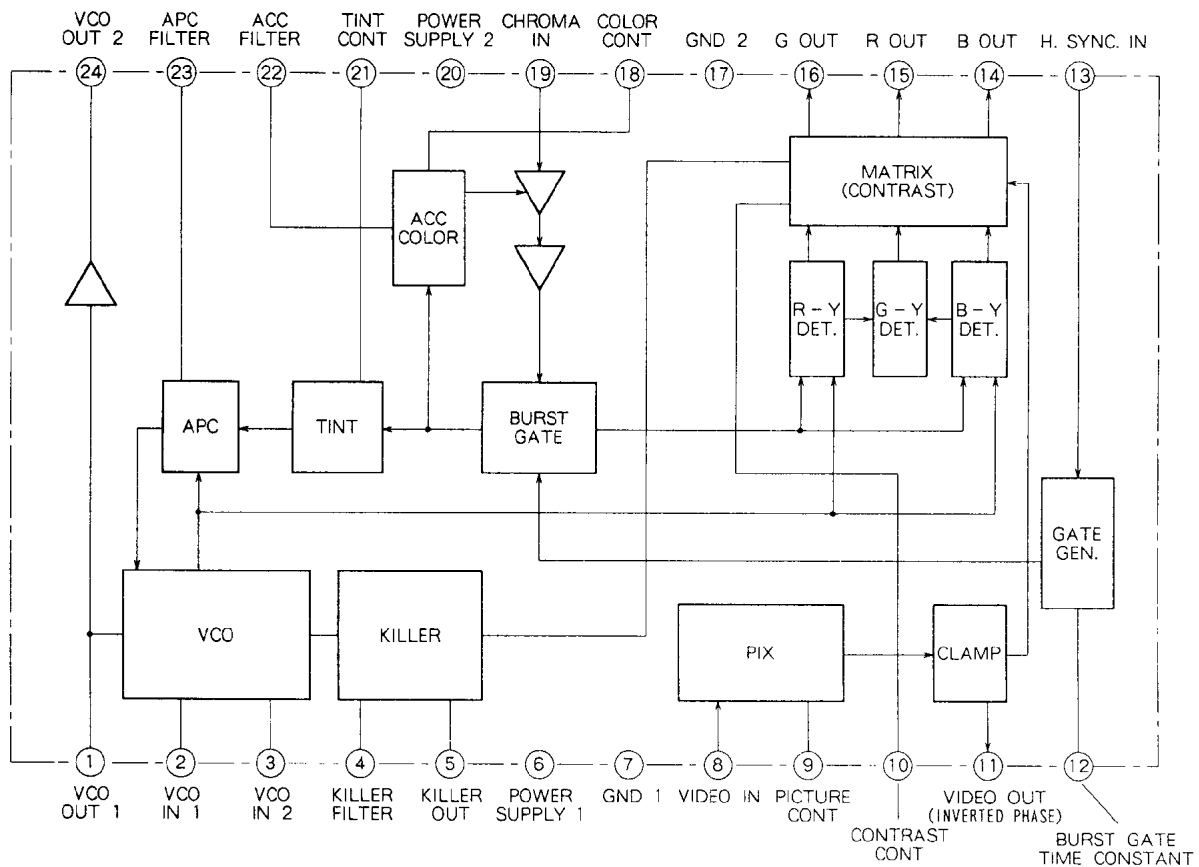
Supply voltage range3.5~4.5V
Rated supply voltage4.0V

PIN CONFIGURATION (TOP VIEW)



Outline 24P2W-D

BLOCK DIAGRAM



NTSC VIDEO CHROMA SIGNAL PROCESSOR

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Ratings	Unit
V _{CC}	Supply voltage	4.8	V
V _{IN2}	Input amplitude at pin ②	500	mV _{P-P}
I _{OUT5}	Output current at pin ⑤	800	mA
I _{OUT11}	Output current at pin ⑪	900	mA
V _{IN13}	Input voltage at pin ⑬	V _{CC} + 0.3	V
I _{OUT14} I _{OUT15} I _{OUT16}	Output current at pins ⑭, ⑮ and ⑯	900	mA
I _{OUT24}	Output current at pin ⑳	450	mA
P _d	Power dissipation	500	mW
T _{opr}	Operating temperature	-20~75	°C
T _{stg}	Storage temperature	-40~125	°C

ELECTRICAL CHARACTERISTICS (T_a = 25°C, V_{CC} = 4.0V, unless otherwise noted)

Symbol	Parameter	Test conditions/Method	Limits			Unit
		Circled numerals indicate pin numbers	Min.	Typ.	Max.	
I _{CC}	Circuit current	Measure input current with 4V _{DC} applied to pins ⑤ and ⑳.	8.0	13.0	19.0	mA
VIDEO SECTION *						
Y _{max}	Maximum output	Input 100kHz sine wave of 0.3V _{P-P} to pin ⑧, and measure output amplitude at pin ⑭ with 4V _{CC} at pin ⑩.	1.8	2.2		V _{P-P}
GY	Video amplifier gain	Input 100kHz sine wave of 0.3V _{P-P} to pin ⑧, measure output amplitude at pin ⑭ when voltage at pin ⑩ is 2V _{CC} , and calculate output/input amplitude ratio.	10.5	13.0	15.0	dB
Y _{CTST} (2)	Contrast control characteristics	Input 100kHz sine wave of 0.3V _{P-P} to pin ⑧, measure output amplitude at pin ⑭ when voltage at pin ⑩ is changed to 2, 0.5 and 0V _{CC} , and calculate ratio of measured amplitude to that in GY.	1.0	3.4	5.8	dB
Y _{CTST} (0.5)			-12.0	-5.5	0	dB
Y _{CTST} (0)				-23	-20	dB
Y _{TONE} (4)	Picture quality control characteristics	Input 1.5MHz sine wave of 0.3V _{P-P} to pin ⑧, measure output amplitude at pin ⑭ when voltage at pin ⑩ is changed to 2, 4 and 0V _{CC} with voltage at pin ⑩ set at 1V _{CC} , and calculate ratio of measured amplitude to that obtained with pin ⑩ voltage at 2V _{CC} .	-4.0	-2.7	-1.5	dB
Y _{TONE} (0)			3.0	6.3	10.0	dB
Y _{FREQ}	Frequency characteristics	Input 100kHz and 2MHz sine waves of 0.3V _{P-P} in sequence to pin ⑧, and measure output amplitude at pin ⑭ for each input with voltages at pins ⑩ and ⑪ set at 2V _{CC} and 1V _{CC} , respectively. Then calculate output amplitude ratio for 2MHz/100kHz input.	-9.5	-6.5	-4.5	dB
CHROMA SECTION *						
C _{max}	Maximum output	Input sine wave of 0dB to pin ⑩, and measure output amplitude at pin ⑭ when voltage at pins ⑩ and ⑪ is 4V _{CC} . (See Note 2.)	2.0	2.4	3.0	V _{P-P}
G _C	Chroma maximum gain	Input sine wave of -26dB to pin ⑩, and measure output amplitude at pin ⑭ when voltage at pins ⑩ and ⑪ is 4V _{CC} . (See Note 2.)	45	51	57	dB
C _{ACC} (+6)	ACC control characteristics	Input sine waves of 0, +6 and -20dB in sequence to pin ⑩, and measure output amplitude at pin ⑭ for each input with voltages at pins ⑩ and ⑪ set at 1V _{CC} and 1.5V _{CC} , respectively. Then calculate ratio of measured amplitude to that for 0dB input. (See Note 2.)	0	0.1	1.0	dB
C _{ACC} (-20)			-5.0	-2.0	0	dB
C _{KLR}	Killer operating input	Input sine wave of 0dB to pin ⑩, decrease input amplitude until voltage at pin ⑤ becomes 2.9V _{CC} or more, and measure input amplitude. Then calculate ratio of measured amplitude to that for 0dB input. (See Note 1.)	-55	-45	-35	dB
D _{KLR}	Killer color residual	Input sine wave of 0dB to pin ⑩, and measure output amplitude at pin ⑭ when voltages at pins ⑩ and ⑪ are 1V _{CC} and 1.5V _{CC} , respectively. (See Note 4.)	0	10	25	mV _{P-P}
C _{SAT} (4)	Color saturation control characteristics	Input sine wave of 0dB to pin ⑩, and measure output amplitude at pin ⑭ when voltage at pin ⑩ is changed to 1.5, 4, 2, 1 and 0.5V _{CC} with voltage at pin ⑩ set at 1V _{CC} . Then calculate ratio of measured amplitude to that obtained when voltage at pin ⑩ is 1.5V _{CC} . (See Note 2.)	1.2	2.8	4.8	dB
C _{SAT} (2)			0.5	2.3	4.8	dB
C _{SAT} (1)			-6.5	-4.0	-0.5	dB
C _{SAT} (0.5)			-17.5	-12.0	-8.0	dB
Δf _{VCO}	VCO free run frequency	Input synchronization signal only, measure oscillation frequency at pin ②, and calculate difference from 3.579545MHz. (Pin ⑨=No input)	-950	0	+950	Hz
Δf _{VCOpull}	APC pull-in range	Input sine wave of 0dB to pin ⑩, and change frequency. Measure frequency at which DC voltage ⑤ changes from H to L. (See Note 5.)	+300	+550	+900	Hz
D _B	B demodulator sensitivity	Input sine wave of 0dB to pin ⑩, and measure output amplitude at pin ⑭ when voltages at pins ⑩ and ⑪ are 1V _{CC} and 1.5V _{CC} , respectively. (See Note 2.)	1.0	1.4	1.8	V _{P-P}

M52015FP

NTSC VIDEO CHROMA SIGNAL PROCESSOR

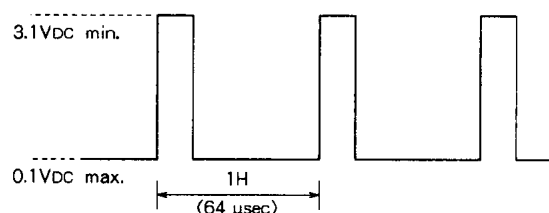
ELECTRICAL CHARACTERISTICS (cont.)

Symbol	Parameter	Test conditions/ Method	Limits			Unit
		Circled numerals indicate pin numbers	Min.	Typ.	Max.	
R (R/B)	Demodulated output voltage ratio	Input sine wave of 0dB to pin ⑧, measure output amplitude at pin ⑭ when voltages at pins ⑩ and ⑪ are 1V _{cc} and 1.5V _{cc} , respectively, and calculate ratio of measured amplitude to that obtained in test 15. (See Note 2.)	0.5	0.6	0.7	—
R (G/B)			0.28	0.35	0.42	—
D _{leakB}	Demodulated output carrier leak	Input sine wave of 0dB to pin ⑧, and measure output amplitude at pins ⑭, ⑮ and ⑯ for 7.1MHz component when voltages at pins ⑩ and ⑪ are 1V _{cc} and 1.5V _{cc} , respectively. (See Note 3.)	0	8	20	mV _{P-P}
D _{leakR}			0	8	25	mV _{P-P}
D _{leakG}			0	8	20	mV _{P-P}
V _{SKLR-H}	H voltage at KILLER OUT pin	Input synchronization signal alone, and measure DC voltage at pin ⑤ when pin ④ is connected via 10kΩ to GND(L) and when pin ④ is connected via 10kΩ to V _{CC} (H). (Pin ②=No input)	3.0	3.3	4.0	V _{cc}
V _{SKLR-L}	L voltage at KILLER OUT pin		0	0.1	0.3	V _{cc}
T	Tint control variation	Input sine wave of 0dB to pin ⑧, and measure phase variation at pin ⑭ when voltage at pin ⑫ is changed from 0 to 4V _{cc} , with voltages at pins ⑩ and ⑪ set at 1V _{cc} and 1.5V _{cc} , respectively.	80	125		deg
T _{min}	Tine control characteristics	Input sine wave of 0dB to pin ⑧, and measure phase variation at pin ⑭ when voltage at pin ⑫ is changed from 2 to 0.5V _{cc} and from 2 to 3.5V _{cc} with the voltages at pins ⑩ and ⑪ set at 1V _{cc} and 1.5V _{cc} , respectively.	- 83	- 57	- 31	deg
T _{max}			44	66	90	deg
Y OUTPUT SECTION						
V _{cont}	Voltage for no signal input	Measure output DC voltage at pin ⑪ when no signal is input	2.50	2.75	3.00	V _{cc}
G _{co}	Luminance amplifier gain	Input 100kHz sine wave of 0.1V _{P-P} to pin ⑧, measure output amplitude at pin ⑪, and calculate measured/input amplitude ratio.	11.5	14.0	16.5	dB
G _{max}	Maximum output	Input 100kHz sine wave of 0.5V _{P-P} to pin ⑧, and measure output amplitude at pin ⑪.	1.7	2.5		V _{P-P}

ELECTRICAL CHARACTERISTIC TEST METHOD

* Video section..... Unless otherwise specified, measure with 2V_{cc} at pin ⑨, 0V_{cc} at pin ⑩ and 2V_{cc} at pin ⑪.

Chroma section .. Unless otherwise specified, measure with 2V_{cc} at pin ⑨, 2V_{cc} at pin ⑪ and 2.85V_{cc} at pin ⑩, and with following synch signal input to pin ⑬.



Notes:1 Input sine wave signal with frequency of 3.579545MHz to pin ⑧. Input level of 100mV_{P-P} is set at 0dB.

Notes:2 Same as Note 1. Beat output (input frequency: 3.580545MHz) by VCO is measured for each output. Pin ④ is connected via 10kΩ to GND.

Notes:3. Same as Note 1. Pin ④ is connected via 10kΩ to GND.

Notes:4. Same as Note 1. Pin ④ is connected via 10kΩ to V_{cc}.

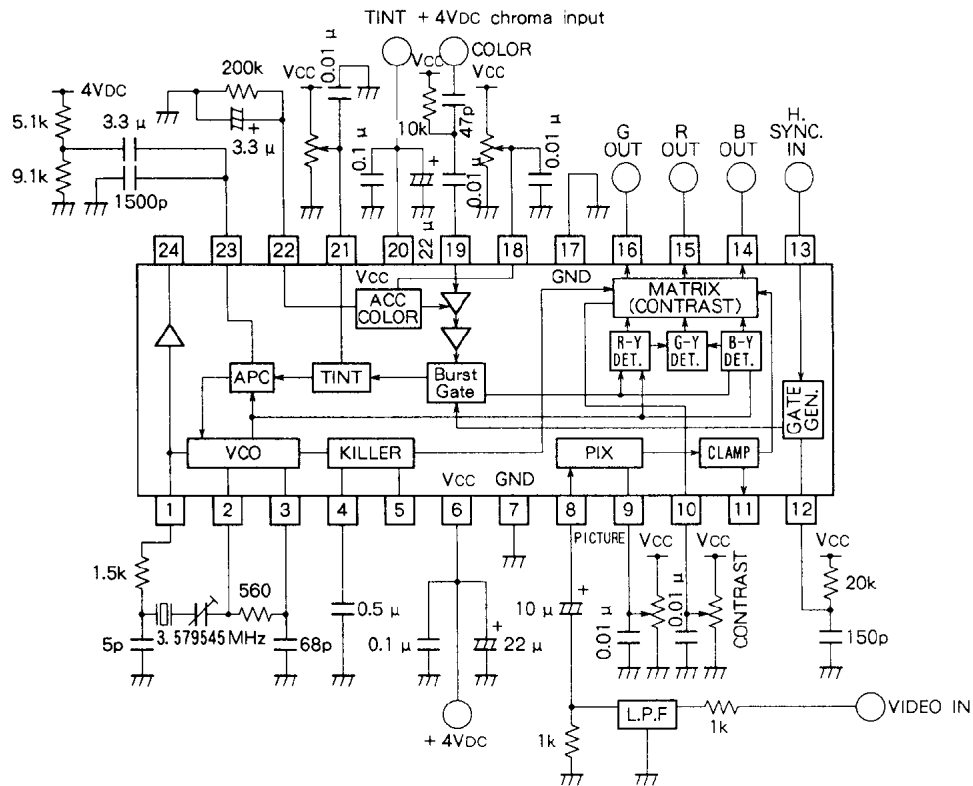
Notes:5. Same as Note 1. Increase input frequency, and measure frequency at which Pin ⑤ DC voltage changes from H (3.2V_{cc}) to L (locked state). Take same measurement after decreasing input frequency. Calculate difference between each measured input frequency and free run frequency. When adjusting free run frequency, connect CHROMA INPUT pin ⑩ with POWER SUPPLY 2 pin ⑫ via 0.1μF, and input signal with no chroma component. Measure oscillation frequency at pin ⑭. Adjust typical trimmer capacitor of X'tal circuit to set free run frequency at 3.579545MHz; and maintain this frequency during test.

Units Resistance: Ω
Capacitance: F

The graph shows the power dissipation capability of the 7805 voltage regulator as a function of ambient temperature. The y-axis represents Power Dissipation P_d in Watts (W), ranging from 0 to 0.6. The x-axis represents Ambient Temperature T_a in degrees Celsius ($^{\circ}\text{C}$), ranging from 0 to 150. A solid line indicates a maximum power dissipation of 0.5 W for temperatures up to 25°C. Beyond 25°C, the power dissipation capability decreases linearly, represented by a dashed line that reaches 0 W at 125°C. The two lines intersect at 75°C, where the power dissipation is 0.25 W.

Ambient Temperature T_a ($^{\circ}\text{C}$)	Power Dissipation P_d (W)
0	0.5
25	0.5
75	0.25
125	0

APPLICATION EXAMPLE

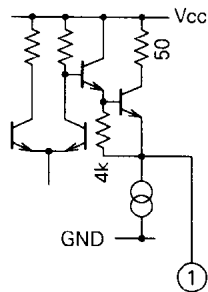
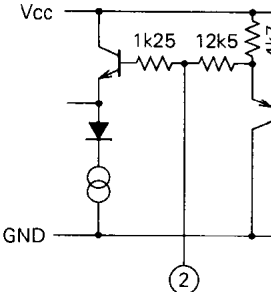
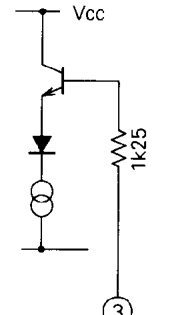
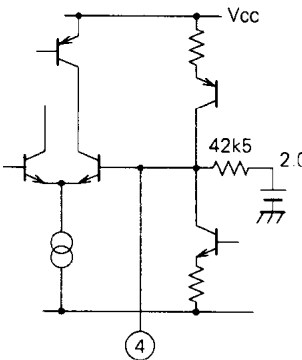
Units Resistance: Ω

Capacitance: F

M52015FP

NTSC VIDEO CHROMA SIGNAL PROCESSOR

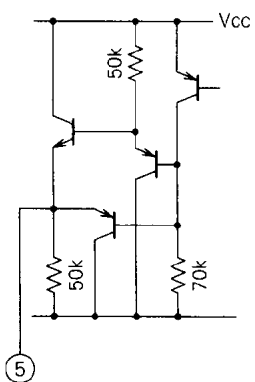
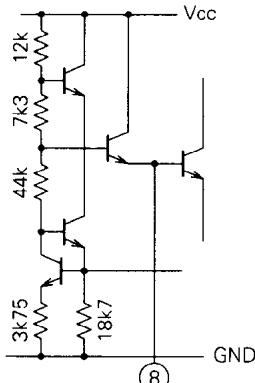
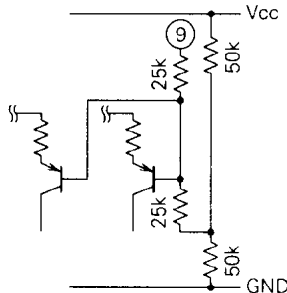
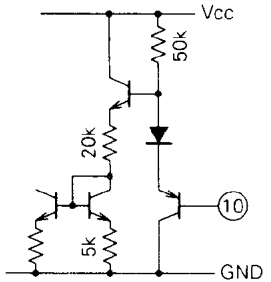
DESCRIPTION OF PIN

Pin No.	Name	Voltage and wave information	Peripheral circuit of pins
①	VCXO OUT 1	2.3V Emitter follower output $Z_o \approx 10\Omega$	
②	VCXO IN 1	3.3V $Z_i = 17K\Omega$	
③	VCXO IN 2	Open base input $Z_i > 100K\Omega$	
④	KILLER FILTER	3.0V $Z_i > 100K\Omega$ 42K5 with B.G.P. ON	

M52015FP

NTSC VIDEO CHROMA SIGNAL PROCESSOR

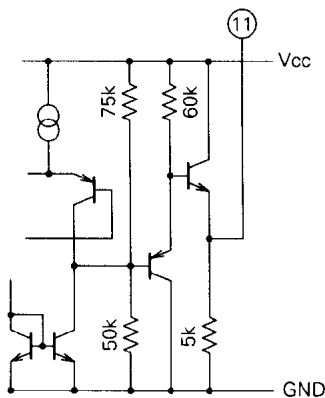
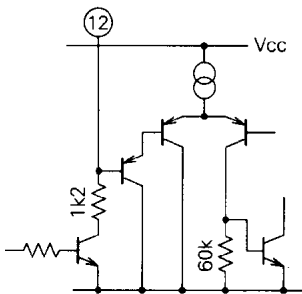
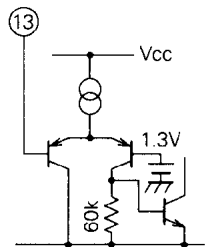
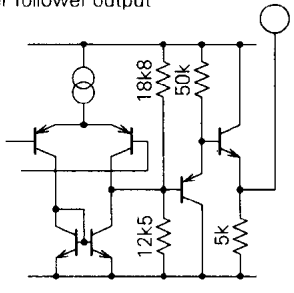
DESCRIPTION OF PIN (cont.)

Pin No.	Name	Voltage and wave information	Peripheral circuit of pins
⑤	KILLER OUT	H: 3.3V L: 0.1V Emitter follower output $Z_o=300\Omega$	
⑥	POWER SUPPLY 1	4.0V	—
⑦	GND 1	0V	—
⑧	VIDEO IN	Sync tip 2.5V Sync tip clamping input Low $Z_i<100\Omega$ at clamping	
⑨	PICTURE CONT	2.5V $Z_i=75K$	
⑩	CONTRAST CONT	Open base input $Z_i>100K\Omega$	

M52015FP

NTSC VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION OF PIN (cont.)

Pin No.	Name	Voltage and wave information	Peripheral circuit of pins
⑪	VIDEO OUT	2.5~0V Emitter follower output Approx. $Z_o = 60\Omega$	
⑫	BURST GATE TIME CONSTANT	$Z_i > 100k\Omega$	
⑬	H. SYNC IN	Same as above	
⑭	B OUT	2.2~0V $Z_o = 100\Omega$	
⑮	R OUT		
⑯	G OUT		
⑰	GND 2	0V	—
⑳	POWER SUPPLY 2	4.0V	—

DESCRIPTION OF PIN (cont.)

Pin No.	Name	Voltage and wave information	Peripheral circuit of pins
(18)	COLOR CONT	$Z_i > 100k\Omega$	
(19)	CHROMA IN	3.3V $Z_i = 12k\Omega$	
(21)	TINT CONT	1.6V $Z_i = 87K5$	
(22)	ACC FILTER	0.9V Hi Z_i	

M52015FP

NTSC VIDEO CHROMA SIGNAL PROCESSOR

DESCRIPTION OF PIN (cont.)

Pin No.	Name	Voltage and wave information	Peripheral circuit of pins
②③	APC FILTER	2.6V *Z _I =12K5 with B. G. P. "ON" *Z _I >100kΩ with B. G. P. "OFF"	
②④	VCXO OUT 2	3.2V Z _o =26Ω when R=3K2	

PRECAUTIONS FOR APPLICATION

- 1) Adjust trimmer capacitor to set VCO free run frequency at 3.579545MHz.
- 2) Pin ②④ outputs VCO signal through open emitter. Use this pin to measure VCO oscillation frequency.
- 3) Burst gate width varies with time constant at pin ⑫. Set resistance at 20kΩ or higher.
- 4) Sync tip-clamped luminance signal is output at pin ⑪ in inverted phase through emitter follower. With standard input, output amplitude is about 1.5V_{P-P}.
- 5) R, G and B output clamping voltages vary with contrast voltage. Signals other than color burst component of chroma signal are not blanked.