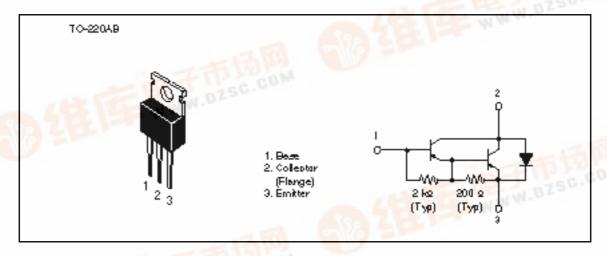
Silicon PNP Epitaxial

HITAÇHI

Application

Medium speed and power switching complementary pair with 2SD970(K)

Outline



Absolute Maximum Ratings (Ta = 25°C)

Item W W	Symbol	Rating	Unit V	
Collector to base voltage	V _{CBO}	-120		
Collector to emitter voltage	V_{CEO}	-120	V	
Emitter to base voltage	V_{EBO}	-7	-V TEG.CO	
Collector current	I _c	-8	Α	
Collector peak current	C(peak)	- 12	А	
Collector power dissipation	Pc*1	40	W	
Junction temperature	Тj	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

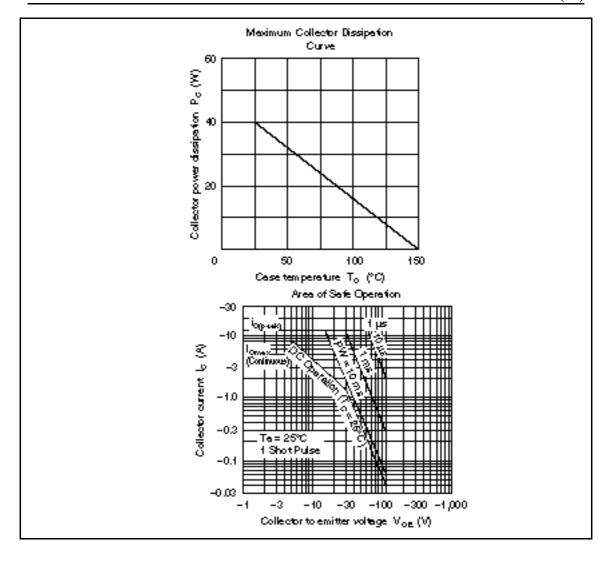
Note: 1. Value at $T_c = 25^{\circ}C$

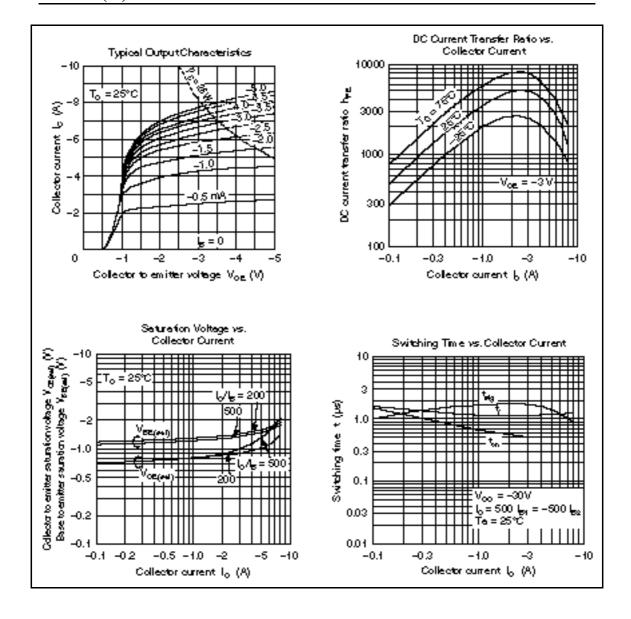


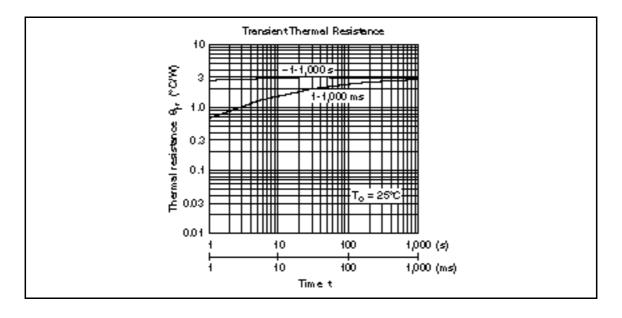
Electrical Characteristics ($Ta = 25^{\circ}C$)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	-120	_	_	V	$I_{\rm C} = -25$ mA, $R_{\rm BE} =$
Emitter to base breakdown voltage	$V_{(BR)EBO}$	- 7	_	_	V	$I_{\rm E} = -50 \text{ mA}, I_{\rm C} = 0$
Collector cutoff current	I _{CBO}	_	_	-100	μΑ	$V_{CB} = -120 \text{ V}, I_{E} = 0$
	I _{CEO}	_	_	-10	μΑ	$V_{CE} = -100 \text{ V}, R_{BE} =$
DC current transfer ratio	h _{FE}	1000	_	20000		$V_{CE} = -3 \text{ V}, I_{C} = -4 \text{ A}^{*1}$
Collector to emitter saturation	V _{CE(sat)(1)}	_	_	-1.5	V	$I_{\rm C} = -4 \text{ A}, I_{\rm B} = -8 \text{ mA}^{*1}$
voltage	V _{CE(sat)(2)}	_	_	-3.0	V	$I_{\rm C} = -8 \text{ A}, I_{\rm B} = -80 \text{ mA}^{*1}$
Base to emitter saturation	$V_{BE(sat)(1)}$	_	_	-2.0	V	$I_{\rm C} = -4 \text{ A}, I_{\rm B} = -8 \text{ mA}^{*1}$
voltage	$V_{BE(sat)(2)}$	_	_	-3.5	V	$I_{\rm C} = -8 \text{ A}, I_{\rm B} = -80 \text{ mA}^{*1}$
Turn on time	t _{on}	_	0.5	_	μs	$I_{\rm C} = -4 \text{ A}, I_{\rm B1} = I_{\rm B2} = -8 \text{ mA}$
Storage time	t _{stg}		1.6		μs	_
Fall time	t _f	_	1.5	_	μs	

Note: 1. Pulse test







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