2SD2121(L)/(S)

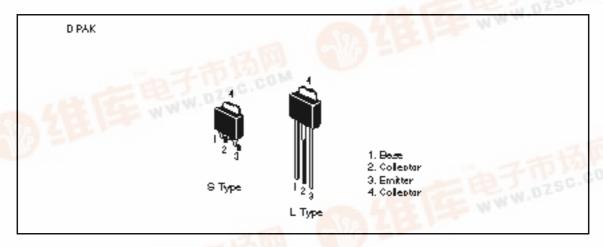
Silicon NPN Epitaxial

HITACHI

Application

Low frequency power amplifier complementary pair with 2SB1407(L)/(S)

Outline



Absolute Maximum Ratings ($Ta = 25^{\circ}C$)

Item	Symbol	Ratings	Unit
Collector to base voltage	V_{CBO}	35	V
Collector to emitter voltage	V_{CEO}	35	V
Emitter to base voltage	V_{EBO}	5	V Cot
Collector current	I _c	2.5	A
Collector peak current	I _{C(peak)}	3	А
Collector power dissipation	P _c *1	18	W
Junction temperature	onth Tj	150	°C
Storage temperature	Tstg	-55 to +150	°C
N			

Note: 1. Value at $T_c = 25$ °C.



2SD2121(L)/(S)

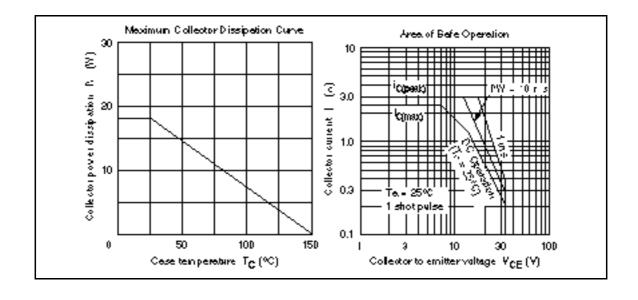
Electrical Characteristics (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test conditions
Collector to base breakdown voltage	$V_{(BR)CBO}$	35	_	_	V	$I_{c} = 1 \text{ mA}, I_{E} = 0$
Collector to emitter breakdown voltage	$V_{(BR)CEO}$	35	_	_	V	$I_{\rm C}$ = 10 mA, $R_{\rm BE}$ =
Emitter to base breakdown voltage	$V_{(BR)EBO}$	5	_	_	V	$I_E = 1 \text{ mA}, I_C = 0$
Collector cutoff current	I _{CBO}	_	_	20	μA	$V_{CB} = 35 \text{ V}, I_{E} = 0$
DC current transfer ratio	h _{FE1} *1	60	_	320		$V_{CE} = 2 \text{ V}, I_{C} = 0.5 \text{ A}^{*2}$
	h _{FE2}	20	_	_		$V_{CE} = 2 \text{ V}, I_{C} = 1.5 \text{ A}^{*2}$
Base to emitter voltage	V_{BE}	_	_	1.5	V	$V_{CE} = 2 \text{ V}, I_{C} = 1.5 \text{ A}^{*2}$
Collector to emitter saturation voltage	$V_{\text{CE(sat)}}$	_	_	1.0	V	$I_{\rm C} = 2 \text{ A}, I_{\rm B} = 0.2 \text{ A}^{*2}$

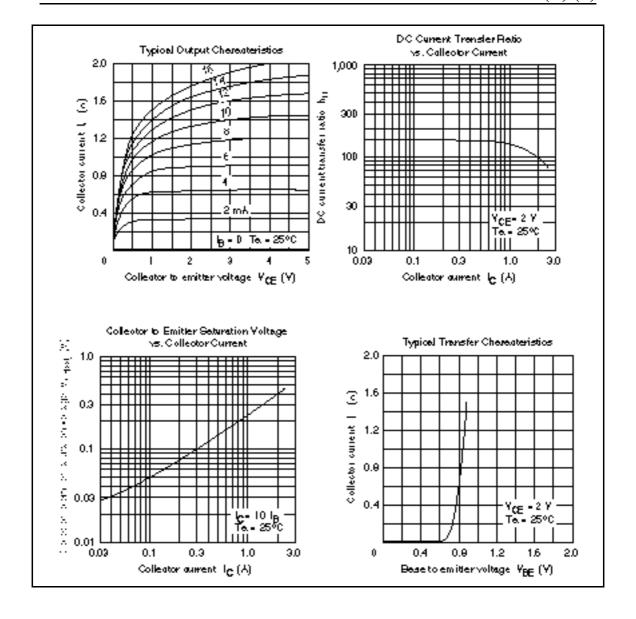
Notes: 1. The 2SD2121(L)/(S) is grouped by h_{FE1} as follows.

В	С	D
60 to 120	100 to 200	160 to 320

2. Pulse test.



2SD2121(L)/(S)



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HITACHI

Hitachi, Ltd.
Semiconductor & IC Div.
Nippon Bidg., 2-5-2, Ohte-medii, Chiyode-ku, Tokyo 100, Jepan Tet Tokyo (03, 3270-2111 Fax: (03, 3270-5109

For further in forme flon write to: Hitechi America, Ltd.

Paracon America, Dec. Semiconductor & IC Dw. 2000 Sierre Point Perkwey Briebene, CA. 94005-4835 U.S.A.

Tet 415-589-8300 Fex: 415-583-4207 Hitechi Burope GmbH Bedronic Componente Group Continental Burope Dornacher Straße 3 D-85622 Feldkirchen München Tet 089-9 94 80-0 Fex 089-9 20 30 00 Hitechi Burope Ltd.
Bedronic Componente Div.
Northern Burope Headquartere
Whitebrook Ferk
Lower Cook hem Road
Maidenhead
Berkehire SL68YA
Urited Kingdom
Tet 0628-585000
Fex: 0628-778322

Hitachi Asia Pta, Ltd 45 Collyer Quay \$20-00 Hitachi Towar Singapore 0404 Tat 535-2400 Fex: 535-1533

Hitachi Asia (Hong Kong) Ltd. Unit 705, North Towar, World Finance Cantra, Harbour City, Carton Road Taim Sha Taul, Kowloon Hong Kong Tat 27:592/18 Fax: 27:306074