捷多邦,专业PCB打样工厂,24小时加急出货

# 2SK3209

Silicon N Channel MOS FET High Speed Power Switching

# HITACHI

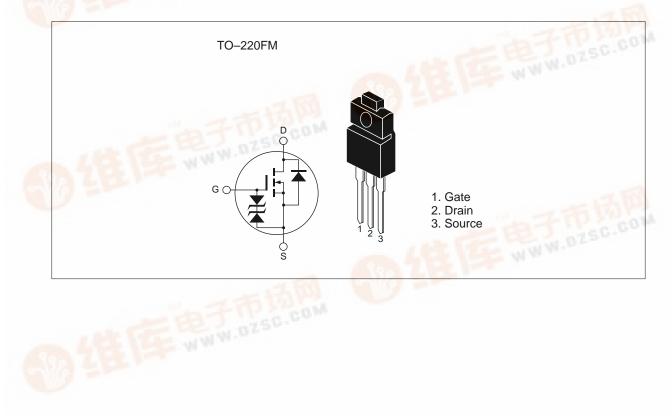
ADE-208-759(Z) Target Specification, 1st. Edition Dec. 1, 1998

WWW.DZSC.C

#### Features

- Low on-resistance
  - $R_{DS} = 35m\Omega$  typ.
- High speed switching
- 4V gate drive device can be driven from 5V source

#### Outline





## 2SK3209

## Absolute Maximum Ratings ( $Ta = 25^{\circ}C$ )

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	150	V	
Gate to source voltage	V <sub>GSS</sub>	±20	V	
Drain current	I <sub>D</sub>	25	A	
Drain peak current	Note1 D(pulse)	100	A	
Body-drain diode reverse drain current	I <sub>DR</sub>	25	A	
Avalanche current	AP Note3	25	A	
Avalanche energy	E <sub>AR</sub> <sup>Note3</sup>	46	mJ	
Channel dissipation	Pch Note2	35	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Note: 1.  $PW \le 10\mu s$ , duty cycle  $\le 1 \%$ 

2. Value at Tc =  $25^{\circ}C$ 

3. Value at Tch =  $25^{\circ}$ C, Rg  $\geq 50\Omega$ 

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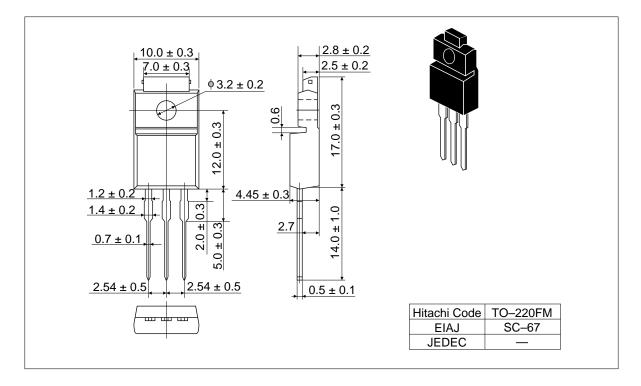
## **Electrical Characteristics** (Ta = 25°C)

Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	150	—	_	V	$I_{\rm D} = 10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20			V	$I_{g} = \pm 100 \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>		_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0$
Zero gate voltage drain current	I <sub>DSS</sub>		_	10	μA	$V_{\rm DS} = 150 \ V, \ V_{\rm GS} = 0$
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.0	_	2.5	V	$I_{\rm D} = 1 {\rm mA}, V_{\rm DS} = 10 {\rm V}$
Static drain to source on state	R <sub>DS(on)</sub>		0.035	0.045	Ω	$I_{\rm D}$ =15A, $V_{\rm GS}$ = 10 $V^{\rm Note4}$
resistance	R <sub>DS(on)</sub>		0.042	0.063	Ω	$I_{\rm D}$ =15A, $V_{\rm GS}$ = 4V <sup>Note4</sup>
Forward transfer admittance	y <sub>fs</sub>	18	30	_	S	$I_{\rm D}$ =15A, $V_{\rm DS}$ = 10V <sup>Note4</sup>
Input capacitance	Ciss		2600		pF	V <sub>DS</sub> = 10V
Output capacitance	Coss	_	820	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		350		pF	f = 1MHz
Turn-on delay time	t <sub>d(on)</sub>		25		ns	$I_{\rm D}$ =15A, $V_{\rm GS}$ = 10V
Rise time	t,		180		ns	$R_{L} = 2\Omega$
Turn-off delay time	t <sub>d(off)</sub>	—	600	_	ns	_
Fall time	t <sub>f</sub>		280		ns	_
Body-drain diode forward voltage	V <sub>DF</sub>		0.95		V	$I_{\rm F} = 25$ A, $V_{\rm GS} = 0$
Body–drain diode reverse recovery time	t <sub>rr</sub>		100		ns	I <sub>F</sub> = 25A, V <sub>GS</sub> = 0 diF/ dt =50A/μs

Note: 4. Pulse test

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#### Package Dimensions (Unit: mm)



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