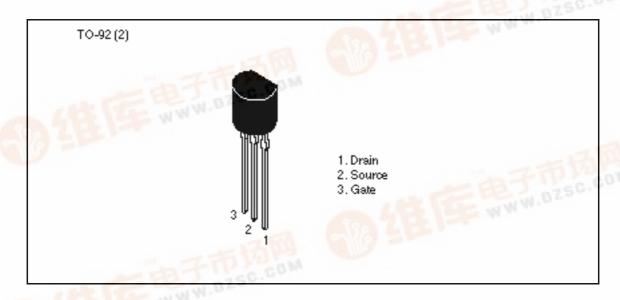
Silicon N-Channel Junction FET

# HITACHI

#### **Application**

Low frequency / High frequency amplifier

#### Outline





#### **Absolute Maximum Ratings** ( $Ta = 25^{\circ}C$ )

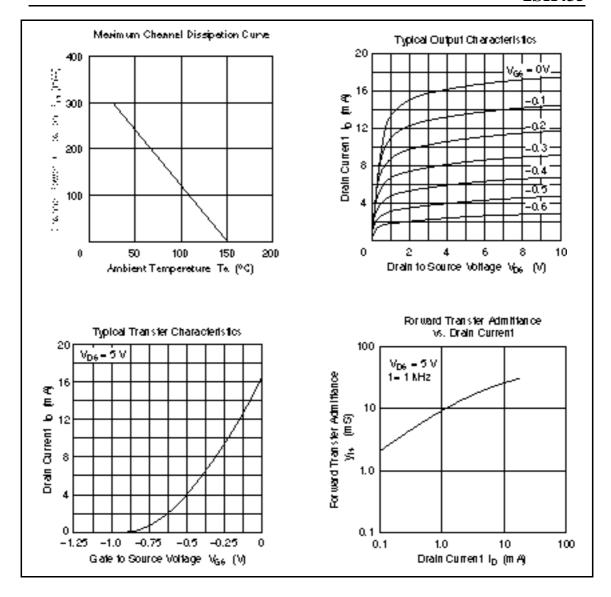
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DS</sub>	22	V
Gate to source voltage	$V_{\sf GSO}$	-22	V
Drain current	I <sub>D</sub>	100	mA
Gate current	I <sub>G</sub>	10	mA
Channel power dissipation	Pch	300	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

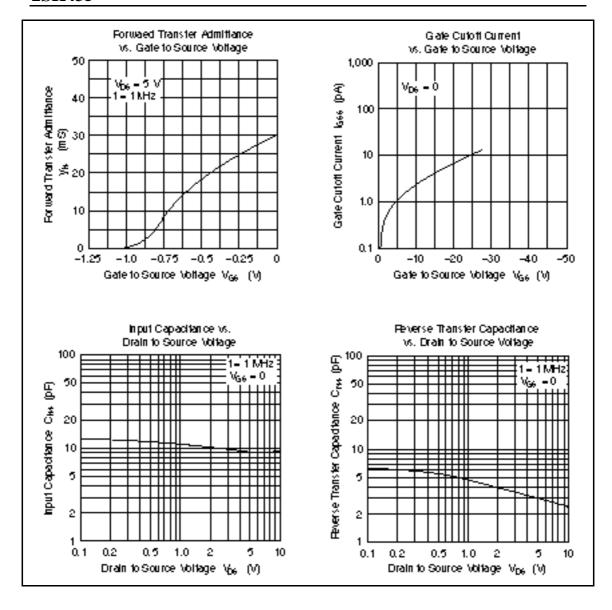
#### **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

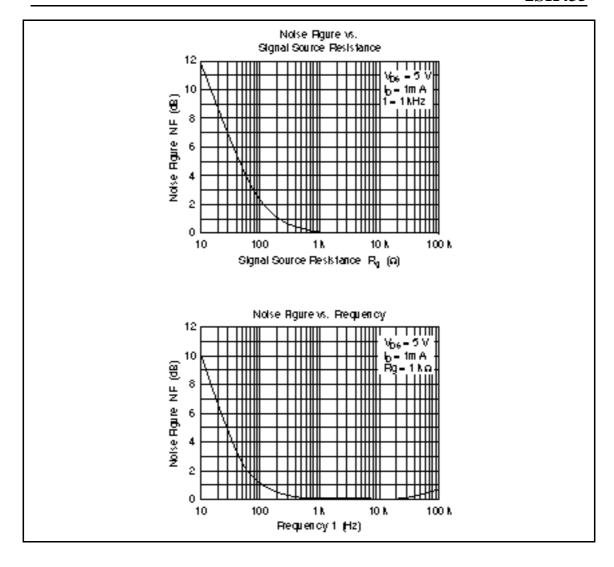
Item	Symbol	Min	Тур	Max	Unit	Test conditions
Gate to source breakdown voltage	$V_{(BR)GSS}$	-22	_	_	V	$I_{G} = -10 \ \mu A, \ V_{DS} = 0$
Gate cutoff current	I <sub>GSS</sub>		_	-10	nA	$V_{GS} = -15 \text{ V}, V_{DS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	_	_	-2.5	V	$V_{DS} = 5 \text{ V}, I_{D} = 10 \mu\text{A}$
Drain current	l <sub>DSS</sub> *1	6	_	40	mA	$V_{DS} = 5 \text{ V}, V_{GS} = 0, \text{ Pulse test}$
Forward transfer admittance	y <sub>fs</sub>	20	_	_	mS	$V_{DS} = 5 \text{ V}, I_{D} = 10 \text{ mA},$ f = 1kHz
Input capacitance	Ciss	_	9.0	11.0	pF	$V_{DS} = 5 \text{ V}, V_{GS} = 0,$ f = 1MHz
Reverse transfer capacitance	Crss	_	2.8	4.0	pF	$V_{DS} = 5 \text{ V}, V_{GS} = 0,$ f = 1MHz
Noise figure	NF	_	0.5	3.0	dB	$V_{DS} = 5 \text{ V}, I_{D} = 1 \text{ mA},$ f = 1kHz, Rg = 1k

Note: 1. The 2SK435 is grouped by I<sub>DSS</sub> as follows.

Grade	В	С	D	E	
I <sub>DSS</sub>	6 to 14	12 to 22	18 to 30	26 to 40	







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