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# WWW.DZSC.CO **Low Power** Narrowband FM IF

MOTOROLA

The MC3371 and MC3372 perform single conversion FM reception and consist of an oscillator, mixer, limiting IF amplifier, guadrature discriminator, active filter, squelch switch, and meter drive circuitry. These devices are designed for use in FM dual conversion communication equipment. The MC3371/MC3372 are similar to the MC3361/MC3357 FM IFs, except that a signal strength indicator replaces the scan function controlling driver which is in the MC3361/MC3357. The MC3371 is designed for the use of parallel LC components, while the MC3372 is designed for use with either a 455 kHz ceramic discriminator, or parallel LC components.

These devices also require fewer external parts than earlier products. The MC3371 and MC3372 are available in dual-in-line and surface mount packaging.

- Wide Operating Supply Voltage Range: V<sub>CC</sub> = 2.0 to 9.0 V
- Input Limiting Voltage Sensitivity of –3.0 dB
- Low Drain Current: I<sub>CC</sub> = 3.2 mA, @ V<sub>CC</sub> = 4.0 V, Squelch Off
- Minimal Drain Current Increase When Squelched
- Signal Strength Indicator: 60 dB Dynamic Range
- Mixer Operating Frequency Up to 100 MHz
- Fewer External Parts Required than Earlier Devices

### **MAXIMUM RATINGS**

Rating	Pin	Symbol	Value	Unit
Power Supply Voltage	4	V <sub>CC</sub> (max)	10	Vdc
RF Input Voltage (V <sub>CC</sub> $\ge$ 4.0 Vdc)	16	V16	1.0	Vrms
Detector Input Voltage	8	V8	1.0	Vpp
Squelch Input Voltage (V <sub>CC</sub> ≥ 4.0 Vdc)	12	V12	6.0	Vdc
Mute Function	14	V <sub>14</sub>	-0.7 to 10	V <sub>pk</sub>
Mute Sink Current	14	114	50	mA
Junction Temperature	-	Тј	150	°C
Storage Temperature Range	-	T <sub>stg</sub>	-65 to +150	°C

NOTES: 1. Devices should not be operated at these values. The "Recommended Operating" Conditions" table provides conditions for actual device operation.

MC3371

(Top View)

**PIN CONNECTIONS** 

Crystal Osc

Limiter Input 5

Decoupling 6

Quad Input 8

Limiter Output 7

16 Mixer Input

13 Meter Drive

12 Squelch Input

11 Filter Output

10 Filter Input

9 Recovered Audio

15 Gnd

14 Mute

2. ESD data available upon request.

Crystal Osc

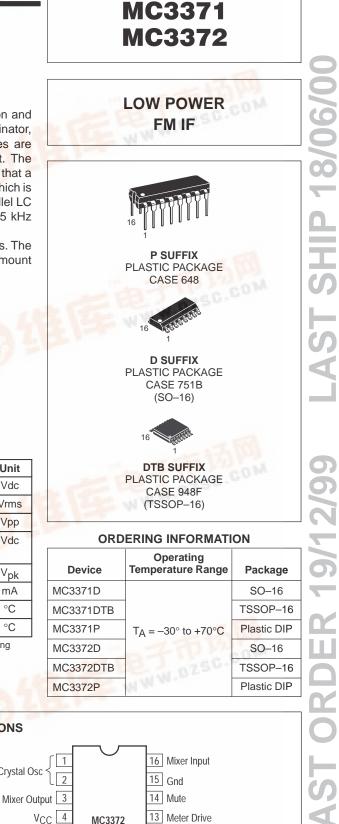
Mixer Output

Limiter Input

Decoupling

VCCL

Quad Coil 8



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(Top View)

12 Squelch Input

11 Filter Output

10 Filter Input

9 Recovered Audio

### **RECOMMENDED OPERATING CONDITIONS**

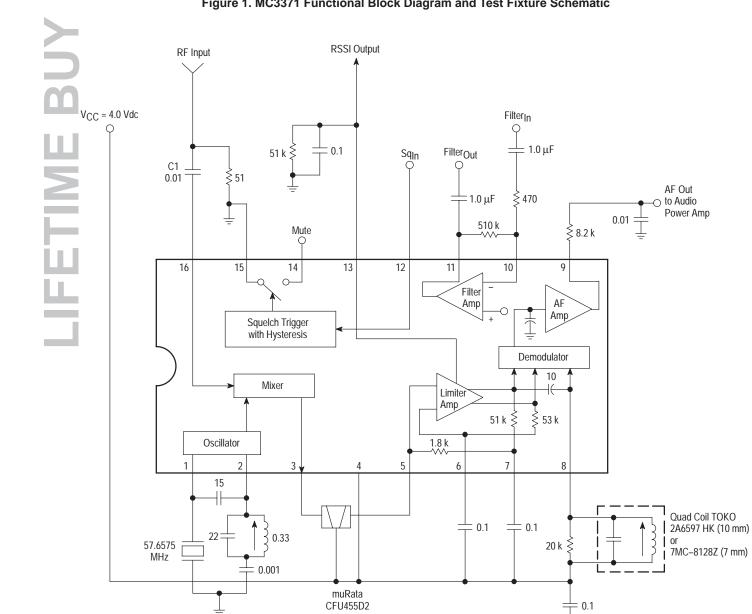
Rating	Pin	Symbol	Value	Unit
$\label{eq:supply Voltage} \begin{array}{c} (@\ T_A = 25^\circ C) \\ (-30^\circ C \leqslant T_A \leqslant +75^\circ C) \end{array}$	4	VCC	2.0 to 9.0 2.4 to 9.0	Vdc
RF Input Voltage	16	V <sub>rf</sub>	0.0005 to 10	mVrms
RF Input Frequency	16	f <sub>rf</sub>	0.1 to 100	MHz
Oscillator Input Voltage	1	V <sub>local</sub>	80 to 400	mVrms
Intermediate Frequency	-	f <sub>if</sub>	455	kHz
Limiter Amp Input Voltage	5	V <sub>if</sub>	0 to 400	mVrms
Filter Amp Input Voltage	10	V <sub>fa</sub>	0.1 to 300	mVrms
Squelch Input Voltage	12	V <sub>sq</sub>	0 or 2	Vdc
Mute Sink Current	14	l <sub>sq</sub>	0.1 to 30	mA
Ambient Temperature Range	-	Тд	-30 to +70	°C

## **AC ELECTRICAL CHARACTERISTICS** ( $V_{CC} = 4.0 \text{ Vdc}, f_0 = 58.1125 \text{ MHz}, df = \pm 3.0 \text{ kHz}, f_{mod} = 1.0 \text{ kHz}, 50 \Omega \text{ source}, f_{10} = 57.6575 \text{ MHz}$ ( $V_{10} = 1.0 \text{ kHz}, 50 \Omega \text{ source}, f_{10} = 57.6575 \text{ MHz}$ )

Characteristic	Pin	Symbol	Min	Тур	Max	Unit
Input for 12 dB SINAD Matched Input – (See Figures 11, 12 and 13) Unmatched Input – (See Figures 1 and 2)	-	VSIN		1.0 5.0	- 15	μVrm
Input for 20 dB NQS	_	V <sub>NQS</sub>	_	3.5	_	μVrm
Recovered Audio Output Voltage $V_{rf} = -30 \text{ dBm}$	-	AFO	120	200	320	mVrm
Recovered Audio Drop Voltage Loss Vrf = $-30 \text{ dBm}$ , V <sub>CC</sub> = 4.0 V to 2.0 V	-	AF <sub>loss</sub>	-8.0	-1.5	_	dB
Meter Drive Output Voltage (No Modulation) $V_{rf} = -100 \text{ dBm}$ $V_{rf} = -70 \text{ dBm}$ $V_{rf} = -40 \text{ dBm}$	13	M <sub>Drv</sub> MV1 MV2 MV3	_ 1.1 2.0	0.3 1.5 2.5	0.5 1.9 3.1	Vdc
Filter Amp Gain $R_{S} = 600 \Omega$ , $f_{S} = 10 \text{ kHz}$ , $V_{fa} = 1.0 \text{ mVrms}$	-	AV(Amp)	47	50	_	dB
Mixer Conversion Gain $V_{rf} = -40 \text{ dBm}, R_L = 1.8 \text{ k}\Omega$	-	A <sub>V(Mix)</sub>	14	20	_	dB
Signal to Noise Ratio V <sub>rf</sub> = -30 dBm	-	s/n	36	67	_	dB
Total Harmonic Distortion $V_{ff} = -30 \text{ dBm}, \text{BW} = 400 \text{ Hz} \text{ to } 30 \text{ kHz}$	-	THD	_	0.6	3.4	%
Detector Output Impedance	9	ZO	-	450	-	Ω
Detector Output Voltage (No Modulation) $V_{rf} = -30 \text{ dBm}$	9	DVO	_	1.45	_	Vdc
Meter Drive V <sub>rf</sub> = -100 to -40 dBm	13	MO	_	0.8	_	μA/dl
Meter Drive Dynamic Range RF <sub>In</sub> IF <sub>In</sub> (455 kHz)	13	MVD	-	60 80		dB
Mixer Third Order Input Intercept Point f1 = 58.125 MHz f2 = 58.1375 MHz	-	ITO <sub>Mix</sub>		-22	_	dBm
Mixer Input Resistance	16	R <sub>in</sub>		3.3	-	kΩ
Mixer Input Capacitance	16	C <sub>in</sub>	_	2.2	_	pF

DC ELECTRICAL CHARACTERISTICS (V <sub>CC</sub> = 4.0 Vdc, T <sub>A</sub>	A = 25°C,	unless otherwise noted)
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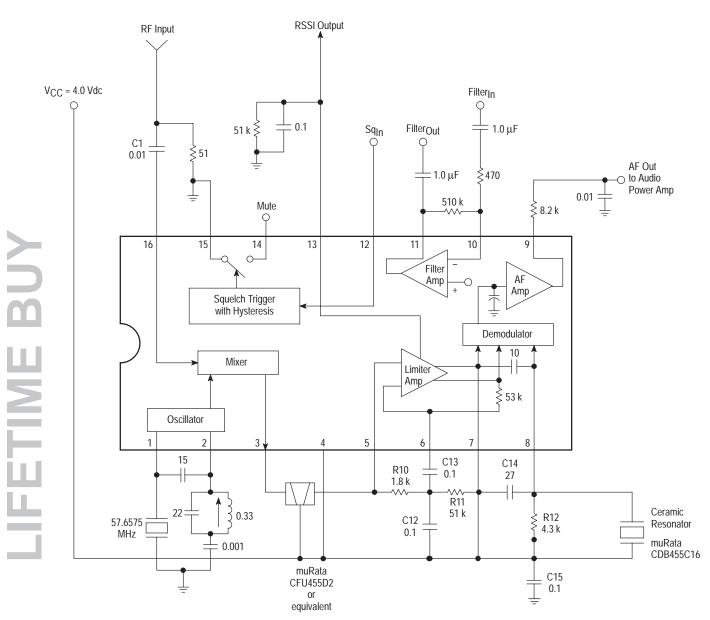
Characteristic	Pin	Symbol	Min	Тур	Max	Unit
Drain Current (No Input Signal) Squelch Off, $V_{Sq} = 2.0 \text{ Vdc}$ Squelch On, $V_{Sq} = 0 \text{ Vdc}$ Squelch Off, $V_{CC} = 2.0 \text{ to } 9.0 \text{ V}$	4	lcc1 lcc2 dlcc1		3.2 3.6 1.0	4.2 4.8 2.0	mA
Detector Output (No Input Signal) DC Voltage, V8 = V <sub>CC</sub>	9	V9	0.9	1.6	2.3	Vdc
Filter Output (No Input Signal) DC Voltage Voltage Change, V <sub>CC</sub> = 2.0 to 9.0 V	11	V11 dV11	1.5 2.0	2.5 5.0	3.5 8.0	Vdc
Trigger Hysteresis	-	Hys	34	57	80	mV



or equivalent



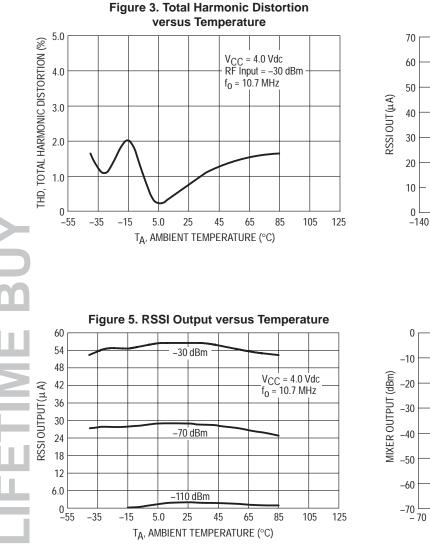
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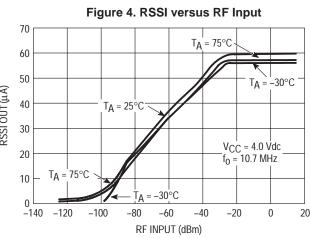


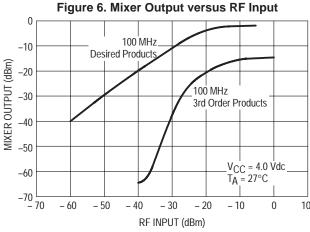
#### Figure 2. MC3372 Functional Block Diagram and Test Fixture Schematic

### **TYPICAL CURVES**

(Unmatched Input)







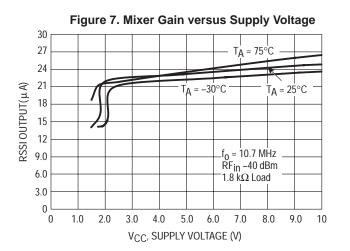
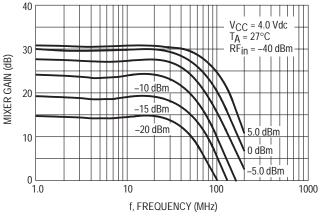


Figure 8. Mixer Gain versus Frequency



### MC3371 PIN FUNCTION DESCRIPTION

**OPERATING CONDITIONS** V<sub>CC</sub> = 4.0 Vdc, RF<sub>In</sub> = 100 μV, f<sub>mod</sub> = 1.0 kHz, f<sub>dev</sub> = 3.0 kHz. MC3371 at f<sub>RF</sub> = 10.7 MHz (see Figure 11).

Pin	Symbol	Internal Equivalent Circuit	Description	Waveform
1	OSC1		The base of the Colpitts oscillator. Use a high impedance and low capacitance probe or a "sniffer" to view the wave– form without altering the frequency. Typical level is 450 mVpp.	198e¢ 20es
2	OSC2	$0SC1 \rightarrow 13K$ $2$ $0SC2 \rightarrow 200$ $\mu A$	The emitter of the Colpitts oscillator. Typical signal level is 200 mVpp. Note that the signal is somewhat distorted compared to that on Pin 1.	1800; 2011
3	MX <sub>Out</sub>	V <sub>CC</sub> <sup>4</sup> MixerOut	Output of the Mixer. Riding on the 455 kHz is the RF carrier component. The typical level is approximately 60 mVpp.	$\sim$
4	VCC	$\begin{bmatrix} 1.5 k \\ 1.00 \\ \mu A \\ \pm \end{bmatrix}$	Supply Voltage –2.0 to 9.0 Vdc is the operating range. V <sub>CC</sub> is decoupled to ground.	28xC 500xs
5	IFIn	$ \begin{array}{c} \text{IF}_{\text{In}} \xrightarrow{5} \\ 6 \\ \text{DEC1} \\ 7 \\ 51 k \end{array} $	Input to the IF amplifier after passing through the 455 kHz ceramic filter. The signal is attenuated by the filter. The typical level is approximately 50 mVpp.	$\sim$
6 7	DEC1 DEC2	DEC2 →	IF Decoupling. External 0.1 $\mu$ F capacitors connected to V <sub>CC</sub> .	20m; \$00m s%
8	Quad Coil	Quad Coil V <sub>CC</sub> 10 50 μA	Quadrature Tuning Coil. Composite (not yet demodulated) 455 kHz IF signal is present. The typical level is 500 mVpp.	

### MC3371 PIN FUNCTION DESCRIPTION (continued)

**OPERATING CONDITIONS**  $V_{CC}$  = 4.0 Vdc, RF<sub>In</sub> = 100  $\mu$ V, f<sub>mod</sub> = 1.0 kHz, f<sub>dev</sub> = 3.0 kHz. MC3371 at f<sub>RF</sub> = 10.7 MHz (see Figure 11).

Pir		Internal Equivalent Circuit	100 μV, f <sub>mod</sub> = 1.0 kHz, f <sub>dev</sub> = 3.0 kHz. M Description	Waveform
9	RA	V <sub>CC</sub>	Recovered Audio. This is a composite FM demodulated output having signal and carrier component. The typical level is 1.4 Vpp.	500m <sup>-</sup> 200m <sup>-1</sup>
BUY		$\begin{array}{c} 200 \\ 9 \\ 9 \\ 100 \ \mu A \\ = \end{array}$	The filtered recovered audio has the carrier component removed and is typically 800 mVpp.	500m; 200p+1
	) Fil <sub>ln</sub>	10 FilterIn VCC 30 μA	Filter Amplifier Input	
	FilOut	V <sub>CC</sub> 240 μA FilterOut	Filter Amplifier Output. The typical signal level is 400 mVpp.	240.47 200,45
12	2 Sq <sub>In</sub>	Sq <sub>In</sub>	Squelch Input. See discussion in application text.	20-5 20ns

### MC3371 PIN FUNCTION DESCRIPTION (continued)

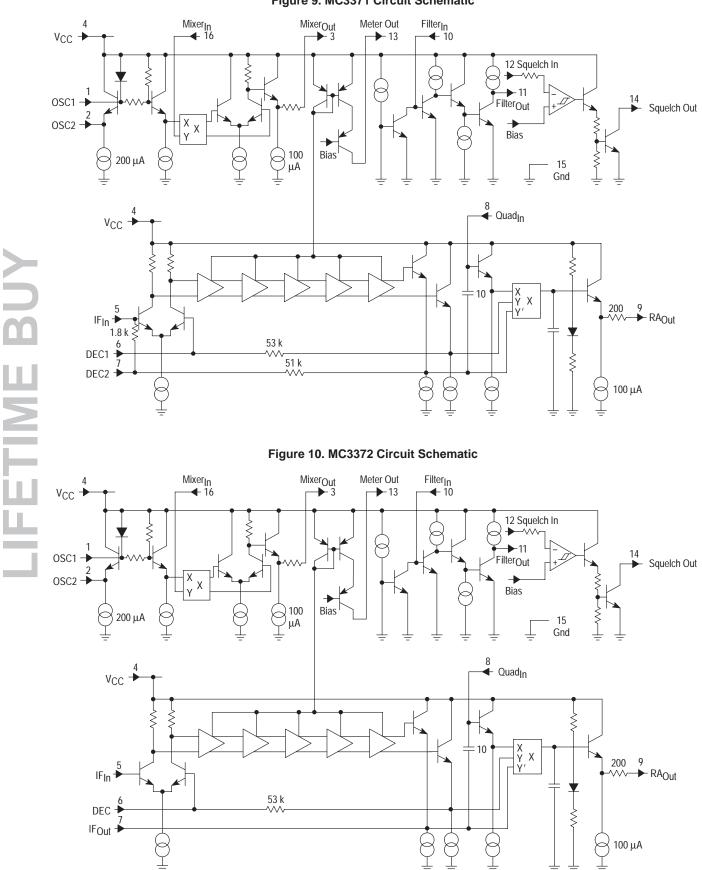
 $\textbf{OPERATING CONDITIONS} \ \forall_{CC} = 4.0 \ \forall dc, \ \mathsf{RF}_{In} = 100 \ \mu \mathsf{V}, \ \mathsf{f}_{mod} = 1.0 \ \mathsf{kHz}, \ \mathsf{f}_{dev} = 3.0 \ \mathsf{kHz}. \ \mathsf{MC3371} \ at \ \mathsf{f}_{RF} = 10.7 \ \mathsf{MHz} \ (see \ \mathsf{Figure 11}).$ 

Pin	Symbol	Internal Equivalent Circuit	Description	Waveform
13	RSSI	V <sub>CC</sub> 1.8 k Bias RSSI <sub>Out</sub>	RSSI Output. Referred to as the Received Signal Strength Indicator or RSSI. The chip sources up to 60 µA over the linear 60 dB range. This pin may be used many ways, such as: AGC, meter drive and carrier triggered squelch circuit.	
14	MUTE	40 k s	Mute Output. See discussion in application text.	
15	Gnd	Gnd 15	Ground. The ground area should be continuous and unbroken. In a two– sided layout, the component side has the ground plane. In a one–sided layout, the ground plane fills around the traces on the circuit side of the board and is not interrupted.	
16	MIXIn	Mixer <sub>In</sub> 3.3 k 10 k	Mixer Input – Series Input Impedance: @ 10 MHz: 309 – j33 Ω @ 45 MHz: 200 – j13 Ω	

#### MC3372 PIN FUNCTION DESCRIPTION

 $\textbf{OPERATING CONDITIONS} \ \forall_{CC} = 4.0 \ \forall dc, \ \mathsf{RF}_{In} = 100 \ \mu \mathsf{V}, \ \mathsf{f}_{mod} = 1.0 \ \mathsf{kHz}, \ \mathsf{f}_{dev} = 3.0 \ \mathsf{kHz}. \ \mathsf{MC3372} \ \mathsf{at} \ \mathsf{f}_{RF} = 45 \ \mathsf{MHz} \ (\mathsf{see Figure 13}).$ 

	Pin	Symbol	Internal Equivalent Circuit	Description	Waveform
	5	IF <sub>In</sub>	IFIn → ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓ ↓	IF Amplifier Input	
-	6	DEC1	DEC → 60 μA <u><u><u></u></u> =</u>	IF Decoupling. External 0.1 μF capacitors connected to V <sub>CC</sub> .	
BUY	7	IFOut	V <sub>CC</sub> 7 IF <sub>Out</sub> 50 μA = 120 μA	IF Amplifier Output Signal level is typically 300 mVpp.	
FETIME	8	QuadIn	<sup>8</sup> Quad <sub>In</sub> V <sub>CC</sub> = 10 = 50 μA	Quadrature Detector Input. Signal level is typically 150 mVpp.	
	9	RA	V <sub>CC</sub> 	Recovered Audio. This is a composite FM demodulated output having signal and carrier components. Typical level is 800 mVpp.	
			100 μA	The filtered recovered audio has the carrier signal removed and is typically 500 mVpp.	500m; 200a rt



#### Figure 9. MC3371 Circuit Schematic

#### **CIRCUIT DESCRIPTION**

The MC3371 and MC3372 are low power narrowband FM receivers with an operating frequency of up to 60 MHz. Its low voltage design provides low power drain, excellent sensitivity, and good image rejection in narrowband voice and data link applications.

This part combines a mixer, an IF (intermediate frequency) limiter with a logarithmic response signal strength indicator, a quadrature detector, an active filter and a squelch trigger circuit. In a typical application, the mixer amplifier converts an RF input signal to a 455 kHz IF signal. Passing through an external bandpass filter, the IF signal is fed into a limiting amplifier and detection circuit where the audio signal is recovered. A conventional quadrature detector is used.

The absence of an input signal is indicated by the presence of noise above the desired audio frequencies. This "noise band" is monitored by an active filter and a detector. A squelch switch is used to mute the audio when noise or a tone is present. The input signal level is monitored by a meter drive circuit which detects the amount of IF signal in the limiting amplifier.

#### **APPLICATIONS INFORMATION**

The oscillator is an internally biased Colpitts type with the collector, base, and emitter connections at Pins 4, 1 and 2 respectively. This oscillator can be run under crystal control. For fundamental mode crystals use crystal characterized parallel resonant for 32 pF load. For higher frequencies, use 3rd overtone series mode type crystals. The coil (L2) and resistor RD (R13) are needed to ensure proper and stable operation at the LO frequency (see Figure 13, 45 MHz application circuit).

The mixer is doubly balanced to reduce spurious radiation. Conversion gain stated in the AC Electrical Characteristics table is typically 20 dB. This power gain measurement was made under stable conditions using a 50  $\Omega$  source at the input and an external load provided by a 455 kHz ceramic filter at the mixer output which is connected to the V<sub>CC</sub> (Pin 4) and IF input (Pin 5). The filter impedance closely matches the 1.8 k $\Omega$  internal load resistance at Pin 3 (mixer output). Since the input impedance at Pin 16 is strongly influenced by a  $3.3 \text{ k}\Omega$  internal biasing resistor and has a low capacitance, the useful gain is actually much higher than shown by the standard power gain measurement. The Smith Chart plot in Figure 17 shows the measured mixer input impedance versus input frequency with the mixer input matched to a 50  $\Omega$  source impedance at the given frequencies. In order to assure stable operation under matched conditions, it is necessary to provide a shunt resistor to ground. Figures 11, 12 and 13 show the input networks used to derive the mixer input impedance data.

Following the mixer, a ceramic bandpass filter is recommended for IF filtering (i.e. 455 kHz types having a bandwidth of  $\pm 2.0$  kHz to  $\pm 15$  kHz with an input and output impedance from 1.5 k $\Omega$  to 2.0 k $\Omega$ ). The 6 stage limiting IF

amplifier has approximately 92 dB of gain. The MC3371 and MC3372 are different in the limiter and quadrature detector circuits. The MC3371 has a 1.8 k $\Omega$  and a 51 k $\Omega$  resistor providing internal dc biasing and the output of the limiter is internally connected, both directly and through a 10 pF capacitor to the quadrature detector; whereas, in the MC3372 these components are not provided internally. Thus, in the MC3371, no external components are necessary to match the 455 kHz ceramic filter, while in the MC3372, external 1.8 k $\Omega$  and 51 k $\Omega$  biasing resistors are needed between Pins 5 and 7, respectively (see Figures 12 and 13).

In the MC3371, a parallel LCR quadrature tank circuit is connected externally from Pin 8 to  $V_{CC}$  (similar to the MC3361). In the MC3372, a quadrature capacitor is needed externally from Pin 7 to Pin 8 and a parallel LC or a ceramic discriminator with a damping resistor is also needed from Pin 8 to  $V_{CC}$  (similar to the MC3357). The above external quadrature circuitry provides 90° phase shift at the IF center frequency and enables recovered audio.

The damping resistor determines the peak separation of the detector and is somewhat critical. As the resistor is decreased, the separation and the bandwidth is increased but the recovered audio is decreased. Receiver sensitivity is dependent on the value of this resistor and the bandwidth of the 455 kHz ceramic filter.

On the chip the composite recovered audio, consisting of carrier component and modulating signal, is passed through a low pass filter amplifier to reduce the carrier component and then is fed to Pin 9 which has an output impedance of 450  $\Omega$ . The signal still requires further filtering to eliminate the carrier component, deemphasis, volume control, and further amplification before driving a loudspeaker. The relative level of the composite recovered audio signal at Pin 9 should be considered for proper interaction with an audio post amplifier and a given load element. The MC13060 is recommended as a low power audio amplifier.

The meter output indicates the strength of the IF level and the output current is proportional to the logarithm of the IF input signal amplitude. A maximum source current of 60  $\mu$ A is available and can be used to drive a meter and to detect a carrier presence. This is referred to as a Received Strength Signal Indicator (RSSI). The output at Pin 13 provides a current source. Thus, a resistor to ground yields a voltage proportional to the input carrier signal level. The value of this resistor is estimated by (V<sub>CC</sub>(Vdc) – 1.0 V)/60  $\mu$ A; so for V<sub>CC</sub> = 4.0 Vdc, the resistor is approximately 50 k $\Omega$  and provides a maximum voltage swing of about 3.0 V.

A simple inverting op amp has an output at Pin 11 and the inverting input at Pin 10. The noninverting input is connected to 2.5 V. The op amp may be used as a noise triggered squelch or as an active noise filter. The bandpass filter is designed with external impedance elements to discriminate between frequencies. With an external AM detector, the filtered audio signal is checked for a tone signal or for the presence of noise above the normal audio band. This information is applied to Pin 12.

An external positive bias to Pin 12 sets up the squelch trigger circuit such that the audio mute (Pin 14) is open or connected to ground. If Pin 12 is pulled down to 0.9 V or below by the noise or tone detector, Pin 14 is internally shorted to ground. There is about 57 mV of hyteresis at Pin 12 to prevent jitter. Audio muting is accomplished by connecting Pin 14 to the appropriate point in the audio path between Pin 9 and an audio amplifier. The voltage at Pin 14 should not be lower than -0.7 V; this can be assured by connecting Pin 14 to the point that has no dc component.

Another possible application of the squelch switch may be as a carrier level triggered squelch circuit, similar to the MC3362/MC3363 FM receivers. In this case the meter output can be used directly to trigger the squelch switch when the RF input at the input frequency falls below the desired level. The level at which this occurs is determined by the resistor placed between the meter drive output (Pin 13) and ground (Pin 15).

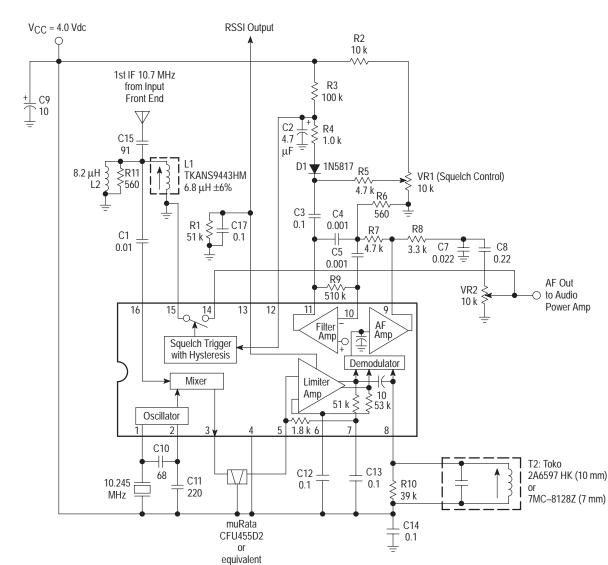
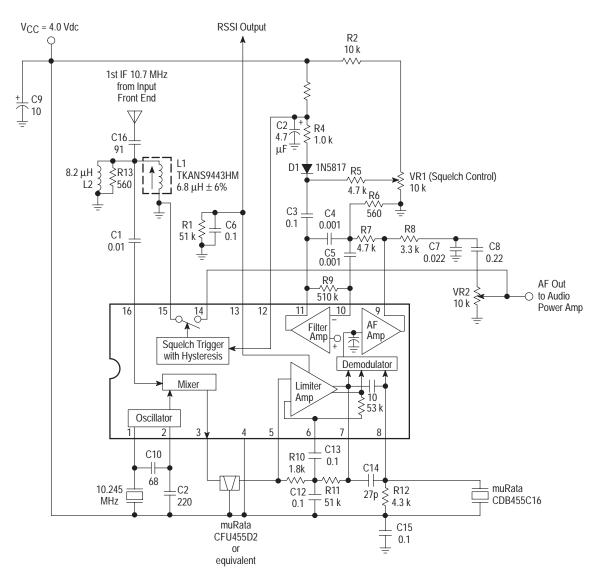
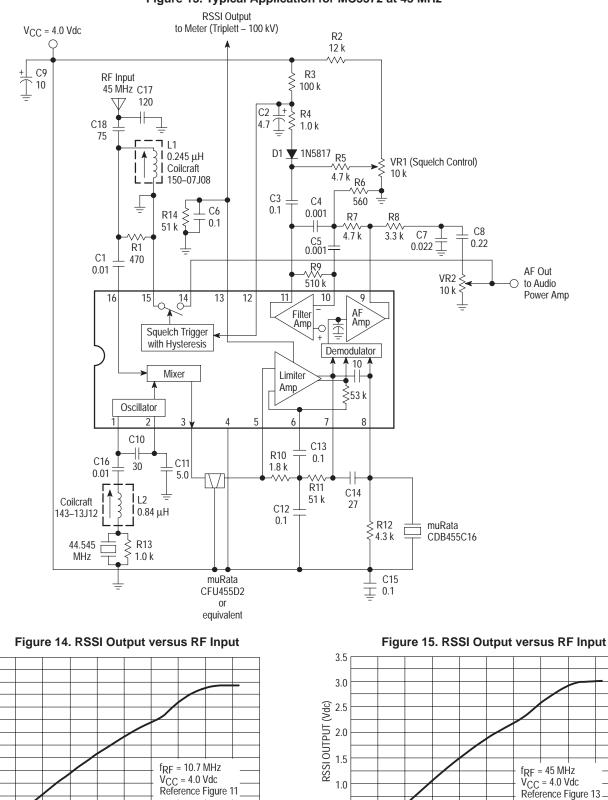


Figure 11. Typical Application for MC3371 at 10.7 MHz

BUY



#### Figure 12. Typical Application for MC3372 at 10.7 MHz



0.5

0 └**-**-120

-100

-80

#### Figure 13. Typical Application for MC3372 at 45 MHz



-60

RF INPUT (dBm)

-40

-20

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3.5

3.0

(XPR) 2.5 2.5 2.0 LTPUT (Vdc) 1.5 1.0 1.5 1.0 LTPUT (Vdc) 1.5 1.0 LTPUT (Vdc) 1.0 LTPUT (Vdc)

1.0

0.5

0

-120

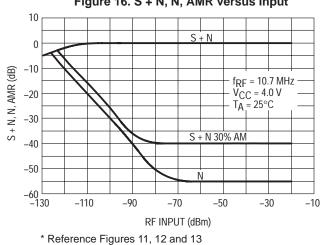
-100

-80

-60

-40

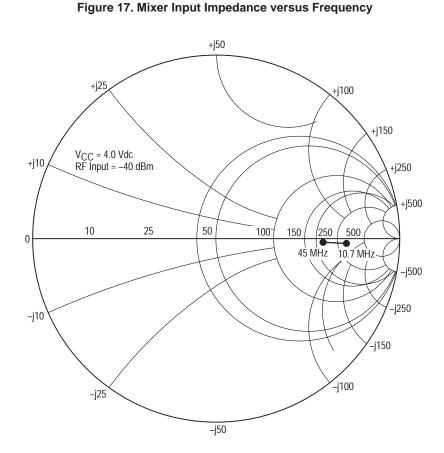
-20

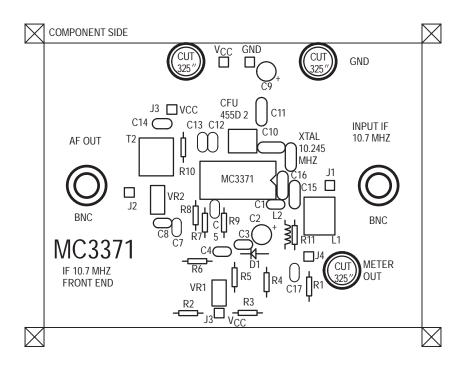


#### Figure 16. S + N, N, AMR versus Input



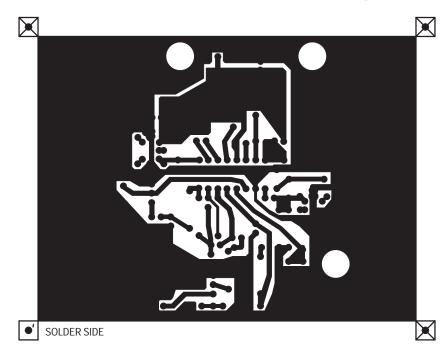
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#### Figure 18. MC3371 PC Board Component View with Matched Input at 10.7 MHz

Figure 19. MC3371 PC Board Circuit or Solder Side as Viewed through Component Side



Above PC Board is laid out for the circuit in Figure 11.

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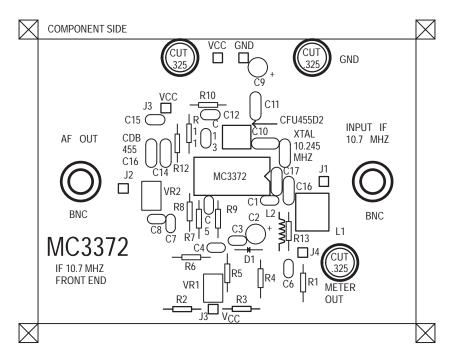
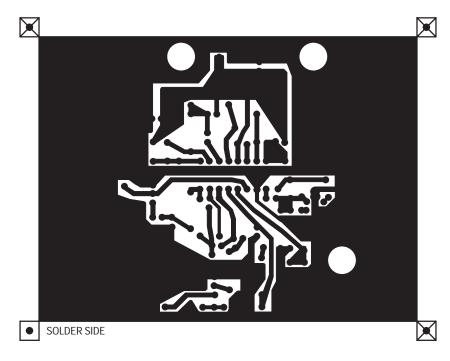


Figure 20. MC3372P PC Board Component View with Matched Input at 10.7 MHz

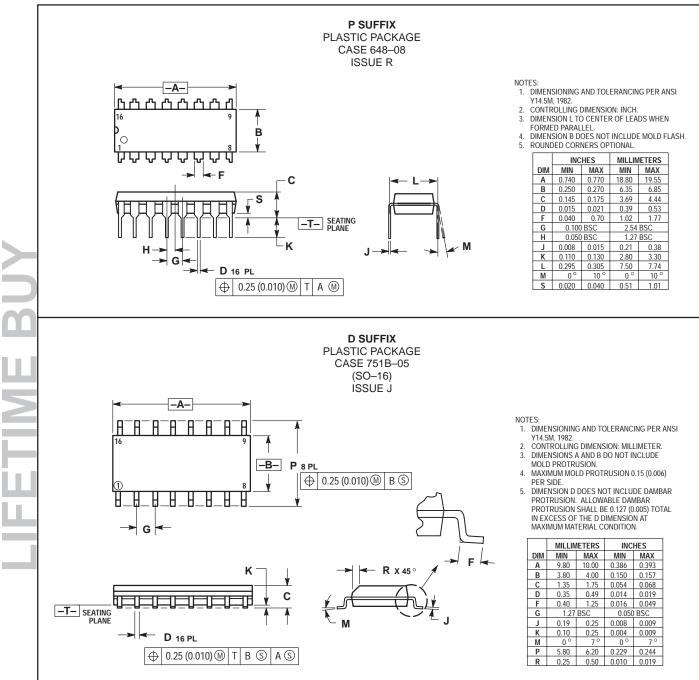
Figure 21. MC3372P PC Board Circuit or Solder Side as Viewed through Component Side

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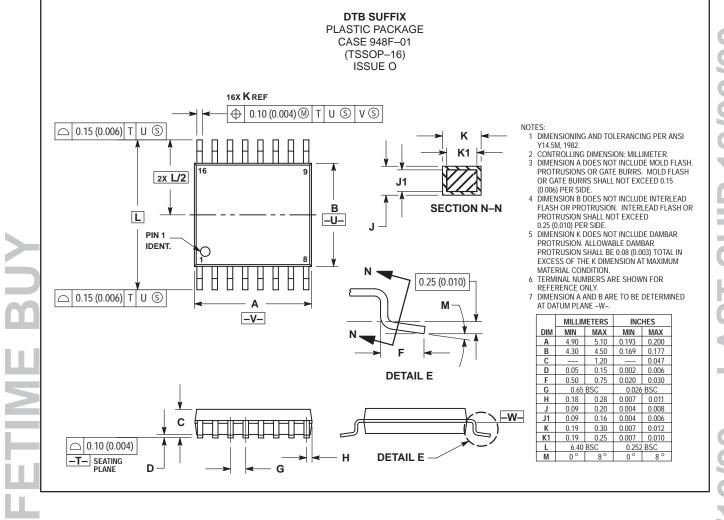


Above PC Board is laid out for the circuit in Figure 12.

#### OUTLINE DIMENSIONS



### OUTLINE DIMENSIONS



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ASIA/PACIFIC: Motorola Semiconductors H.K. Ltd.; 8B Tai Ping Industrial Park, 51 Ting Kok Road, Tai Po, N.T., Hong Kong. 852-26629298

JAPAN: Nippon Motorola Ltd.: SPD, Strategic Planning Office, 4-32-1, Nishi-Gotanda, Shinagawa-ku, Tokyo 141, Japan. 81-3-5487-8488

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