查询MC12019供应商

MOTOROLA

捷多邦,专业PCB打样_Order204is时如急的的y MC12019/D



Dual Modulus Prescaler

The MC12019 is a divide by 20 and 21 dual modulus prescaler. It will divide by 20 when the modulus control input is HIGH and divide by 21 when the modulus control input is LOW.

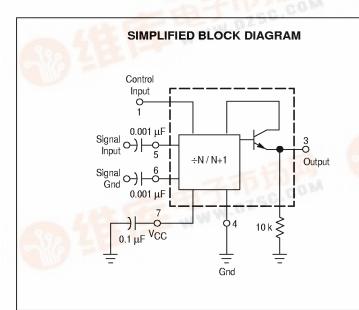
- 225 MHz Toggle Frequency
- Low-Power 7.5 mA Maximum at 5.5 V
- Control Input is Compatible with Standard Motorola CMOS Synthesizers WWW.DZSC.COM
- Emitter Follower Output

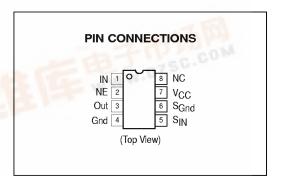
MC12019

MECL PLL COMPONENTS ÷20/21 DUAL MODULUS PRESCALER

SEMICONDUCTOR **TECHNICAL DATA**







ORDERING INFORMATION

Device	Operating Temperature Range	Package
MC12019D	$T_{A} = -40 \text{ to } 85^{\circ}\text{C}$	SO-8



MC12019

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Power Supply Voltage, Pin 7	Vcc	8.0	Vdc
Operating Temperature Range	ТА	-40 to +85	°C
Storage Temperature Range	T _{stg}	–65 to +175	°C

NOTE; ESD data available upon request.

ELECTRICAL CHARACTERISTICS (V_{CC} = 4.5 to 5.5 V; $T_A = -40$ to 85°C), unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave Input)	^f max ^f min	225 —	_	- 20	MHz
Supply Current	lcc	-	-	7.5	mA
Control Input HIGH (÷20)	VIH	2.0	-	-	V
Control Input LOW (÷21)	VIL	-	-	0.8	V
Output Swing Voltage (10 k Ω to ground)	Vout	600	-	1200	mVpp
Input Voltage Sensitivity 20 MHz to 225 MHz	V _{in}	200	_	800	mVpp
PLL Response Time (Notes 1 and 2)	^t PLL	_	_	t _{out} -70	ns

NOTES: 1. tpLL = the period of time the PLL has from the prescaler rising output tranistion (50%) to the modulus control input edge transition (50%) to ensure proper modulus selection. 2. t_{out} = period of output waveform.

MC12019

OUTLINE DIMENSIONS

