

Order this document by MC12015/D



# **Dual Modulus Prescaler**

The MC12015, MC12016 and MC12017 are dual modulus prescalers which will drive divide by 32 and 33, 40 and 41, and 64 and 65, respectively. An internal regulator is provided to allow these devices to be used over a wide range of power–supply voltages. The devices may be operated by applying a supply voltage of 5.0 Vdc  $\pm$ 10% at Pin 7, or by applying an unregulated voltage source from 5.5Vdc to 9.5 Vdc to Pin 8.

- 225 MHz Toggle Frequency
- Low-Power 7.5 mA Maximum at 6.8 V
- Control Input and Output Are Compatible With Standard CMOS
- Connecting Pins 2 and 3 Allows Driving One TTL Load
- Supply Voltage 4.5 V to 9.5 V

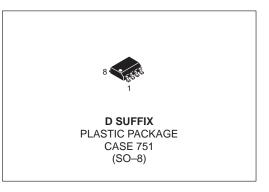
NOT RECOMMENDED FOR NEW DESIGN DEVICES TO BE PHASED OUT. For the MC12015 and MC12016 no replacement available. For the MC12017 consider MC12054A for New Designs.

# MECL PLL COMPONENTS DUAL MODULUS PRESCALER

MC12015

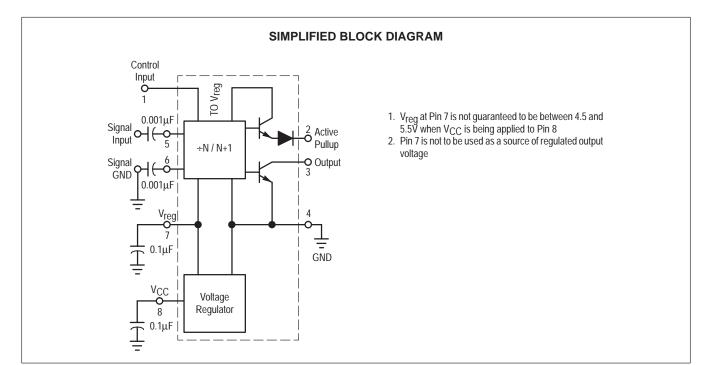
MC12016 MC12017

SEMICONDUCTOR TECHNICAL DATA



#### **ORDERING INFORMATION**

Device	Operating Temperature Range	Package
MC12015D		
MC12016D	T <sub>A</sub> = −40 to 85°C	SO–8
MC12017D		



## MC12015 MC12016 MC12017

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Regulated Voltage, Pin 7	V <sub>reg</sub>	8.0	Vdc
Power Supply Voltage, Pin 8	Vcc	10	Vdc
Operating Temperature Range	TA	-40 to +85	°C
Storage Temperature Range	T <sub>stg</sub>	-65 to +175	°C

NOTE: ESD data available upon request.

### **ELECTRICAL CHARACTERISTICS** (V<sub>CC</sub> = 5.5 to 9.5 V; V<sub>reg</sub> = 4.5 to 5.5 V; T<sub>A</sub> = -40 to 85°C, unless otherwise noted.)

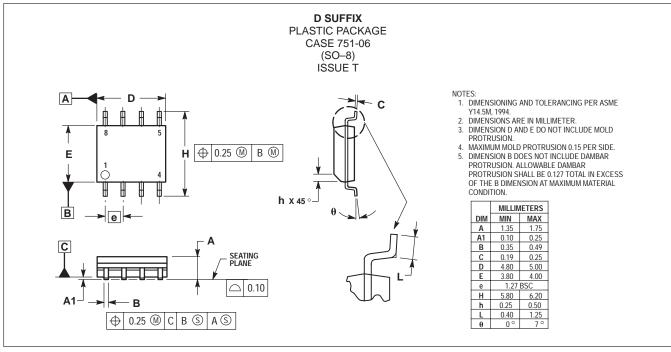
Characteristic	Symbol	Min	Тур	Max	Unit
Toggle Frequency (Sine Wave Input)					MHz
	fmax	225	-	-	
	fmin	-	-	35	
Supply Current	ICC	-	6.0	7.8	mA
Control Input HIGH (+32, 40 or 64)	VIH	2.0	-	-	V
Control Input LOW (+33, 41 or 65)	VIL	-	-	0.8	V
Output Voltage HIGH (I <sub>SOUICE</sub> = 50µA) [Nofe 1]	VOH	2.5	-	-	V
Output Voltage LOW (I <sub>SINk</sub> = 2mA) [Note 1]	V <sub>OL</sub>	-	-	0.5	V
Input Voltage Sensitivity	Vin				mVpp
35 MHz		400	-	800	
50 to 225 MHz		200	-	800	
PLL Response Time [Notes 2 and 3]	tPLL	-	_	t <sub>out</sub> to 70	ns

**NOTES:** 1. Pin 2 connected to Pin 3.

2. tpLL = the period of time the PLL has from the prescaler rising output tranistion (50%) to the modulus control input edge transition (50%) to ensure proper modulus selection. 3.  $t_{out}$  = period of output waveform.

### MC12015 MC12016 MC12017

#### **OUTLINE DIMENSIONS**



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