

**TOSHIBA**

**TC7WH14FU/FK**

TENTATIVE

TOSHIBA CMOS DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

# TC7WH14FU, TC7WH14FK

(UNDER DEVELOPMENT)

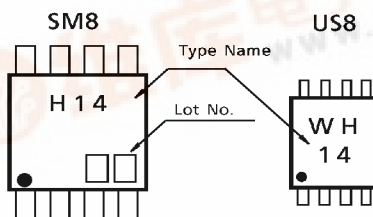
## TRIPLE SCHMITT INVERTER

The TC7WH14 is an advanced high speed CMOS SCHMITT INVERTER fabricated with silicon gate CMOS technology. It achieves the high speed operation similar to equivalent Bipolar Schottky TTL while maintaining the CMOS low power dissipation. Pin configuration and function are the same as the TC7SH14 but the inputs have hysteresis and with its schmitt trigger function, the TC7SH14 can be used as a line receivers which will receive slow input signals. An input protection circuit ensures that 0 to 7V can be applied to the input pins without regard to the supply voltage. This device can be used to interface 5V to 3V systems and two supply systems such as battery back up. This circuit prevents device destruction due to mismatched supply and input voltages.

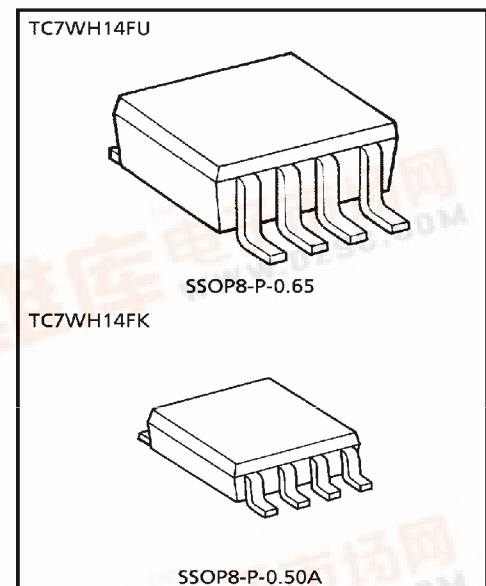
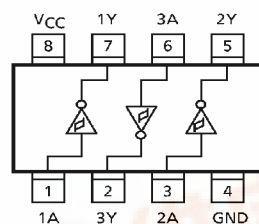
### FEATURES

- High Speed .....  $t_{pd} = 5.5\text{ns}$  (Typ.) at  $V_{CC} = 5\text{V}$
- Low Power Dissipation .....  $I_{CC} = 2\mu\text{A}$  (Max.) at  $T_a = 25^\circ\text{C}$
- High Noise Immunity .....  $V_{NIH} = V_{NIL} = 28\% V_{CC}$  (Min.)
- Power Down Protection is provided on all inputs.
- Balanced Propagation Delays .....  $t_{pLH} = t_{pHL}$
- Wide Operating Voltage Range...  $V_{CC}(\text{opr}) = 2 \sim 5.5\text{V}$

### MARKING



### PIN ASSIGNMENT (TOP VIEW)



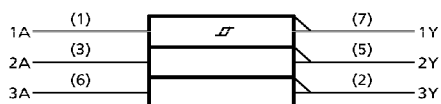
Weight  
 SSOP8-P-0.65 : 0.02g (Typ.)  
 SSOP8-P-0.50A : 0.01g (Typ.)

980508EBA1

- TOSHIBA is continually working to improve the quality and the reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to observe standards of safety, and to avoid situations in which a malfunction or failure of a TOSHIBA product could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent products specifications. Also, please keep in mind the precautions and conditions set forth in the TOSHIBA Semiconductor Reliability Handbook.
- The products described in this document are subject to foreign exchange and foreign trade laws.
- The information contained herein is presented only as a guide for the applications of our products. No responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other rights of the third parties which may result from its use. No license is granted by implication or otherwise under any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.

**MAXIMUM RATINGS** ( $T_a = 25^\circ\text{C}$ )

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	$V_{CC}$	$-0.5 \sim 7.0$	V
DC Input Voltage	$V_{IN}$	$-0.5 \sim 7.0$	V
DC Output Voltage	$V_{OUT}$	$-0.5 \sim V_{CC} + 0.5$	V
Input Diode Current	$I_{IK}$	$-20$	mA
Output Diode Current	$I_{OK}$	$\pm 20$	mA
DC Output Current	$I_{OUT}$	$\pm 25$	mA
DC $V_{CC}$ / Ground Current	$I_{CC}$	$\pm 50$	mA
Power Dissipation	$P_D$	300 (SM8)	mW
		200 (US8)	
Storage Temperature	$T_{stg}$	$-65 \sim 150$	$^\circ\text{C}$
Lead Temperature (10 s)	$T_L$	260	$^\circ\text{C}$

**LOGIC DIAGRAM****TRUTH TABLE**

A	Y
L	H
H	L

**RECOMMENDED OPERATING CONDITIONS**

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	$V_{CC}$	$2.0 \sim 5.5$	V
Input Voltage	$V_{IN}$	$0 \sim 5.5$	V
Output Voltage	$V_{OUT}$	$0 \sim V_{CC}$	V
Operating Temperature	$T_{opr}$	$-40 \sim 85$	$^\circ\text{C}$
Input Rise and Fall Time	$dt/dv$	$0 \sim 100$ ( $V_{CC} = 3.3 \pm 0.3\text{V}$ )	ns / V
		$0 \sim 20$ ( $V_{CC} = 5 \pm 0.5\text{V}$ )	

## DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST CONDITION		V <sub>CC</sub> (V)	Ta = 25°C			Ta = -40~85°C		UNIT
					MIN.	TYP.	MAX.	MIN.	MAX.	
Positive Threshold Voltage	V <sub>P</sub>			3.0	—	—	2.20	—	2.20	V
				4.5	—	—	3.15	—	3.15	
				5.5	—	—	3.85	—	3.85	
Negative Threshold Voltage	V <sub>N</sub>			3.0	0.90	—	—	0.90	—	V
				4.5	1.35	—	—	1.35	—	
				6.0	1.65	—	—	1.65	—	
Hysteresis Voltage	V <sub>H</sub>			3.0	0.30	—	1.20	0.30	1.20	V
				4.5	0.40	—	1.40	0.40	1.40	
				5.5	0.50	—	1.60	0.50	1.60	
High-Level Output Voltage	V <sub>OH</sub>	V <sub>IN</sub> = V <sub>IL</sub>	I <sub>OH</sub> = -50μA	2.0	1.9	2.0	—	1.9	—	V
				3.0	2.9	3.0	—	2.9	—	
				4.5	4.4	4.5	—	4.4	—	
			I <sub>OH</sub> = -4mA	3.0	2.58	—	—	2.48	—	
			I <sub>OH</sub> = -8mA	4.5	3.94	—	—	3.80	—	
Low-Level Output Voltage	V <sub>OL</sub>	V <sub>IN</sub> = V <sub>IH</sub>	I <sub>OL</sub> = 50μA	2.0	—	0.0	0.1	—	0.1	V
				3.0	—	0.0	0.1	—	0.1	
				4.5	—	0.0	0.1	—	0.1	
			I <sub>OL</sub> = 4mA	3.0	—	—	0.36	—	0.44	
			I <sub>OL</sub> = 8mA	4.5	—	—	0.36	—	0.44	
Input Leakage Current	I <sub>IN</sub>	V <sub>IN</sub> = 5.5V or GND		0~5.5	—	—	±0.1	—	±1.0	μA
Quiescent Supply Current	I <sub>CC</sub>	V <sub>IN</sub> = V <sub>CC</sub> or GND		5.5	—	—	2.0	—	20.0	μA

**AC ELECTRICAL CHARACTERISTICS** (Input  $t_r = t_f = 3\text{ns}$ )

CHARACTERISTIC	SYMBOL	TEST CONDITION			Ta = 25°C			Ta = - 40~85°C		UNIT
			V <sub>CC</sub> (V)	C <sub>L</sub> (pF)	MIN.	TYP.	MAX.	MIN.	MAX.	
Propagation Delay Time	t <sub>pLH</sub> t <sub>pHL</sub>	—	3.3 ± 0.3	15	—	8.3	12.8	1.0	15.0	ns
				50	—	10.8	16.3	1.0	18.5	
			5.0 ± 0.5	15	—	5.5	8.6	1.0	10.0	
				50	—	7.0	10.6	1.0	12.0	
Input Capacitance	C <sub>IN</sub>	—			—	4	10	—	10	pF
Power Dissipation Capacitance	C <sub>pD</sub>	(Note 1)			—	21	—	—	—	pF

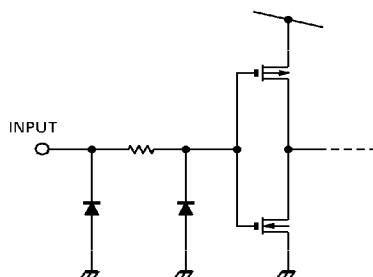
(Note 1) : C<sub>PD</sub> is defined as the value of the internal equivalent capacitance which is calculated from the operating current consumption without load.

Average operating current can be obtained by the equation :

$$I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}$$

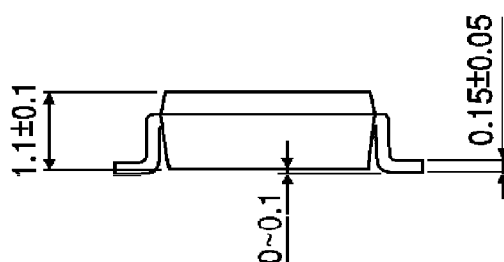
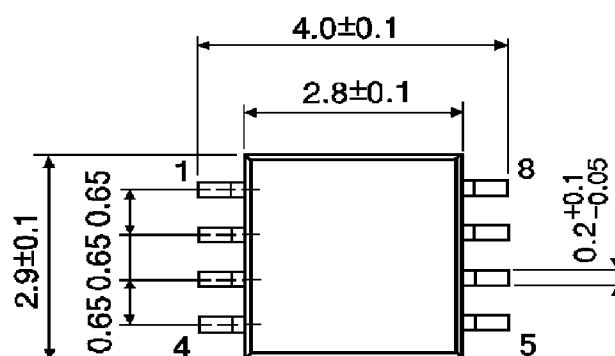
**NOISE CHARACTERISTICS** (Ta = 25°C, Input  $t_r = t_f = 3\text{ns}$ )

CHARACTERISTIC	SYMBOL	TEST CONDITION	V <sub>CC</sub> (V)	TYP.	LIMIT	UNIT
Quiet Output Maximum Dynamic V <sub>OL</sub>	V <sub>OLP</sub>	C <sub>L</sub> = 50pF	5.0	0.3	0.8	V
Quiet Output Minimum Dynamic V <sub>OL</sub>	V <sub>OLV</sub>	C <sub>L</sub> = 50pF	5.0	−0.3	−0.8	V
Minimum High Level Dynamic Input Voltage	V <sub>IHD</sub>	C <sub>L</sub> = 50pF	5.0	—	3.5	V
Maximum Low Level Dynamic Input Voltage	V <sub>ILD</sub>	C <sub>L</sub> = 50pF	5.0	—	1.5	V

**INPUT EQUIVALENT CIRCUIT**

**OUTLINE DRAWING**  
SSOP8-P-0.65

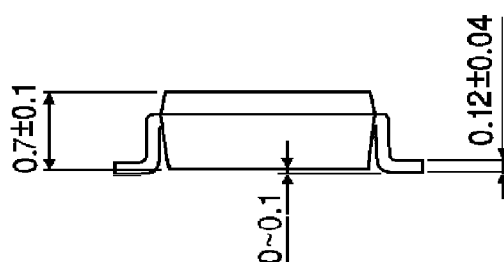
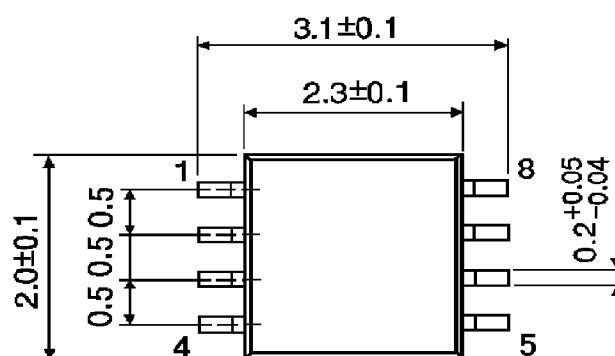
Unit : mm



Weight : 0.02g (Typ.)

**OUTLINE DRAWING**  
SSOP8-P-0.50A

Unit : mm



Weight : 0.01g (Typ.)