查询MPF990 供应商

# 捷多邦,专业PCB打样工厂,24小时加急出货

# MPF930, MPF960, MPF990

Preferred Device

# Small Signal MOSFET 2 Amps, 35, 60, 90 Volts N-Channel TO-92

#### MAXIMUM RATINGS

	•				
Rating	Symbol	MPF930	MPF960	MPF990	Unit
Drain–Source Voltage	VDS	35	60	90	Vdc
Drain-Gate Voltage	VDG	35	60	90	Vdc
$\label{eq:Gate-Source} \begin{array}{c} \mbox{Voltage} \\ - \mbox{Continuous} \\ - \mbox{Non-repetitive} \\ (t_p \leq 50 \ \mu s) \end{array}$	VGS VGSM		±20 ±40		Vdc Vpk
Drain Current Continuous (Note 1.) Pulsed (Note 2.)	I <sub>D</sub> I <sub>DM</sub>	-13	2.0	M	Adc
Total Device Dissipation @ $T_A = 25^{\circ}C$ Derate above $25^{\circ}C$	PD	N W W.	1.0 8.0	6.0	Watts mW/°C
Operating and Storage Junction Temperature Range	TJ, T <sub>stg</sub>		–55 to 150		°C
Thermal Resistance	θJA		125	FR	°C/W

1. The Power Dissipation of the package may result in a lower continuous drain current.

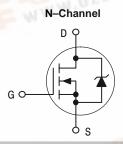
2. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.

# ON ....

## **ON Semiconductor**

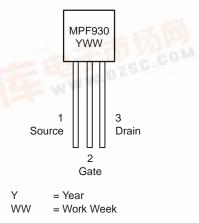
http://onsemi.com

2 AMPERES 35, 60, 90 VOLTS RDS(on) = 0.7 Ω (MPF930) RDS(on) = 0.8 Ω (MPF960) RDS(on) = 1.2 Ω (MPF990)





#### MARKING DIAGRAM & PIN ASSIGNMENT



#### ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 3 of this data sheet.

**Preferred** devices are recommended choices for future use and best overall value.



Characteristic	Characteristic		Min	Тур	Max	Unit
OFF CHARACTERISTICS				•	•	•
Drain–Source Breakdown Voltage (V <sub>GS</sub> = 0, I <sub>D</sub> = 10 $\mu$ Adc)	MPF930 MPF960 MPF990	V <sub>(BR)</sub> DSX	35 60 90	_ _ _	_ _ _	Vdc
Gate Reverse Current ( $V_{GS}$ = 15 Vdc, $V_{DS}$ =	0)	IGSS	-	-	50	nAdc
ON CHARACTERISTICS (Note 2.)						
Zero–Gate–Voltage Drain Current ( $V_{DS}$ = Maximum Rating, $V_{GS}$ = 0)		IDSS	-	-	10	μAdc
Gate Threshold Voltage $(I_D = 1.0 \text{ mAdc}, V_{DS} = V_{GS})$		VGS(Th)	1.0	-	3.5	Vdc
Drain–Source On–Voltage ( $V_{GS} = 10$ Vdc) ( $I_D = 0.5$ Adc)	MPF930 MPF960 MPF990	VDS(on)	- - -	0.4 0.6 0.6	0.7 0.8 1.2	Vdc
(I <sub>D</sub> = 1.0 Adc)	MPF930 MPF960 MPF990		_ _ _	0.9 1.2 1.2	1.4 1.7 2.4	
(I <sub>D</sub> = 2.0 Adc)	MPF930 MPF960 MPF990		- - -	2.2 2.8 2.8	3.0 3.5 4.8	
Static Drain–Source On Resistance (V <sub>GS</sub> = 10 Vdc, I <sub>D</sub> = 1.0 Adc)	MPF930 MPF960 MPF990	<sup>r</sup> DS(on)	_ _ _	0.9 1.2 1.2	1.4 1.7 2.0	Ω
On–State Drain Current (V <sub>DS</sub> = 25 Vdc, V <sub>GS</sub> = 10 Vdc)		ID(on)	1.0	2.0	-	Amps
SMALL-SIGNAL CHARACTERISTICS						•
Input Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$		C <sub>iss</sub>	_	70	-	pF
Reverse Transfer Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$		C <sub>rss</sub>	_	20	_	pF
Output Capacitance $(V_{DS} = 25 \text{ Vdc}, V_{GS} = 0, f = 1.0 \text{ MHz})$		C <sub>OSS</sub>	-	49	-	pF
Forward Transconductance $(V_{DS} = 25 \text{ Vdc}, I_D = 0.5 \text{ Adc})$		9fs	200	380	-	mmhos
SWITCHING CHARACTERISTICS				•	•	
Turn–On Time		ton	_	7.0	15	ns
Turn–Off Time		toff	_	7.0	15	ns

## **ELECTRICAL CHARACTERISTICS** (T<sub>A</sub> = 25°C unless otherwise noted)

2. Pulse Test: Pulse Width  $\leq$  300 µs, Duty Cycle  $\leq$  2.0%.

## **RESISTIVE SWITCHING**

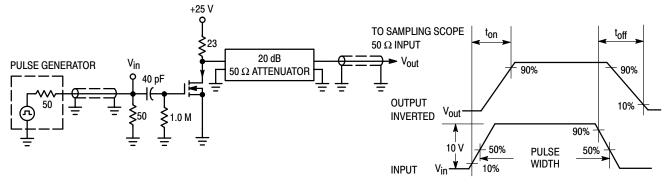
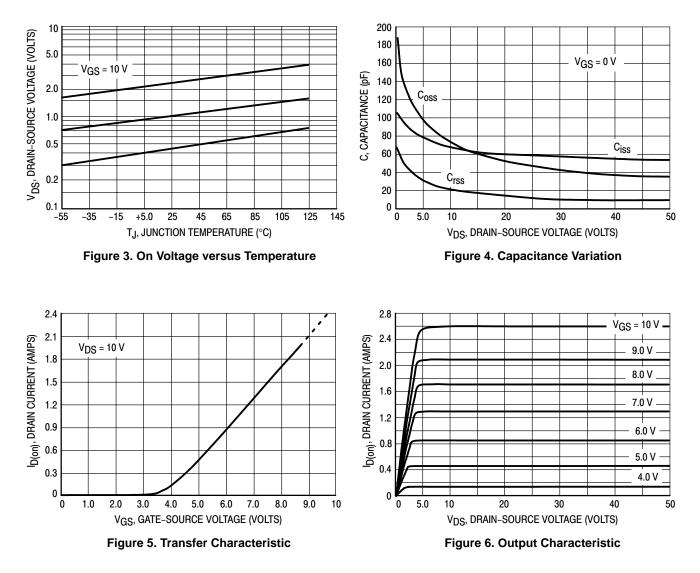


Figure 1. Switching Test Circuit

Figure 2. Switching Waveforms

Device	Package	Shipping
MPF930	TO-92	1000 Unit/Box
MPF930RLRE	TO-92	2000 Tape & Reel
MPF930A	TO-92	1000 Unit/Box
MPF930ARLRE	TO-92	2000 Tape & Reel
MPF960	TO-92	1000 Unit/Box
MPF960RLRA	TO-92	2000 Tape & Reel
MPF990	TO-92	1000 Unit/Box
MPF990RLRA	TO-92	2000 Tape & Reel
MPF990RLRP	TO-92	2000 Ammo Pack

#### **ORDERING INFORMATION**



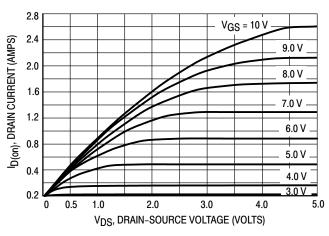
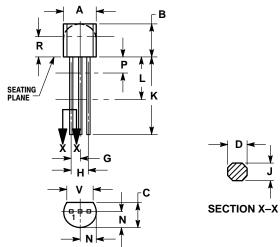


Figure 7. Saturation Characteristic

#### PACKAGE DIMENSIONS

TO-92 CASE 29-11 ISSUE AL





- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH. 3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED. 4. LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

	INCHES		MILLIM	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.175	0.205	4.45	5.20
В	0.170	0.210	4.32	5.33
С	0.125	0.165	3.18	4.19
D	0.016	0.021	0.407	0.533
G	0.045	0.055	1.15	1.39
Η	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
Κ	0.500		12.70	
L	0.250		6.35	
Ν	0.080	0.105	2.04	2.66
Ρ		0.100		2.54
R	0.115		2.93	
٧	0.135		3.43	

STYLE 22: PIN 1. SOURCE 2. GATE 3. DRAIN

# <u>Notes</u>

# <u>Notes</u>

ON Semiconductor and 🤍 are trademarks of Semiconductor Components Industries, LLC (SCILLC). SCILLC reserves the right to make changes without further notice to any products herein. SCILLC makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does SCILLC assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. "Typical" parameters which may be provided in SCILLC data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. SCILLC does not convey any license under its patent rights nor the rights of others. SCILLC products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the SCILLC product could create a situation where personal injury or death may occur. Should Buyer purchase or use SCILLC products for any such unintended or unauthorized application, Buyer shall indemnify and hold SCILLC and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that SCILLC was negligent regarding the design or manufacture of the part. SCILLC is an Equal Opportunity/Affirmative Action Employer.

#### PUBLICATION ORDERING INFORMATION

#### NORTH AMERICA Literature Fulfillment: Literature Distribution Center for ON Semiconductor P.O. Box 5163, Denver, Colorado 80217 USA Phone: 303-675-2175 or 800-344-3860 Toll Free USA/Canada Fax: 303-675-2176 or 800-344-3867 Toll Free USA/Canada Email: ONlit@hibbertco.com Fax Response Line: 303-675-2167 or 800-344-3810 Toll Free USA/Canada N. American Technical Support: 800-282-9855 Toll Free USA/Canada

- EUROPE: LDC for ON Semiconductor European Support German Phone: (+1) 303-308-7140 (Mon-Fri 2:30pm to 7:00pm CET) Email: ONlit-german@hibbertco.com
- Phone: (+1) 303-308-7141 (Mon-Fri 2:00pm to 7:00pm CET) French Email: ONlit-french@hibbertco.com
- English Phone: (+1) 303-308-7142 (Mon-Fri 12:00pm to 5:00pm GMT) Email: ONlit@hibbertco.com

#### EUROPEAN TOLL-FREE ACCESS\*: 00-800-4422-3781

\*Available from Germany, France, Italy, UK, Ireland

#### **CENTRAL/SOUTH AMERICA:**

Spanish Phone: 303-308-7143 (Mon-Fri 8:00am to 5:00pm MST) Email: ONlit-spanish@hibbertco.com Toll-Free from Mexico: Dial 01-800-288-2872 for Access then Dial 866-297-9322

ASIA/PACIFIC: LDC for ON Semiconductor - Asia Support Phone: 303-675-2121 (Tue-Fri 9:00am to 1:00pm, Hong Kong Time) Toll Free from Hong Kong & Singapore: 001-800-4422-3781 Email: ONlit-asia@hibbertco.com

JAPAN: ON Semiconductor, Japan Customer Focus Center 4-32-1 Nishi-Gotanda, Shinagawa-ku, Tokyo, Japan 141-0031 Phone: 81-3-5740-2700 Email: r14525@onsemi.com

#### ON Semiconductor Website: http://onsemi.com

For additional information, please contact your local Sales Representative.