M51437FP

ELECTRONIC VOLUME CONTROL WITH TONE CONTROLLER FOR MULTIAMPLIFIER APPLICATIONS

DESCRIPTION

The M51137FP is a Bi-CMOS IC developed for audio-visual systems. It is suitable for multiamplifiers, being used for processing small analog signals in the stage before power amplifier. The IC uses 8-bit serial data transmitted from a microcomputer in order to perform sound control such as master volume control (VCA system), tone control (bass, mid, and treble), and bass boosting. Its applications also include use as a single output and car audio systems.

FEATURES

- Built-in VCA circuit for main volume control
- Variable volume range······ 96dB~ + 9dB
- Capability of controlling VCA from external source
- Built-in bass booster enhances heavy bass
- ■Tone control

Treble ······-	-	10dB~+	16dB	(2dB/step)
Mid	-	10dB~+	10dB	(2dB/step)
Bass	-	10dB~+	16dB	(2dB/step)
Bass boost ·····	-	10dB~+	10dB	(2dB/step)

For controlling in each mode, the IC uses built-in microcomputer interface and serial data that regulates volume (8-bit), treble, mid, bass, and bass boost (4-bit)

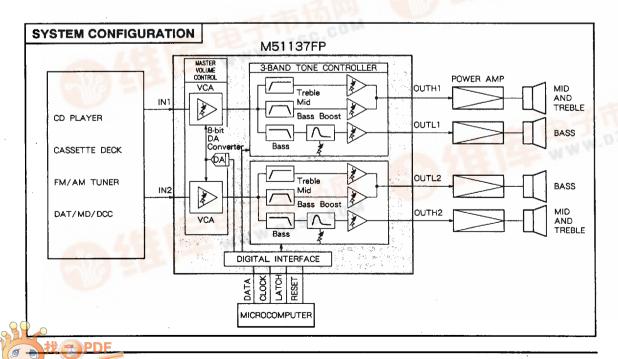
RECOMMENDED OPERATING CONDITIONS

Supply voltage	range ····· Vcc = 7.5~12V
Rated supply v	voltageVcc = 9V



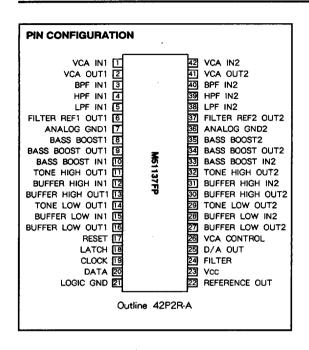
Outline 42P2R-A

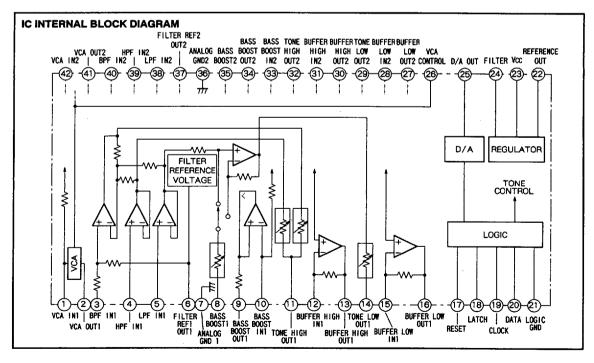
0.8mm pitch 450mil SSOP
(8.4mm × 17.5mm × 2.0mm)



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PIN DESCRIPTION

Pin No.	Name	Function
① (②)	VCA IN 1 (2)	Signal input terminal of ch1 (2)
② (41)	VCA OUT 1 (2)	Signal output terminal of ch1 (2)
3 (40)	BPF IN 1 (2)	BPF input terminal of ch1 (2)
4 (39)	HPF IN 1 (2)	HPF input terminal of ch1 (2)
(S) (SS)	LPF IN 1 (2)	LPF input terminal of ch1 (2)
(G) (D)	Filter REF 1 (2)	Filter output for analog reference voltage
⑦ (多)	Analog GND 1 (2)	Ground of analog circuit
® (S)	Bass-boost 1 (2)	Bass-boost gain terminal
(9)	Bass-boost OUT 1 (2)	Bass-boost resonanse Amplifier output
10 (33)	Bass-boost IN 1 (2)	Bass-boost resonanse Amplifier input
① (②)	Tone high OUT 1 (2)	Treble, mid output
12 (31)	Buffer high IN 1 (2)	Treble, mid buffer input
(3) (30)	Buffer high OUT 1 (2)	Treble, mid buffer output
(B) (S)	Tone low OUT 1 (2)	Bass, bass-boost out
(3) (28)	Buffer low IN 1 (2)	Bass, bass-boost buffer input
16 (27)	Buffer low OUT 1 (2)	Bass, bass-boost buffer output
0	RESET	MUTE. Set Volume minimum and tone control minimum by high level voltage.
18	LATCH	Latch signal of serial data from microcomputer to the IC. Operate at rising eges of pulse.
19	CLOCK	Clock signal of serial data from microcomputer to the IC. Operate at rising eges of pulse.
Ø	DATA	Serial data input. (LSB first)
Ø	Logic GND	Ground of digital circuit
22	REFERENCE OUT	Reference output voltage source. (5.8Vtyp)
3	Vcc	Supply voltage (7.5~12V)
29	FILTER	Filter for ripple
8	D/A OUT	VCA control voltage source by D/A convertor
⊗	VCA CONTROL	VCA gain control terminal

ABSOLUTE MAXIMUM RATINGS

Symbol	Parameter	Ratings	Unit
Vcc	Supply voltage	14	V
Vı	Digital input voltage	- 0.3~7.0	V
Pa	Power dissipation	1000 * standard board	mW
Ke	Thermal derating	10 (T _a ≥ 25°C)	mW/℃
Topr	Operating temperature	- 10~ + 70	ొ
Tstg	Storage temperature	- 40~ + 125	ొ

* Standard board

board sizeboard thickness

70mm × 70mm

1.6mm glass epoxy

 board material copper pattern

18µm

copper thickness copper size

0.25mm(width) 30mm(length/lead)



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ELECTRICAL CHARACTERISTICS

(Ta = 25 ℃, Vcc = 9V, Control data: FF5550 (volume max/tone flat), f = 1kHz, unless otherwise noted)

	_		Test conditions						Measu-	Measu- Limits				
Symbol	Parameter Input condition C				SA	Switch condition SA SB1 SB2 SC			rement point	: 	Тур	Max	Unit	
lcc	tage	Circuit current	Quiescent	FF5550	OPEN	OPEN	BPF	а	PIN@	26	38	50	mA	
VREF	Reference voltage		Û	Û	Û	Û	Û	Û	PIN@	5.4	5.8	6.3	V	
VFIL	Supp	Filter voltage	Û	Û	Û	Û	Û	បិ	PIN 2	8.2	8.9	-	V	
ìн	Logic		VIH = 4.5V	FF5550	OPEN	OPEN	BPF	Ь	PIN® PIN® PIN® PIN®	0.3	1.0	3.0	μА	
lı <u>.</u>		Level "L" input current	VIL = 0.5V	Û	Û	Û	Û	Û	Û	- 0.3	0.0	0.3	μА	
OFSTM1	offset	Tre/mid switching offset voltage	Quiescent (data switching offset voltage difference)	FFD050	OPEN	OPEN	BPF	а	PIN®	- 20	0	+ 20	mV _,	
OFSBB1	tching	Boost switching offset voltage 1	Û	FF5550 → FF5555	Û	Û	Û	Û	PIN® PIN®	- 10	0	+ 10	mV	
OFSBB2	Sw.	Boost switching offset voltage 2	Û	FF5550	Û	Û	Û	Û	PIN® PIN®	- 10	0	+ 10	m∨	
CBVT	balance	Total channel balance 1	(Calculation)	_	_	_	-	_	CB+ CBT	- 3	0	3	dB	
СВУМ	-	Total channel balance 2	(Calculation)	-	_	-	-	-	CB + CBM	- 3	٥	3	dB	
CBVB	Chann	Total channel balance 3	(Calculation)	_	-	-	-	_	CB + CBBA	- 3	0	3	ďΒ	
ATT (min)		Minimum Attenuation level	$V_i = -14dBV$ * 1	FF5550	CLOSE	OPEN	BPF	а	A(1), A(2)	7.2	9.0	10.8	dB	
СВ		Channel balance	Û	Û	Û	Û	Û	Û	A(1)/ A(2)	- 1.8	0	1.8	dB	
THD		Total harmonic distortion	V _i =-14dBV, * 1 BPF=400Hz~30kHz	Û	Û	Û	Û	Û	A(1), A(2)	_	0.02	0.1	%	
No (min)	awn	Noise voltage	Quiescent JHF-A	Û	OPEN	Û	Û	Û	Û	_	25.0	56.0	μVrms	
THD (max)	5	Maximum total harmonic distortion	V _i =-3dBV, * 1 BPF=400Hz~30kHz	Û	CLOSE	Û	Û	Û	Û	-	0.1	1.0	%	
ATT (-10)	Electronic	Attenuation level (- 10dB)	$V_i = -14dBV^{*1}$	9A5550	Û	Û	Û	Û	Û	- 2.8	- 1.0	0.8	dB	
ATT (max)	읍	Maximum attenuation level	$V_i = -3 dBV_i * 1$ IHF-A	005550	Û	Û	Û	Û	Û	_	- 97	- 77	dB	
No		Maximum attenuation noise voltage	Quiescent IHF-A	Û	OPEN	Û	Û	Û	Û	-	10.0	20.0	μVrms	
СТ		Cross talk	Vi = - 3dBV, * 1 IHF-A	FF5550	OPEN/ CLOSE CLOSE/ OPEN	Û	Û	Û	Û	-	- 90	- 70	dВ	
GVT		Voltage gain	Vi = - 5dBV * 2	FF5550	OPEN	CLOSE	HPF	а	B(1), B(2)	- 19	- 17	- 15	dВ	
СВТ	REBLE	Channel balance	Û	Û	Û	Û	Û	Û	B(1)/ B(2)	- 2	0	+ 2	dB	
THDT	<u></u>	Total harmonic distortion	$V_i = -5 dBV$ BPF=400Hz \sim 30kHz	Û	Û	Û	Û	Û	B(1), B(2)	-	0.01	0.1	%	
NoT	_[Noise voltage	Quiescent IHF-A	Û	Û	OPEN	Û	Û	Û	-	4.0	8.0	μVrms	

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ELECTRICAL CHARACTERISTICS (cont.) (Ta = $25 \, ^{\circ}$ C, Vcc = 9V, Control data : FF5550 (volume max/tone flat), f = 1kHz, unless otherwise noted)

				Test cor	ditions				Measu-	Limits			
Symbol		Parameter	Input condition	Control data		vitch c			rement point	Min	Тур	Max	Unit
TUDT	T Maximum total		V;=+6dBV. * 2		SA	SB1	SB2	SC	B(1),				
THDT		harmonic distortion	BPF=400Hz~30kHz	FFD550	OPEN	CLOSE	HPF	a	B(2)	-	0.1	1.0	%
GVT (max)	щ	Maximum voltage gain	Vi = - 5dBV	Û	Û	Û	Û	Û	Û	-3	- 1	+ 1	dB
GVT (min)	삞	Minimum voltage gain	Û	FF0550	Û	Û	Û	Û	Û	- 29	→ 27	- 25	dB
СТТ		Cross talk	V _i = + 6dBV * 2 IHF-A	FFD550	បិ	OPEN/ CLOSE CLOSE/ OPEN	Û	Û	Û	-	80	- 60	dB
GVM		Voltage gain	$V_i = -5 dBV^{*3}$	FF5550	OPEN	CLOSE	BPF	а	B(1), B(2)	- 19	- 17	- 15	dB
СВМ		Channel balance	Û	Û	Û	Û.	Û	Û	B(1)/ B(2)	- 2	0	+ 2	dB
THDM		Total harmonic distortion	Vi = -5dBV BPF=400Hz~30kHz	Û	Û	Û	Û	Û	B(1), B(2)	_	0.01	0.1	%
NoM		Noise voltage	Quiescent IHF-A	Û	Û	OPEN	Û	Û	Û	_	4.0	8.0	μVrms
THDM max	₽	Maximum total harmonic distortion	Vi=+6dBV, * 3 BPF=400Hz~30kHz	FF5A50	Û	CLOSE	Û	Û	Û	_	0.1	1.0	%
GVM (max)		Maximum voltage gain	V _i = - 5dBV * 3	Û	Û	Û	Û	Û	Û	- 9	- 7	- 5	dB
GVM (min)		Minimum voltage gain	Û	FF5050	Û	Û	Û	Û	Û	- 29∢	., – 27	- 25	dB
СТМ		Cross talk	V _i = + 6dBV, * 3 IHF-A	FF5 A 50	Û	OPEN/ CLOSE/ CLOSE/ OPEN	l 1r	Û	Û	_	- 80	- 60	d₿
GVBA		Voltage gain	Vi = - 5dBV * 4	FF5550	OPEN	CLOSE	LPF	а	C(1), C(2)	- 19	- 17	- 15	dB
CBBA		Channel balance	Û	Û	Û	Û	Û	Û	C(1)/ C(2)	- 2	0	+ 2	dB
THDBA		Total harmonic distortion	V _i = − 5dBV BPF=400Hz ~30kHz	Û	Û	Û	Û	Û	C(1), C(2)	_	0.01	0.1	%
NoBA		Noise voltage	Quiescent IHF-A	Û	Û	OPEN	Û	Û	Û	_	4.0	8.0	μVrms
THDBA max		Maximum total harmonic distortion		FF55DC	Û	CLOSE	Û	Û	Û	-	0.1	1.0	%
GVBA (max)] "	Maximum voltage gain	V _i = - 5dBV * 4	Û	Û	Û	Û	Û	Û	- 3	- 1	+ 1	dB
GVBA (min)	Bass	Minimum voltage gain	Û	FF5500	Û	Û	Û	Û	Û	- 29	- 27	- 25	dB
CTBA		Cross talk	V _i = + 6dBV *4 IHF-A	FF55D0	Û	OPEN/ CLOSE CLOSE, OPEN	Û	Û	î	-	- 80	- 60	dB
THD88 max		Boost maximum total harmonic distortion	V _i =-10dBV, * 4 BPF=400Hz~30kHz	FF55D5	i î	CLOSE	Û	Û	Û	-	0.1	1.0	dB
GVBB (max)		Boost maximum voltage gain	Vi = - 10dBV * 4 · f = 800Hz	FF5555	Û	Û	Û	Û	Û	10	- 7	- 5	dB
GVBB (min)		Boost minimum voltage gain	Û	FF5550	r r	Û	Û	Û	Û	- 29	- 27	- 24	dB

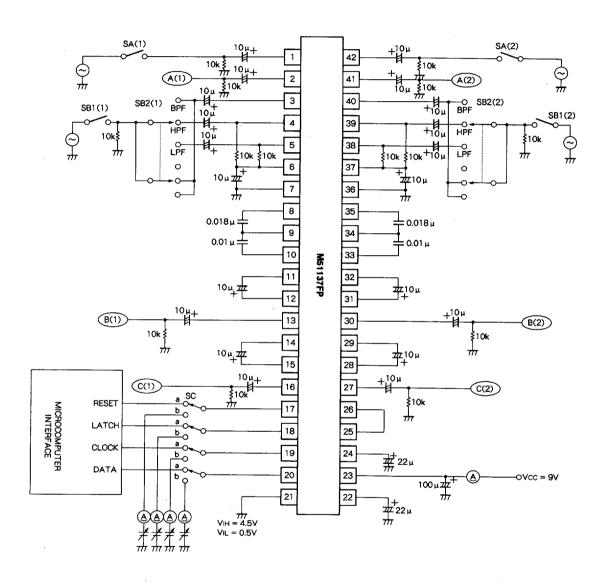
Note: *1; Vi is VCA input voltage. *2; Vi is HPF input voltage. *3; Vi is BPF input voltage. *4; Vi is LPF input voltage.



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TEST CIRCUIT



Units Resistance : Q Capacitance : F

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OPERATIONAL DESCRIPTION

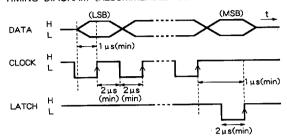
1. CONTROL METHOD

(1) DIGITAL CONTROL SPECIFICATION

Data format

	8bit		4bit	4bit	4bit	4bit	
(MSB)	Volume	•	Treble	Mid	Bass	Bass boost	(LSB)
	Treble : Mid : Bass : Bass-boost :		D (14 A (11 D (14	oit D/A 4 step) I step) 4 step) I step)	data)		

TIMING DIAGRAM (RECOMMENDED CONDITION)



Note 1. RESET (MUTE) is volume minimum and tone control minimum by "H" level.
Puls width 2 µs(min)
2. CLOCK, LATCH functions operates at rising ege of pulse.
3. Recommended input level

"H" level: more than 4V
"L" level: less than 1V
the, threshold voltage (Logic input buffer) is about 2.5V.

CONTROL DATA TABLE

D/A converter for VCA TREBLE					MID		BASS	BASS-BOOST		
DATA	OUTPUT VOLTAGE	DATA	GAIN	DATA	DATA GAIN DA		GAIN	DATA	GAIN	
00	Vz	0	- 10dB	0	- 10dB	0	- 10dB	0	± OdB	
01	255Vz + VF 256	1	– 8dB	1	- 8dB	1	- 8dB	1	+ 2dB	
<u> </u>	250	2	- 6dB	2	- 6dB	2	- 6dB	2	+ 4dB	
		3	- 4dB	3	- 4dB	3	4dB	3	+ 6dB	
1 :		4	- 2dB	4	- 2dB	4	- 2dB	4	+ 8dB	
Li		5	± OdB	5	± OdB	5	± OdB	5	+ 10dB	
1 !	ì	6	+ 2dB	6	+ 2dB	6	+ 2dB	6		
1		7	+ 4dB	7	+ 4dB	7	+ 4dB	7		
	1	8	+ 6dB	8	+ 6dB	8	+ 6dB	8		
l i	1 !	9	+ 8dB	9	+ 8dB	9	+ 8dB	9	- 2dB	
1 !		Ā	+ 10dB	A	+ 10dB	Α	+ 10dB	Α	- 4dB	
		В	+ 12dB	В	_	В	+ 12dB	В	- 6dB	
1 !	į į	C	+ 14dB	С	_	С	+ 14dB	С	- 8dB	
		D	+ 16dB	D	_	D	+ 16dB	D	– 10dB	
FE.	2Vz + 254VF 256	E	-	E	_	E	_	Е	-	
FF	Vz + 255VF 256	F	_	F	_	F	_	F	_	

Note typical or designed value. Vz, VF is internal power supply voltage. $+ 12dB\sim + 16dB$ of treble and bass are for loudness.

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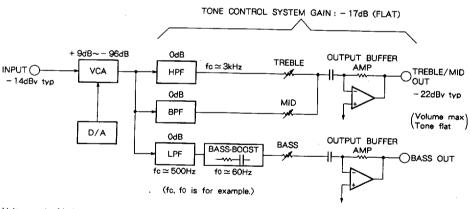
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VCA GAIN LEVEL (EXAMPLE)

Control data	VCA gain level
(D/A converter)	(dB)
OF	(Maximum attenuation)
1F	- 79
2F	- 59
3F	- 44
4F	- 32
5F	- 23
6F	- 15
7F	9
8F	- 4
9F	0
AF	+ 3
BF	+5
CF	+6
DF	+ 7
EF	+ 8
FF	+ 9

(2) SIGNAL PROCESSING SYSTEM

[System] {Total gain: -8dB (VCA MAX)}



Voltage gain (designed value. Tone control system gain: - 17dB (flat))

Volume : + 9dB~ - 96dB typ.

(VCA) Treble

: - 10dB~0dB~ + 10dB (2dB/step)

(+ 12dB, + 14dB, + 16dB for loudness)

Mid : - 10dB~0dB~ + 10dB (2dB/step)

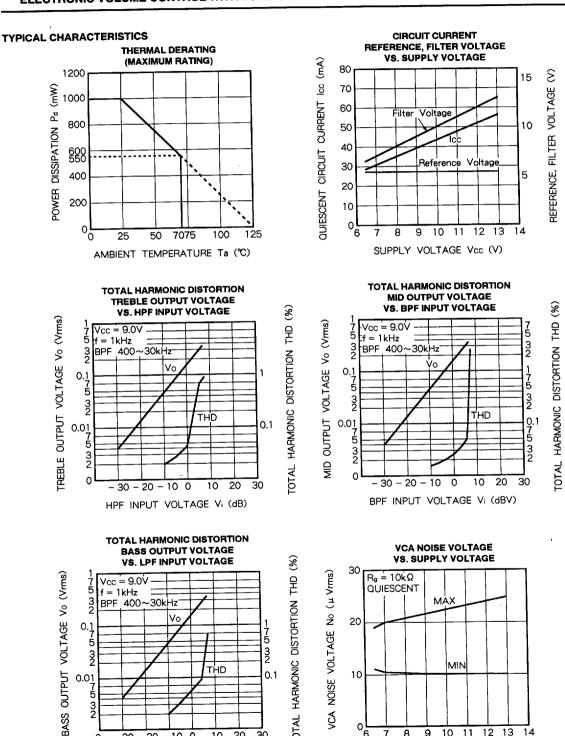
s : - 10dB~0dB~ + 10dB (2dB/step)

(+12dB, +14dB, +16dB for loudness)

Bass-boost : - 10dB~0dB~ + 10dB (2dB/step)

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TOTAL

20

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0 <u>k</u>

10

SUPPLY VOLTAGE Vcc (V)

13

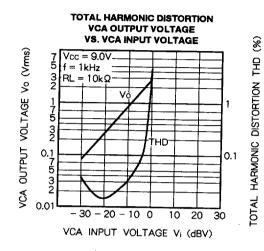
0

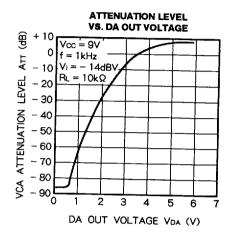
- 30 - 20 - 10 0

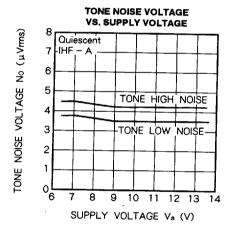
LPF INPUT VOLTAGE Vi (dB)

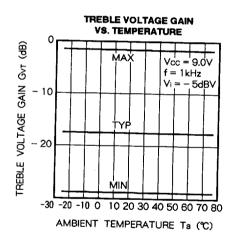
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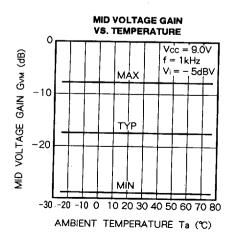
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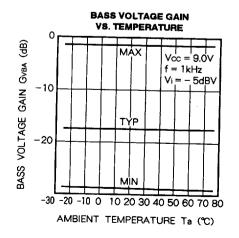










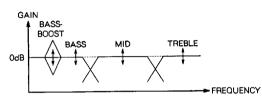


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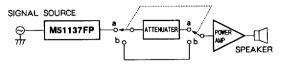
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APPLICATION NOTES

- (1) Take care of the heat radiation of PCB.
- (2) Take care of a PCB design about digital noise.
- (3) The IC has three GND pins.
- (4) Take care of electrostatic damage of 6 pin and 9 pin.
- (5) Take care of gain characteristics of tone control. The loose attenuation characterestics of filters will disturb the frequency respose in another filtering region.



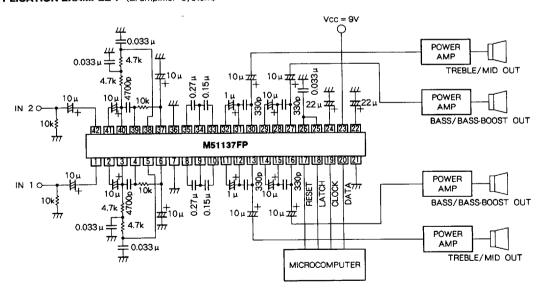
(6) NOISE IMPROVEMENT METHOD



The additional attenuater improves the noise characteristics for small signals.

Volume : Small······a Volume : Large······b

APPLICATION EXAMPLE 1 (Bi-amplifier system)



Units Resistance : Ω

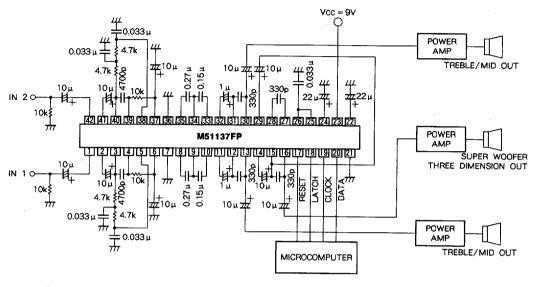
Capacitance : F



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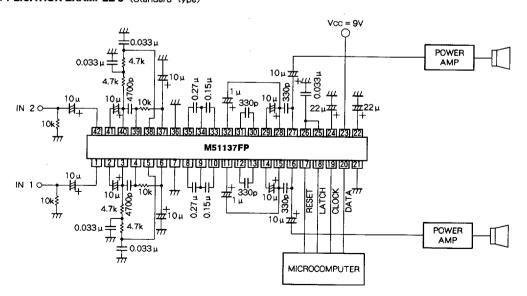
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APPLICATION EXAMPLE 2 (Three dimensional type)



Units Resistance : Q Capacitance : F

APPLICATION EXAMPLE 3 (Standard type)



Units Resistance : Q Capacitance : F