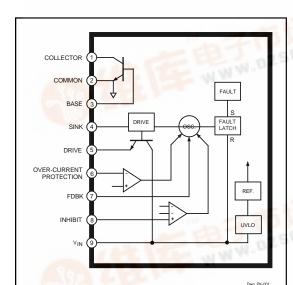
STR-S6707 THRU **STR-S6709**

)ata Sheet **28113‡**

OFF-LINE SWITCHING REGULATORS – WITH BIPOLAR SWITCHING TRANSISTOR



ARSOLLITE MAXIMUM RATINGS

| ABSOLUTE MAXIMUM RATINGS |
|--|
| Supply Voltage, V _{IN} 15 V |
| Output Voltage, V _{CEX} |
| V _{EBO} 7 V |
| Continuous Output Current, |
| I _C See Table |
| ms Single-Pulse Output Current, |
| I _{CM} See Table |
| Sink Current, I _S See Table |
| Orive Current, I _D |
| eedback Current, I _{FDBK} 20 mA |
| nhibit Voltage, V _{INH} 15 V |
| Over-Current Protection Voltage Range, |
| V _{OCP} ±3.5 V |
| nsulation Voltage,V _{WM(RMS)} 2000 V |
| Package Power Diss., Pp See Graph |
| Output Junction Temperature, T _J +150°C |
| nternal Frame Temperature, T _F +125°C |
| Operating Temperature Range, |
| T _A 20°C to +125°C |
| |

The STR-S6707, STR-S6708, and STR-S6709 are specifically designed to meet the requirement for increased integration and reliability in off-line quasi-resonant flyback converters. These devices incorporate the primary control and proportional drive circuit with a third-generation high-voltage bipolar switching transistor.

Crucial system parameters such as maximum ON time and OFF time are fixed during manufacture. Local control circuit decoupling and layout are optimized within each device.

Cycle-by-cycle current limiting, under-voltage lock-out with hysteresis, over-voltage protection, and thermal shutdown protect these devices during all normal and overload conditions. Over-voltage protection and thermal shutdown are latched after a short delay. A versatile triple-level inhibit circuit includes the OFF time synchronization required to establish quasi-resonant operation. The inhibit function has also been expanded to initiate operation in stand-by mode in which the power supply delivers a small fraction of the steady-state output power. The dual requirements of dielectric isolation and low transient thermal impedance and steady-state thermal resistance are satisfied in an overmolded single-in-line power package.

Proven in substantial volumes, these devices and their fixedfrequency counterparts represent a significant advance in off-line SMPS reliability growth and integration.

FEATURES

- Quasi-Resonant Operation for Low EMI and High Efficiency
- Output Power to 220 W
- Low-Power Output Standby Mode
- Pulse-by-Pulse Over-Current Protection
- Latched Over-Voltage and Thermal Protection
- Third-Generation Switching Transistor with Proportional Drive
- Maximum ON Time and Off Time Set During Manufacture
- Internal Under-Voltage Lockout with Hysteresis
- Over-Molded SIP with Integral Isolated Heat Spreader

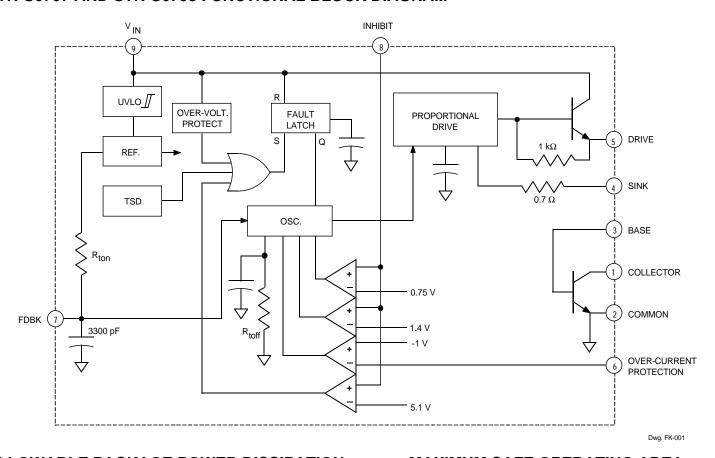
Always order by complete part number:

| Part Number | Max. Cont. Current, I _C | Peak Current, I _{CM} | Max. Sink Current, I _s | | |
|-------------|---------------------------------------|----------------------------------|--------------------------------------|--|--|
| STR-S6707 | 6 A | 12 A | 1.5 A | | |
| STR-S6708 | 7.5 A | 15 A | 1.5 A | | |
| STR-S6709 | 10 A | 20 A | 2 A | | |

Storage Temperature Range,

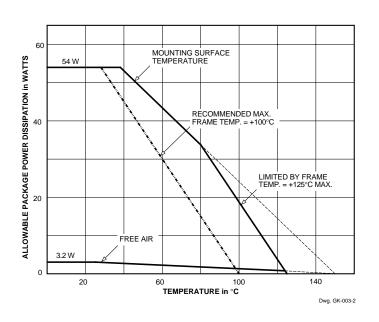
-40°C to +125°C

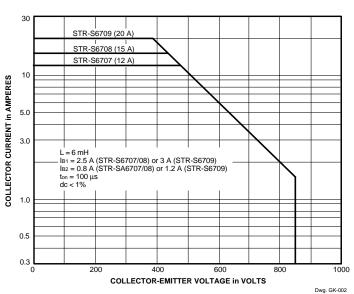
STR-S6707 AND STR-S6708 FUNCTIONAL BLOCK DIAGRAM



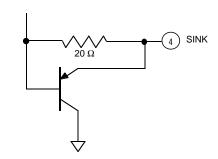
ALLOWABLE PACKAGE POWER DISSIPATION

MAXIMUM SAFE OPERATING AREA





STR-S6709 FUNCTIONAL BLOCK DIAGRAM AS ABOVE EXCEPT FOR SINK OUTPUT

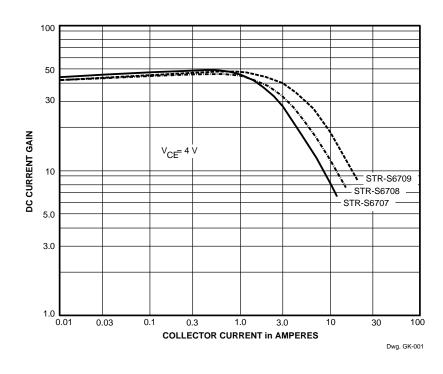


ELECTRICAL CHARACTERISTICS at $T_A = +25^{\circ}C$, $V_{IN} = 8.5$ V, voltage measurements are referenced to Common (pin 2) (unless otherwise noted).

| | | | | Limits | | | |
|---------------------------|-----------------------|---|------|--------|------|-------|--|
| Characteristic | Symbol | Test Conditions | Min. | Тур. | Max. | Units | |
| On-State Voltage | V _{INT} | Turn-on, increasing V _{IN} | 7.6 | 8.0 | 8.4 | V | |
| Under-Voltage Lockout | V _{INQ} | Turn-off, decreasing V _{IN} | 4.6 | 4.9 | 5.2 | V | |
| Over-Voltage Threshold | V _{OVP(th)} | | 9.2 | Đ | 10.7 | V | |
| Output Leakage Current | I _{CEX} | $V_{CE} = 850 \text{ V}, V_{BE} = -1.5 \text{ V}$ | Đ | Đ | 100 | μА | |
| Output Saturation Voltage | V _{CE(sat)} | STR-S6707, $I_C = 2 \text{ A}$, $I_B = 400 \text{ mA}$ | Ð | Đ | 400 | mV | |
| | | STR-S6708, I _C = 3 A, I _B = 600 mA | Ð | Đ | 400 | mV | |
| | | STR-S6709, I _C = 4 A, I _B = 800 mA | Đ | Đ | 400 | mV | |
| | V _{BE(sat)} | STR-S6707, I _C = 2 A, I _B = 400 mA | Ð | Đ | 1.5 | V | |
| | (337) | STR-S6708, I _C = 3 A, I _B = 600 mA | Ð | Đ | 1.5 | V | |
| | | STR-S6709, I _C = 4 A, I _B = 800 mA | Ð | Đ | 1.5 | V | |
| DC Current Gain | h _{FE} | $V_{CE} = 4 \text{ V}, I_{C} = 1 \text{ A}$ | 29 | Ð | 61 | Đ | |
| Maximum ON Time | t _{on} | | 33 | Đ | 41 | μs | |
| Minimum OFF Time | t _{off} | | 45 | Đ | 55 | μs | |
| Over-Current Threshold | V _{OCP(th)} | | -0.9 | -1.0 | -1.1 | V | |
| Feedback Threshold Volt. | V _{FDBK(th)} | | Đ | 650 | Đ | mV | |
| Inhibit Threshold Voltage | V _{INH(th)} | Oscillation stops | 0.65 | 0.75 | 0.85 | V | |
| | | Oscillation synchronized | Ð | 1.4 | 2.0 | V | |
| | | Oscillation stops (fault latch set) | 3.2 | 5.1 | 5.6 | V | |
| Latch Holding Current | I _{INH} | V _{IN} reduced from 10.7 V to 4 V | Ð | Đ | 500 | μΑ | |
| Latch Reset Voltage | V _Q | I _{IN} ≤ 100 μA, V _{IN} reduced from 10.7 V | 2.5 | 3.1 | Đ | V | |
| Supply Current | I _{IN(ON)} | Operating | 15 | Đ | 29 | mA | |
| | I _{IN(OFF)} | | Đ | Đ | 200 | μА | |
| Insulation RMS Voltage | V _{WM(RMS)} | All terminals simultaneous reference metal plate against backside | 2000 | Đ | Đ | V | |
| Thermal Shutdown | T _J | | 125 | 150 | Ð | °C | |
| Thermal Resistance | $R_{\theta JM}$ | Output junction to mounting surface | Ð | 2.0 | Ð | °C/W | |
| | | • | | | | | |

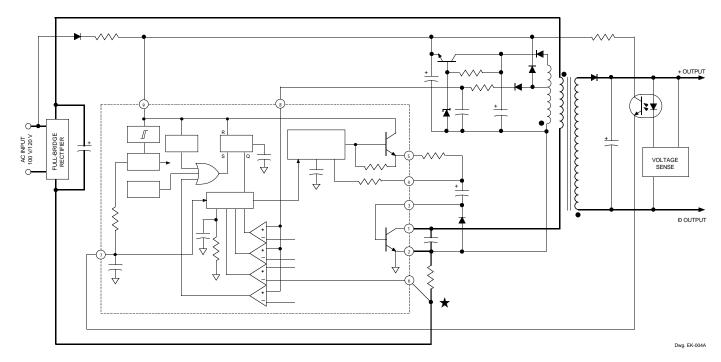
NOTES: Negative current is defined as coming out of (sourcing) the specified device terminal. Typical Data is for design information only.

TYPICAL CHARACTERISTICS



TYPICAL QUASI-RESONANT FLYBACK CONVERTER

WARNING: lethal potentials are present. See text.



APPLICATIONS INFORMATION

WARNING N These devices are designed to be operated at lethal voltages and energy levels. Circuit designs that embody these components must conform with applicable safety requirements. Precautions must be taken to prevent accidental contact with power-line potentials. Do not connect grounded test equipment.

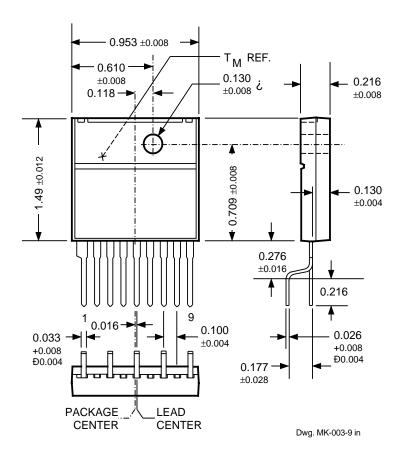
The use of an isolation transformer is recommended during circuit development and breadboarding.

Recommended mounting hardware torque:

4.34 Đ 5.79 lbf¥ft (6 Đ 8 kg¥cm or 0.588 Đ 0.784 Nm).

Recommended metal-oxide-filled, alkyl-degenerated oil base, silicone grease: Dow Corning 340, or equivalent

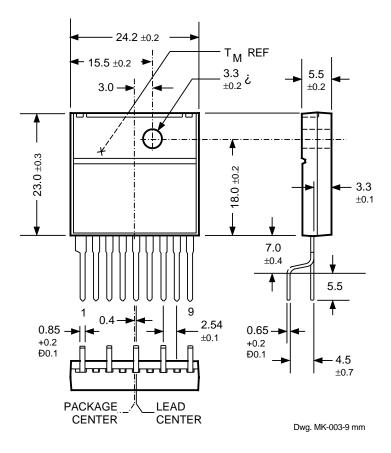
Dimensions in Inches (for reference only)



NOTE: Exact body and lead configuration at vendorÕs option within limits shown.

Dimensions in Millimeters

(controlling dimensions)



NOTE: Exact body and lead configuration at vendorÕs option within limits shown.

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