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LC7535



3010A

SANYO SEMICONDUCTOR CORP

CMOS LSI

High-voltage, Two-channel Electronic Attenuator

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OVERVIEW

The LC7535 is a two-channel electronic attenuator for volume, balance and loudness controls in stereo audio applications.

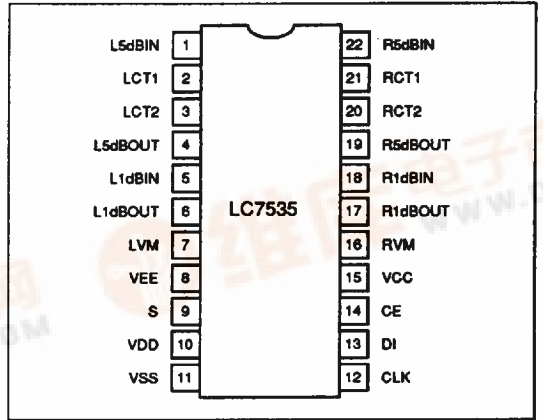
The LC7535 reads data from a three-line serial bus to control attenuation in 1 dB steps over an 80 dB range, up to a maximum of 98 dB. It features a center tap at 20 dB attenuation for a loudness control using a minimum of external components. A device select pin allows two devices to be connected to the serial bus.

The LC7535 operates from 5 V and ± 16 V supplies and is available in 22-pin DIPs.

FEATURES

- Three-line serial control
- Two device select
- Tap at 20 dB attenuation for a loudness control
- 80 dB attenuation range adjustable in 1 dB steps
- 98 dB maximum attenuation
- 75 dB crosstalk rejection
- 0.011% total harmonic distortion
- 47 k Ω input impedance
- 5 V and ± 16 V supplies
- 22-pin DIP

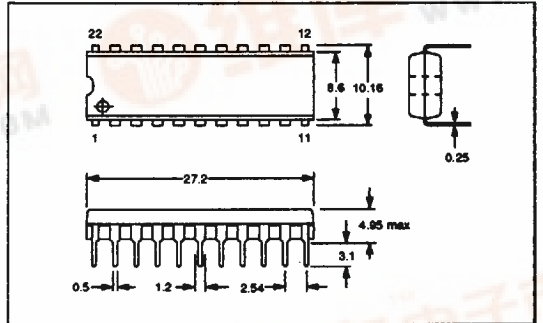
PINOUT



PACKAGE DIMENSIONS

Unit: mm

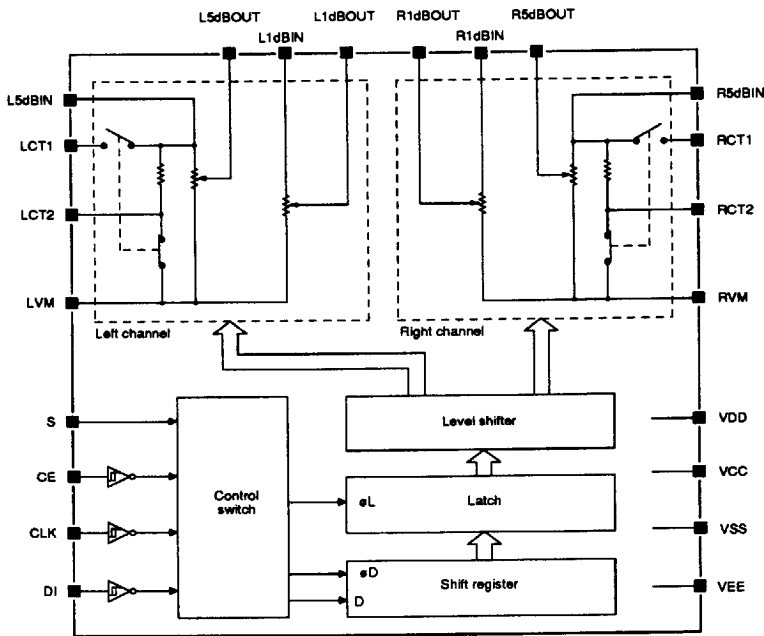
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BLOCK DIAGRAM



PIN DESCRIPTION

Number	Name	Description
1	L5dBIN	Left-channel 5 dB attenuation step input. Low-impedance drive. 75 kΩ total resistance
2	LCT1	Left-channel loudness compensation inputs
3	LCT2	
4	L5dBOUT	Left-channel 5 dB attenuation step output. Approximately 1 MΩ load resistance
5	L1dBIN	Left-channel 1 dB attenuation step input. Low-impedance drive
6	L1dBOUT	Left-channel 1 dB attenuation step output. 47 kΩ to 1 MΩ load resistance
7	LVM	Left-channel volume control common. Normally connected to ground
8	VEE	-16 V supply
9	S	Address select input
10	VDD	16 V supply
11	VSS	Ground
12	CLK	Clock input
13	DI	Serial data input
14	CE	Chip enable input
15	VCC	5 V supply
16	RVM	Right-channel volume control common. Normally connected to ground
17	R1dBOUT	Right-channel 1 dB attenuation step output. 47 kΩ to 1 MΩ load resistance
18	R1dBIN	Right-channel 1 dB attenuation step input. Low-impedance drive
19	R5dBOUT	Right-channel 5 dB attenuation step output. Approximately 1 MΩ load resistance

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Number	Name	Description
20	RCT2	Right-channel loudness compensation inputs
21	RCT1	
22	RSdBIN	Right-channel 5 dB attenuation step input. Low-impedance drive. 75 kΩ total resistance

SPECIFICATIONS

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Absolute Maximum Ratings

Parameter	Symbol	Rating	Unit
Supply voltage ranges	V _{DD}	0 to 18	V
	V _{EE}	-18 to 0	
	V _{CC}	0 to 7	
CLK, DI and CE input voltage range	V _{I1}	0 to V _{CC} + 0.3	V
Analog input voltage range	V _{I2}	V _{EE} - 0.3 to V _{DD} + 0.3	V
Select pin input voltage range	V _{I3}	V _{CC} - 0.3 to V _{DD} + 0.3	V
Power dissipation	P _D	250	mW
Operating temperature range	T _{OPP}	-30 to 75	°C
Storage temperature range	T _{STG}	-40 to 125	°C

Recommended Operating Conditions

T_a = 25 °C, V_{SS} = 0 V

Parameter	Symbol	Rating	Unit
Analog supply voltage ranges	V _{DD}	V _{CC} + 4.5 to 16.0	V
	V _{EE}	-16 to 0	V
Logic supply voltage	V _{CC}	5	V
Logic supply voltage range	V _{CC}	4.5 to 5.5	V

Electrical Characteristics

T_a = 25 °C, V_{SS} = 0 V

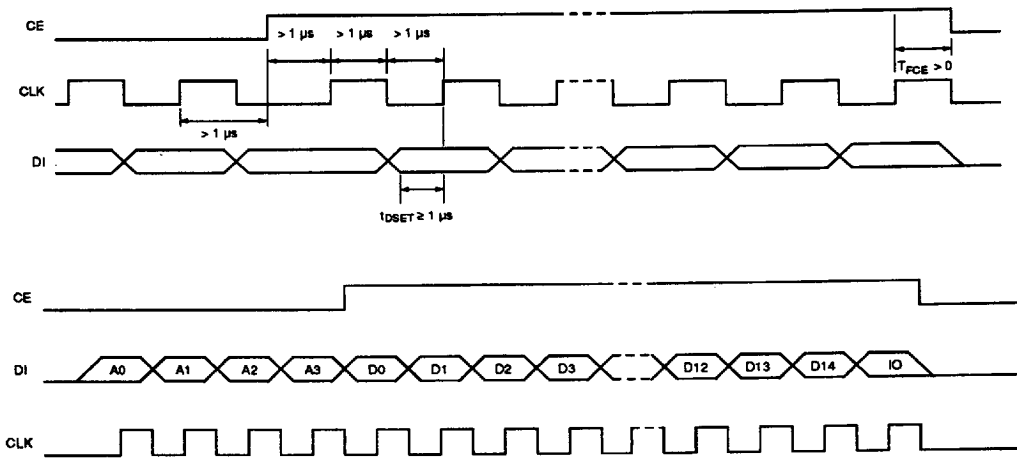
Parameter	Symbol	Condition	Rating			Unit
			min	typ	max	
Supply currents	I _{DD}		-	-	1	mA
	I _{CC}		-	-	1	
Serial bus LOW-level input voltage	V _{IL1}		V _{SS}	-	0.2V _{CC}	V
Serial bus HIGH-level input voltage	V _{IH1}		0.8V _{CC}	-	V _{CC}	V
Select pin LOW-level input voltage	V _{IL2}		V _{CC}	-	$0.2 \times (V_{DD} - V_{CC}) + V_{CC}$	V
Select pin HIGH-level input voltage	V _{IH2}		$0.8 \times (V_{DD} - V_{CC}) + V_{CC}$	-	V _{DD}	V
Input voltage	V _{IN}		V _{EE}	-	V _{DD}	V _{P-P}
Input pulsewidth	t _{LOW}		1	-	-	μs

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Parameter	Symbol	Condition	Rating			Unit
			min	typ	max	
Setup time	t_{setup}		1	-	-	μs
Hold time	t_{hold}		1	-	-	μs
Operating frequency	f_{opp}		-	-	500	kHz
Output leakage current	I_{off}	Analog switches OFF	-10	-	10	μA
Total harmonic distortion	THD	$V_{IN} = 1 V, f = 1 kHz,$ $V_{DD} - V_{EE} = 32 V,$ $V_R = V_R (max)$	-	0.003	-	%
		$V_{IN} = 1 V, f = 1 kHz,$ $V_{DD} - V_{EE} = 32 V,$ $V_R = V_R (max)$	-	0.011	-	
Crosstalk rejection	C_T	$f = 20 kHz, V_{IN} = 1 V$	60	75	-	dB
Maximum attenuation	V_0	$f = 20 kHz, V_{IN} = 1 V$	-	98	-	dB

Timing

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FUNCTIONAL DESCRIPTION

Data Control

Data is clocked into a 20-bit shift register. When 20 bits have been received, the data is latched and then passed to a level shifter.

Data Format

The 20-bit data word comprises a 4-bit address code, two 4-bit 5 dB attenuation step selectors, two 3-bit 1 dB attenuation step selectors and a loudness control ON/OFF bit as shown in figure 1.

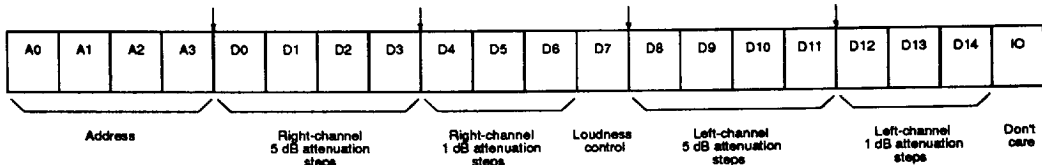


Figure 1. Data format

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The voltage on the select input determines the address of the device as shown in table 1.

Table 1. Address codes

Select pin	A0	A1	A2	A3
VDD	1	0	0	1
VCC	0	0	0	1

Data bits D0 to D3 select the right-channel attenuation in 5 dB steps as shown in table 2. Similarly, bits D8 to D11 select the left-channel attenuation in 5 dB steps.

Table 2. Right-channel 5 dB steps

Attenuation (dB)	D0	D1	D2	D3
0	1	1	1	1
5	0	1	1	1
10	1	0	1	1
15	0	0	1	1
20	1	1	0	1
25	0	1	0	1
30	1	0	0	1
35	0	0	0	1
40	1	1	1	0
45	0	1	1	0
50	1	0	1	0
55	0	0	1	0
60	1	1	0	0
65	0	1	0	0
70	1	0	0	0
75	0	0	0	0

Data bits D4 to D6 select the right-channel attenuation in 1 dB steps as shown in table 3. Similarly, bits D12 to D14 select the left-channel attenuation in 1 dB steps.

Table 3. Right-channel 1 dB steps

Attenuation (dB)	D4	D5	D6
0	0	1	1
Dummy. See note.	1	0	0
1	1	0	1
2	0	0	1
3	1	1	0
4	0	1	0
∞	0	0	0

Note

Dummy data is inserted to give 79 dB to infinity attenuation during switchover.

Data bit D7 selects loudness control. When D7 is 1, loudness control is ON, and when 0, loudness control is OFF.

Audio Signal

The right-channel audio input signal is input on R5dBIN (5 dB attenuation steps). The output, R5dBOUT, is fed back to R1dBIN (1 dB attenuation steps). The right-channel audio output is on R1dBOUT. The left-channel audio signal flow is identical.

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TYPICAL APPLICATION

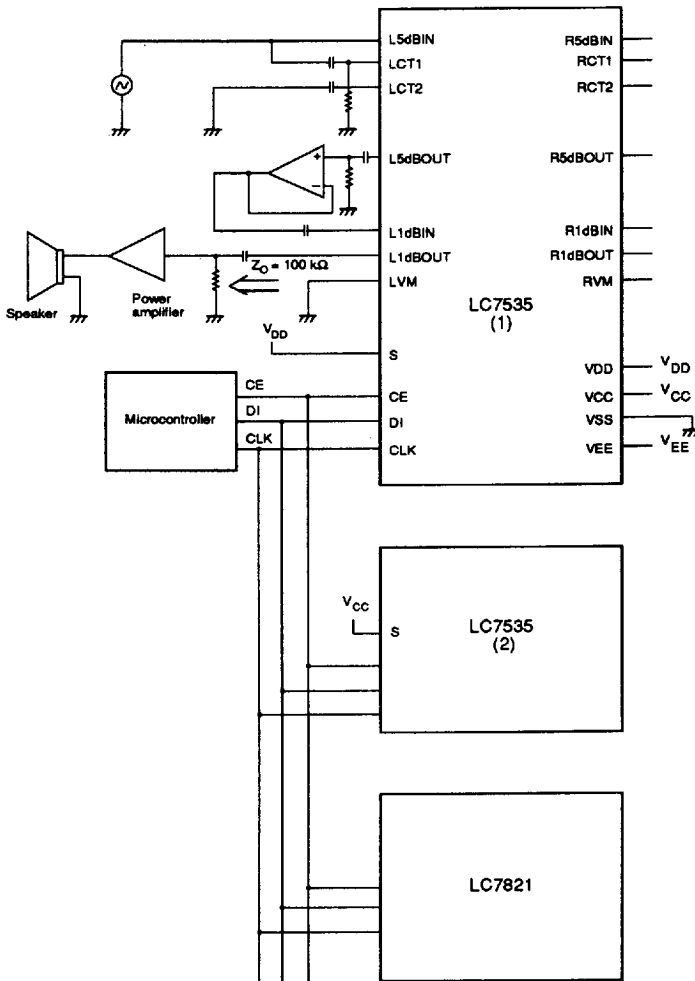


Figure 2. Typical application