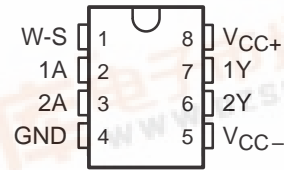


DUAL LINE DRIVER WITH ADJUSTABLE SLEW RATE

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- Meets or Exceeds the Requirements of ANSI Standards EIA/TIA-423-B and -232-E and ITU Recommendations V.10 and V.28
- Output Slew Rate Control
- Output Short-Circuit-Current Limiting
- Wide Supply Voltage Range
- 8-Pin Package
- Designed to Be Interchangeable With National DS9636A

D OR P PACKAGE
(TOP VIEW)

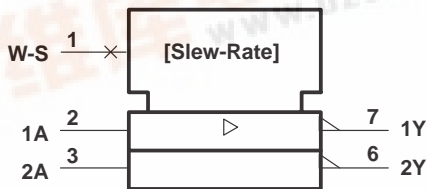


description

The uA9636AC is a dual, single-ended line driver designed to meet ANSI Standards EIA/TIA-423-B and EIA/TIA-232-E and ITU Recommendations V.10 and V.28. The slew rates of both amplifiers are controlled by a single external resistor, $R_{(WS)}$, connected between the wave-shape-control (W-S) terminal and GND. Output current limiting is provided. Inputs are compatible with TTL and CMOS and are diode protected against negative transients. This device operates from ± 12 V and is supplied in an 8-pin package.

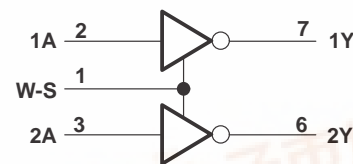
The uA9636AC is characterized for operation from 0°C to 70°C.

logic symbol†



† This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

logic diagram



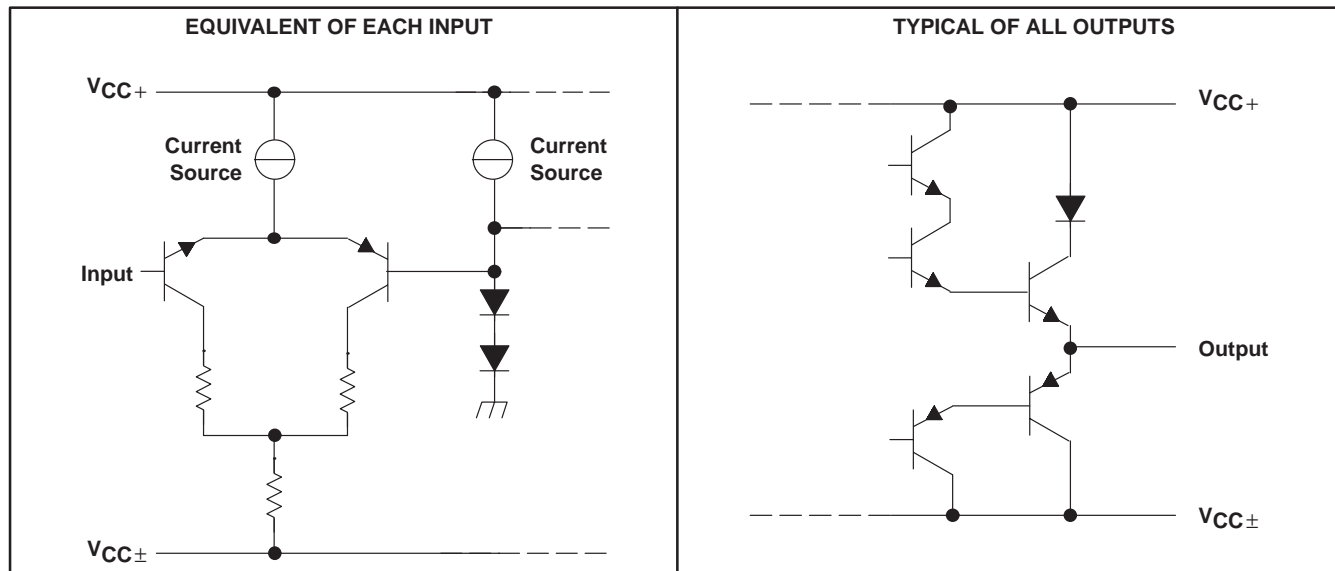
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uA9636AC

DUAL LINE DRIVER WITH ADJUSTABLE SLEW RATE

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schematics of inputs and outputs



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)†

Positive supply voltage range, V_{CC+} (see Note 1)	V_{CC-} to 15 V
Negative supply voltage range, V_{CC-}	0.5 V to -15 V
Output voltage, V_O	± 15 V
Output current, I_O	± 150 mA
Continuous total power dissipation	See Dissipation Rating Table
Operating free-air temperature range, T_A	0°C to 70°C
Storage temperature range, T_{stg}	-65°C to 150°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260°C

† Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTE 1: All voltage values are with respect to the network ground terminal.

DISSIPATION RATING TABLE

PACKAGE	$T_A \leq 25^\circ\text{C}$ POWER RATING	DERATING FACTOR ABOVE $T_A = 25^\circ\text{C}$	$T_A = 70^\circ\text{C}$ POWER RATING
D	725 mW	5.8 mW/°C	464 mW
P	1000 mW	8.0 mW/°C	640 mW

recommended operating conditions

	MIN	NOM	MAX	UNIT
Positive supply voltage, V_{CC+}	10.8	12	13.2	V
Negative supply voltage, V_{CC-}	-10.8	-12	-13.2	V
High-level input voltage, V_{IH}	2			V
Low-level input voltage, V_{IL}			0.8	V
Wave-shaping resistor, $R_{(WS)}$	10	1000		k Ω
Operating free-air temperature, T_A	0		70	°C

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electrical characteristics over recommended ranges of free-air temperature, supply voltage, and wave-shaping resistance (unless otherwise noted)

PARAMETER		TEST CONDITIONS		MIN	TYP†	MAX	UNIT
V_{IK}	Input clamp voltage	$I_I = -15 \text{ mA}$		-1.1	-1.5		V
V_{OH}	High-level output voltage	$V_I = 0.8 \text{ V}$	$R_L = \infty$	5	5.6	6	V
			$R_L = 3 \text{ k}\Omega$ to GND	5	5.6	6	
			$R_L = 450 \Omega$ to GND	4	5.4	6	
V_{OL}	Low-level output voltage	$V_I = 2 \text{ V}$	$R_L = \infty$	-6‡	-5.7	-5	V
			$R_L = 3 \text{ k}\Omega$ to GND	-6‡	-5.6	-5	
			$R_L = 450 \Omega$ to GND	-6‡	-5.4	-4	
I_{IH}	High-level input current	$V_I = 2.4 \text{ V}$				10	μA
		$V_I = 5.5 \text{ V}$				100	
I_{IL}	Low-level input current	$V_I = 0.4 \text{ V}$			-20	-80	μA
I_O	Output current (power off)	$V_{CC\pm} = 0,$	$V_O = \pm 6 \text{ V}$			± 100	μA
I_{OS}	Short-circuit output current§	$V_I = 2 \text{ V}$		15	25	150	mA
		$V_I = 0$		-15	-40	-150	
r_O	Output resistance	$R_L = 450 \Omega$			25	50	Ω
I_{CC+}	Positive supply current	$V_{CC} = \pm 12 \text{ V},$ $R(WS) = 100 \text{ k}\Omega,$	$V_I = 0,$ Output open		13	18	mA
I_{CC-}	Negative supply current	$V_{CC} = \pm 12 \text{ V},$ $R(WS) = 100 \text{ k}\Omega,$	$V_I = 0,$ Output open		-13	-18	mA

† All typical values are at $V_{CC} = \pm 12 \text{ V}, T_A = 25^\circ\text{C}$.

‡ The algebraic convention, in which the less-positive (more-negative) limit is designated as minimum, is used in this data sheet for logic voltage levels, e.g., when -5 V is the maximum, the minimum is a more-negative voltage.

§ Not more than one output should be shorted to ground at a time.

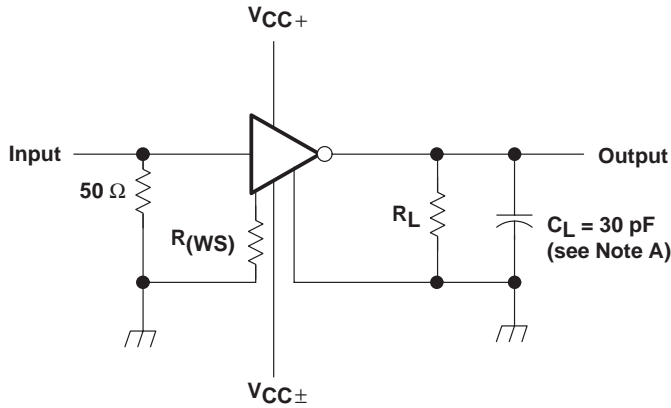
switching characteristics, $V_{CC\pm} = \pm 12 \text{ V}, T_A = 25^\circ\text{C}$ (see Figure 1)

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT	
t_{TLH}	Transition time, low- to high-level output	$R_L = 450 \text{ k}\Omega,$	$C_L = 30 \text{ pF}$	$R(WS) = 10 \text{ k}\Omega$	0.8	1.1	1.4	μs
				$R(WS) = 100 \text{ k}\Omega$	8	11	14	
				$R(WS) = 500 \text{ k}\Omega$	40	55	70	
				$R(WS) = 1 \text{ M}\Omega$	80	110	140	
t_{THL}	Transition time, high- to low-level output	$R_L = 450 \text{ k}\Omega,$	$C_L = 30 \text{ pF}$	$R(WS) = 10 \text{ k}\Omega$	0.8	1.1	1.4	μs
				$R(WS) = 100 \text{ k}\Omega$	8	11	14	
				$R(WS) = 500 \text{ k}\Omega$	40	55	70	
				$R(WS) = 1 \text{ M}\Omega$	80	110	140	

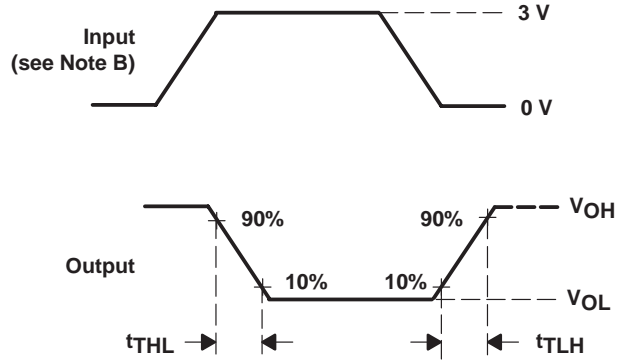
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PARAMETER MEASUREMENT INFORMATION



TEST CIRCUIT



VOLTAGE WAVEFORMS

- NOTES: A. C_L includes probe and jig capacitance.
 B. The input pulse is supplied by a generator having the following characteristics: $t_r \leq 10$ ns, $t_f \leq 10$ ns, $Z_0 = 50 \Omega$, PRR ≤ 1 kHz, duty cycle = 50%.

Figure 1. Test Circuit and Voltage Waveforms

TYPICAL CHARACTERISTICS

OUTPUT VOLTAGE
vs
INPUT VOLTAGE

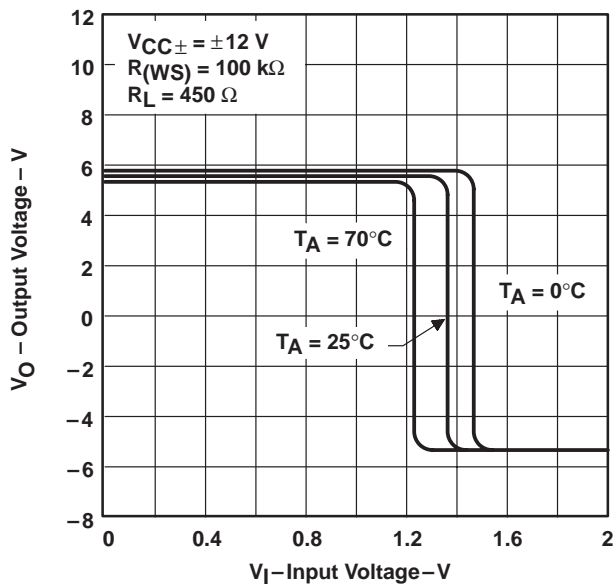


Figure 2

INPUT CURRENT
vs
INPUT VOLTAGE

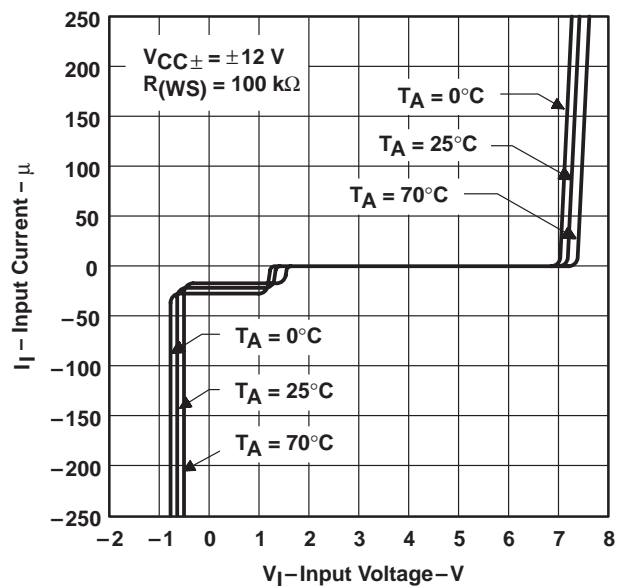
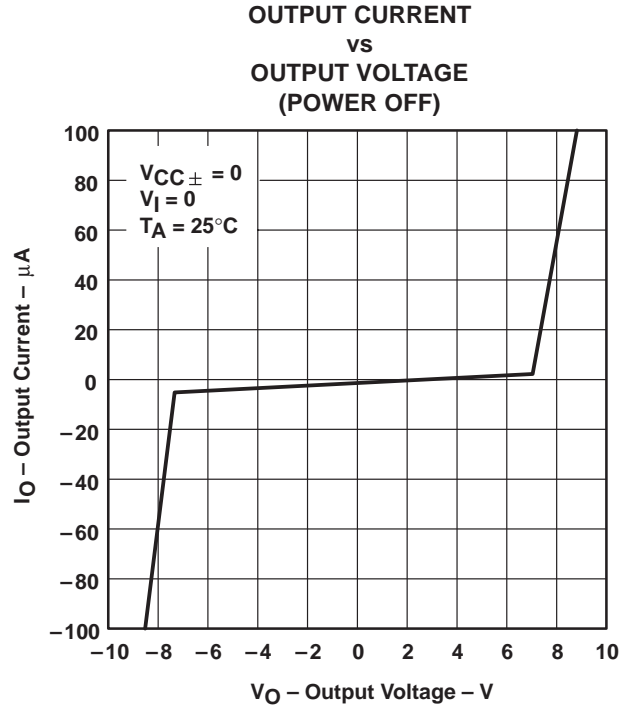
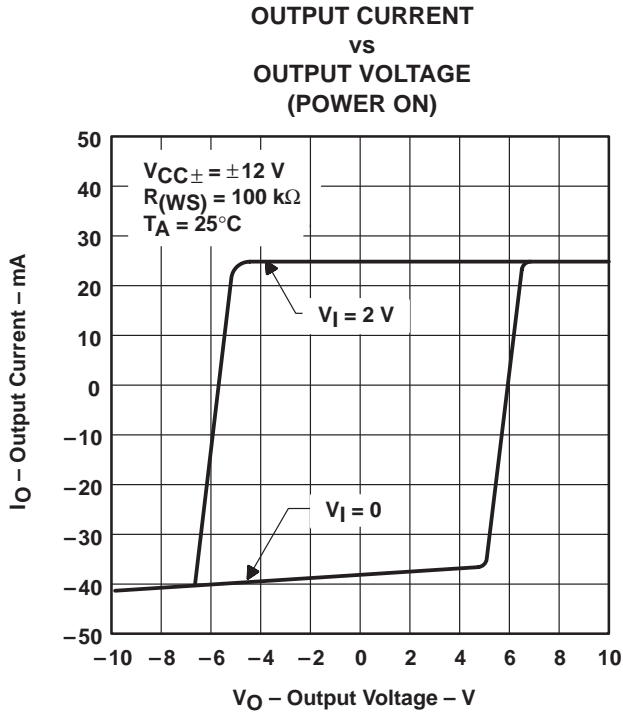


Figure 3

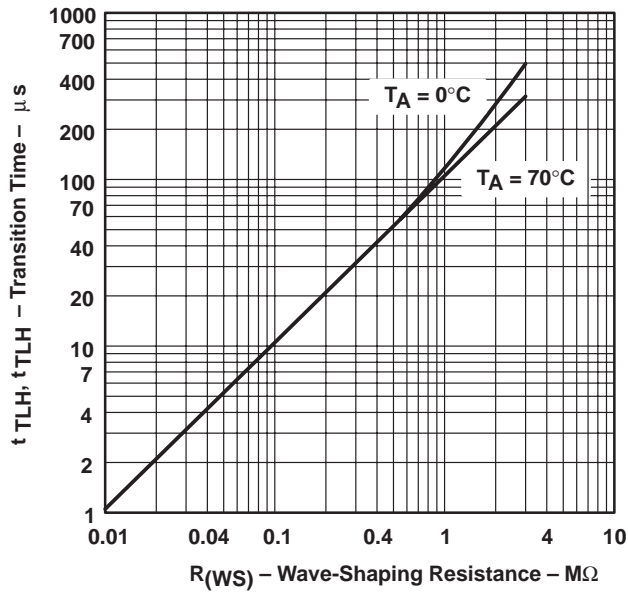
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TYPICAL CHARACTERISTICS



TRANSITION TIME vs WAVE-SHAPING RESISTANCE



uA9636AC DUAL LINE DRIVER WITH ADJUSTABLE SLEW RATE

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APPLICATION INFORMATION

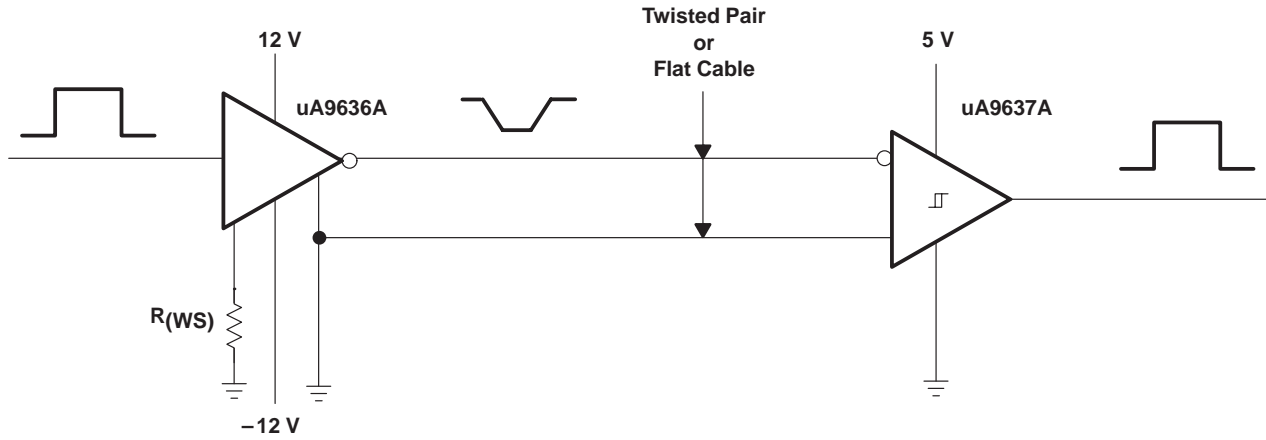


Figure 7. EIA/TIA-423-B System Application

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