1

HYBRID POWER ICS (Refer to individual specification for measurement requirements of electrical characteristics)

MHYBRID IC REGULATORS (SWITCHING TYPE • 2 PACK TYPE)

	Combination line-up	
Control section	Power section (ST	R7000, 7100series)
(SI-8020 series)	lout=6A	lout=12A
SI-8020 (Vout=5V)	STR7001	STR7101
SI-8021 (Vout=12V)	STR7002	STR7102
SI-8022 (Vout = 15V)	STR7002	STR7102
SI-8023 (Vout=24V)	STR7003	STR7103

	Pow	er section	Maximum rat	ings (Ta=2	5°C)	-
Parts No.	Power transistor with- stand voltage V4-1(V)	Collector current Ic(A)	Allowable loss (Tc=25°C) Pc (W)	Operating temperature (Tc) Top (°C)	Storage tempe- rature Tstg (°C)	Fig. No.
STR7001	Wo be			1		
7002	60	6(Peak7.5)	100	-30~+125	-30~+125	, 1
7003						
STR7101					:	
7102] 60.	12(Peak15)	125	-30~+125	-30~+125	1
7103						. :

	Control	section Maxir	num ratings (Ta	a=25°C)	Fig
Parts No.	DC input voltage Vin (V)	Allowable loss Po (W)	Operating temperature Top (*C)	Storage temperature Tstg (°C)	J. No.
SI-8020			T- 1	7.5	
8021	55	1	-20~+85	-20~+100	9
8022	33		-20~+63	-20~+100	2
8023					

	1.	Electric	cal chara	cteristics (T	a=25°C)	
Combined parts No.	DC input voltage range V _{IN} (V)	Output voltage setting Vo (V)	Output curreut lo (A)	Output voltage regulation (against input voltage). △VLINE (mV)max	Output voltage regulation (against output current) \(\Delta VLOAD \) (mV)max	Efficiency 7 (%)typ
SI-8020 —STR7001	11 ~40	5.1±0.1		80	30	72
SI-8021 —STR7002	18~50	12±0.2	6	120	40	84
SI-8022 —STR7002	21~50	15±0.2	ů	150	40	86
SI-8023 —STR7003	30~50	24±0.3	MAN.	200	50	90
SI-8020 —STR7101	11 ~40	5.1±0.1		80	30	70
SI-8021 STR7102	18~50	12±0.2	12	120	40	82
SI-8022 —STR7102	21~50	15±0.2	12	150	40	84
SI-8023 —STR7103	30~50	24±0.3]	200	50	87

MHYBRID IC REGULATORS (SWITCHING TYPE WITH COIL)

	Maximum	ratings (Ta	=25°C)		inte	egrated chara	acteristies (Ta=25°C)		J	
Parts No.	DC input	Operating	Storage	DC input	Output	Output	Efficiency	Oscillation	Switching	Ę.	Remarps
rans No.	voltage	temperature	temperature	voltage	voltage	current		frequency	ripple	공	riemarps
	V _{IN} (V)	Top (°C)	Tstg (°C)	Vin (V)	Vout (V)	lout (A)	η (%)typ	f(kHz)	△Vr (mVp-p)		
SI -8201L				10~40	5.0±0.1	0.02~0.4	73		60max		
-8202L	45	-10~+65		11~40	6.0±0.1	0.02~0.35	74		30typ	3	
-8203L	40	-10.0+65	THE REST	16~40	12.0 ± 0.2	0.02. 0.35	79	25min	100max] 3	
-8204L			All Control	10~40	5.2±0.1	0.02~0.4	73		30typ		
-8211L	60		-25~+85	15~55	5.0±0.1	0.02~0.3	63		60max	4	
-8301L	45	$-20 \sim +85$	-25~ +65	8~40	5.1 ± 0.1	1max	73	25typ	45typ	5	
-8811L	35	-10~+70		12~30	5.0±0.25	0.05~0.45	72	50typ	50typ		
-0011L	33	-10~+70	1.5	12~30	-5.0 ± 0.25	0~-0.05	1 12	эогур	50typ	6	Double
-8911L	60	-10~+60		24~55	5.0 ± 0.25	0.02~0.3	65	Setup	50typ] •	outputs type
-0911	00	-10,0+00	1	24-055	-5.0±0.25	0~-0.1	υĐ	68typ	50typ]	



MHYBRID POWER IC REGULATORS (SWITCHING TYPE)

		Maxin	num ratin	igs (Ta=25°0	C) \			Electrical cha	racteristics	(Ta=25°C	S)] ,,	
David No.	Input		Power -	Operating	Storage	Input	Output	Output voltage regulation	Output voltage regulation	Efficiency	Oscillation frequency	Ripple	Ġ,	Remarks
Parts No.	voltage	current	consumption (Tc=25°C)	temperature	temperature	voltage	voltage	(against input	(against output	7.	f(kHz)	damping ratio	8	Homano
	VIN(V)	lout(A)	Pd(W)	Top(°C)	Tstg(°C)	VIN(V)	Vout(V)	(mV)max	(mV)max	(%)typ	typ	(dB)typ		
STR2005						11~40	5.1±0.1	50		72			· .	
2012	45					18~45	12±0.2]					7	
2013	40	2.0	75	00	-20~+125	19~45	13±0.2	60	100	85	25	45	1	
2015	1	2.0	′°	-20~+100	-20~+125	21~45	15±0.2		100	00		73		
2024	50					30~50	24±0.3	80						
20005	45		55			8~40	5.1±0.1	50		72	30		7	<u> </u>

MHYBRID POWER IC REGULATORS (SWITCHING TYPE)

					Maximum rati	ngs (Ta=25°	C)			
Parts No.	Voltage between drain and source Voss(V)	Drain current (single pulse)	Voltage between gate and drain	Avalanche energy resistance	Out put power Pout(W)	Control power source voltage	Drive terminal source current Isource(A)	Drive terminal sink current	Operating temperature Top(*C)	Channel section temperature Tch('C)
	VD55(V)	I⊳(A)	V _{GS} (V)	Eas(mJ)	FOUI(VV)	V _{IN} (V)	ISOURCE(A)	ISINK(A)	TOP(O)	1011(0)
STR-S6401 -S6401F	500	±40	±20	500	250	35	0.7	1.5	−20 ∼ +125	+150

							Electi	ical characteri	stics	(Ta=2	5°C)									
Thermal	5	Starting]	s	toppin	g	Circuit current	Circuit current	Ö	scillation	on	· M	aximu	m.		termir			termir	ıal
resistance	v	oltage		l v	oltage	_	during operation	non-operation	fre	equenc	зу	0	N time	i		eshold Itage			tflow rent	
θch-F	١ ١	Укиоуи\)	٠v	IN(OFF)(\	<i>(</i>) .	lin(on)(mA)	lin(off)(µA)	fe	osc(kH	z)	-	Ton(us	٠		Vps(mV)		ios(µA))
(°C/W)	min	typ	max	min	typ	max	typ	max	min	typ	max	min	typ	max	min	typ	max	min	typ	max.
0.7	44.4	40	47.0	0.4	0.4	10.4	00	500	93	100	107	5.1	5.7	6.5	160	200	240	250	400	550
0.7	14.4	16	17.6	8.4	9.4	10.4	23	500	93	100	107	3.8	4.5	5,2	100	200	240	200	400	550

					Electr	ical cha	racteristic (Ta	a=25°C)				
Amp terminal outflow current IAmp. (mA)	SS terminal threshold voltage Vss(V)	SS terminal outflow current lss(µA)	Com to outfloy current lcom	٧	resisi	ON tance ον)(Ω)	Forward conduction admittance yfs (S)	Source grounding input capacity Clss(pF)	Switching time tox(ns)	Switching time torr(ns)	Fig. No.	Remarks
typ	max	typ	typ	max	typ	max	typ	typ	typ	typ		
1.8	0.4	100	1.0	1.5	0.5	0.6	9.2	1800	60	140	8	Built-in MOSFET

HYBRID POWER IC REGULATORS (DROPPER TYPE)

		Maximum	ratings ((Ta=25°C)			Elect	rical charact	eristics (Ta=	25°C)		J	
Parts No.	Input	Output	Allowable	Operating	Storage	Input	Output	voltage	Input-output	Temperature	Ripple	Ġ	Remarks
Parts No.	voltage	current	loss (Tc=25°C)	temperature	temperature	voltage	Vo	(V)	differential voltage	coefficient	damping ratio	동	Tientains
	VIN(V)	lo(A)	PD(W)	Top(°C)	Tstg(°C)	VIN(V)	3000C*	3000CA	V _{DIF} (V)max	(mV/°C)typ	(dB)typ		
SI-3050C						6~30	5±0.2	5±0.1		±0.5		.	Low loss type
-3090C	35			-30~	−40 ~	10~30	9±0,36	9±0.18		±1.0		1	Remote sens- ing possible
-3120C	30	1,5	18	+100	+125	13~30	12±0.48	12±0.24	1.0	±1.5	- 54	9	Built-in over- current, over-
-3150C		1		T-100	T 120	16~30	15±0.6	15±0.3]	±1.5			voltage, over- heating protec-
-3240C	45					25~40	24±0.96	24±0.48]	±2.5			tions

* "A" may be stamped at the right of marking.

· · · · · · · · · · · · · · · · · · ·		Maxin	num ratin	gs (Ta=25°0	D)		Electi	rical characte	eristics (Ta=	25°C)		70	
Parts No.	Input	Output	Power	Operating	Storage	Output	Minimum input/output	Temperature	Output voltage regulation	Output voltage regulation	Ripple	ģ	Remarks
raits No.	voltage	current	(Tc=25°C)	temperature	temperature	voltage	differential voltage	coefficient	(against input voltage)	(against output current)	damping ratio	8	Lietilaiva
	VIN(V)	lout(A)	`Pd(W)	Top(°C)	Tstg(°C)	Vout(V)	V _{DIF} (V)max	(mV/*C)typ	(mV)typ	(mV)typ	(dB)typ	1	
SI-3052V	25			-20~+100		5±0.1		±0.5	10	40			Low loss type
-3122V	30	2.0	50	(Tc)	-30~+125	12±0.2	1.0	±1.5	20	80	54	10	Built-in over- current protec-
-3152V	30			(16)		15±0.2		1.0	20	00			tion
SI -3052P						5±0.1		±0.5	2	40			
-3122P	45	20	50	-20~+80	-30~+125	12±0.2	3.0	±1.5	10	80	60	10	Built-in overcurrent
-3152P	1 40	20	טט	-20~+80	-30~+125	15±0.2	3.0	±1.0	10	80	60	10	protection
-3242P	1					24±0.2		±2.5	25	120			
STR9005	25		1	-20~+100		5±0.1		±0.5	10	40			Low loss type Built-in
9012	30	4.0	75		-30~+125	12±0.2	1.0	±1.5	30	80	54	1.	overcurrent protec- tion Output ON/OFF control Output voltage
9015	30	1		(Tc)		15±0.2		41.5	50	100			fine tuning possible

SANKEN ELECTRIC CO LTD

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T-58-11-31

VOLTAGE DOUBLER, BRIDGE RECTIFIER AUTOMATIC SWITCHING ICS

		Maximu	m ratings (Ta	=25°C)		I	Electrical o	haracteristi	ics (Ta≕25°C	;)	
Parts No.	Peak repeated OFF voltage	Effective ON current	Surge ON current	Operating temperature	Storage temperature	Voltage doubler startup voltage	Settling voltage	switch	OFF voltage	ON voltage	ig N
	V _{DRM} (V)	I _T (RMS)(A)	I _{TSM} (A)	Tor(°C)	Tstg(°C)	Vs(V(AC))	Vc ₁ (V)	Vc2(V(AC))	IDRM(μA)	Vth(V)	5
STR80145A		5.0	50	-10~	−30~		196±5	145			
81145A	500	10.0	100	+100(Tc)	+125	80max	19073	143	100max	1.8max	111
81159A]	10.0	100	T100(10)	T120		215±5	159			50.7

STEPPING MOTOR DRIVE ICs

Parts No.	Maximum ratings (T _a =25°C)					Electrical characteristics (Ta=25°C)							
	Power Source voltage	Output current	Junction temperature	Operating temperature Toe(°C)	Storage temperature Tstg(°C)	Power source Vcc(V)		Output current	Exciting signal input current	Vcc2 input current	Fig. No	Remarks	
	Vcc(V)	l₀(A)	T _f (°C)			min	typ	max	lo(A)max	I _{(mA)max}	lcc2(mA)max	٦	
SI-7115B	40	1.7	+125	−20 ~ +80	−30~ +100	20	24	30	1,5	5		12	For unipolar
-7300A	48(Vccı) 7(Vcc2)	1.7				15(Vcc1) 4.5(Vcc2)	30(Vcc1) 5(Vcc2)	42(Vcc1) 5.5(Vcc2)	1.5	10	45	13	
-7330A	42(Vcc1) 7(Vcc2)	3.2				15(Vcc1) 4.5(Vcc2)	30(Vcc1) 5(Vcc2)	35(Vcc1) 5.5(Vcc2)	3.0	100	45	14	
-7500A	40(Vcc1) 8(Vcc2)	. 1.2				17(Vcc1) 4.5(Vcc2)	24(Vcc1) 5(Vcc2)	30(Vcc1) 5.5(Vcc2)	1.0	0.21	15	14	
-7200M	50(Vcc1) 10(Vcc2)	1.2				15(Vcc1) 4.5(Vcc2)	30(Vcc1) 5(Vcc2)	40(Vcc1) 5.5(Vcc2)	1.0	1.6	45 1	13	For bipolar
-7230M	50(Vcc1) 7(Vcc2)	3.2				15(Vcc1) 4.5(Vcc2)	30(Vcc1) 5(Vcc2)	45(Vcc1) 5.5(Vcc2)	3.0	1.6	150	14	drive

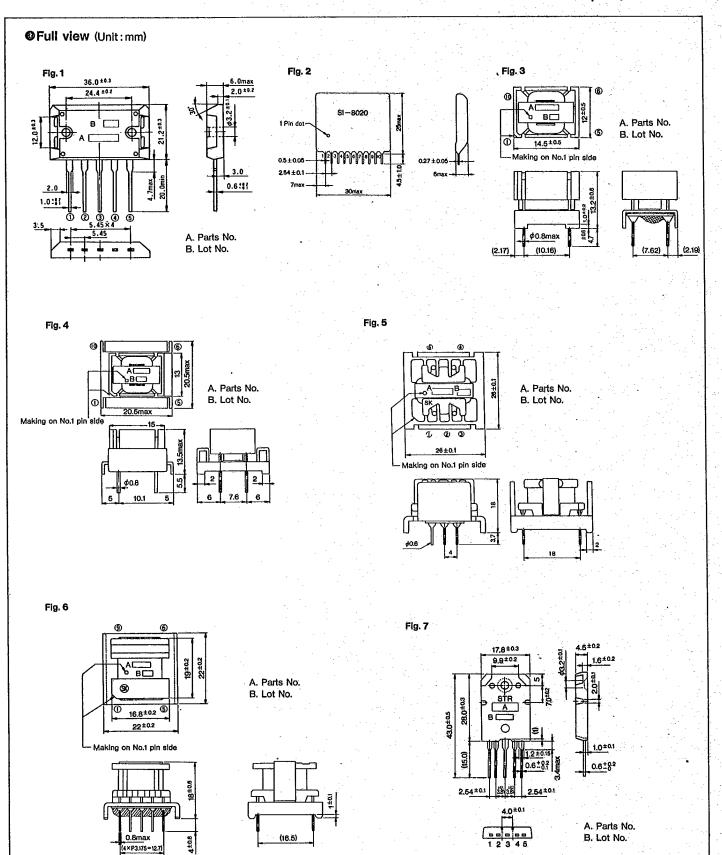


Fig. 8

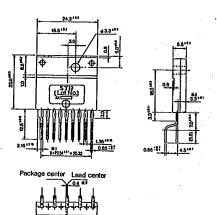


Fig. 9

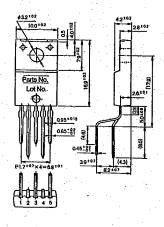
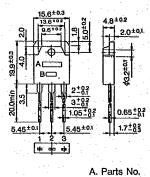


Fig. 10 MT-100(TO-3P)



B. Lot No.

Flg. 11

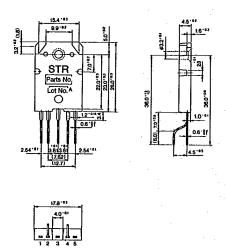
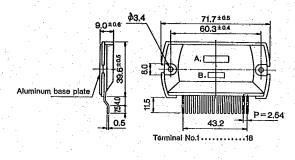
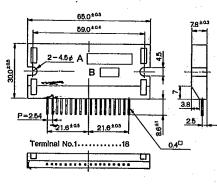


Fig. 12



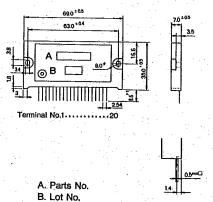
A. Parts No. B. Lot No.

Fig. 13

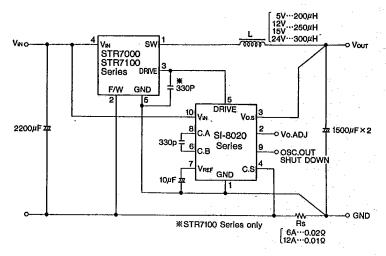


A. Parts No. B. Lot No.

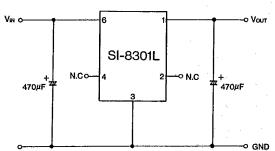
Fig. 14



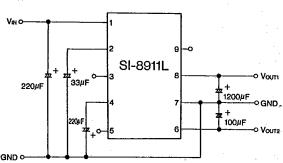
STR7000,7100/\$1-8020 Series



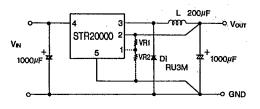




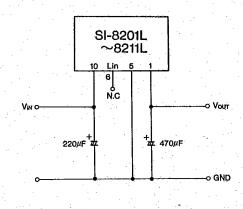
SI-8911L



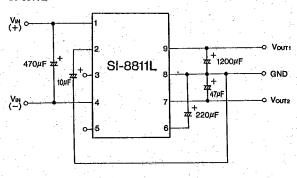
STR20005



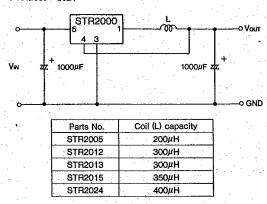
SI-8000L Series



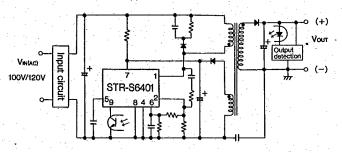
SI-8811L



STR2005~2024



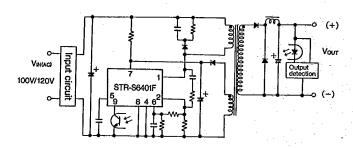
STR-S6401 Example of application circuit



Flyback converter

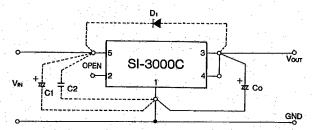
T-58-11-31

STR-S6401F Example of application circuit



Forward converter

SI-3000C

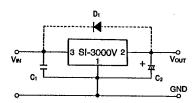


Co: 47~100µF

- C₁ C₂: Oscillation prevention capacitor (C₁: Approx. 47μF₁ C₂: Approx. 0.33μF)
- D₁: Protection diodes

Repuired in case reversely biased between input and output

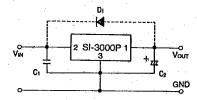
SI-3000V



- C_1 : Oscillation prevention capacitor (0.33 μ F)
- C₂: 47~100μF D₁: Protection diodes

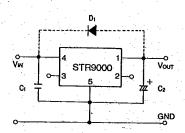
Required in case reversely biased between input and output

SI-3000P



- C1: Oscillation prevention capacitor (0.33µF)
- C2: 47~100#F
- D₁: Protection diodes
 - Required in case reversely blased between input and output

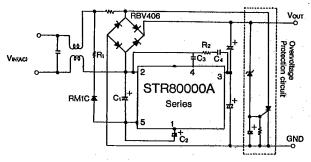
STR9000



- C1: Oscillation prevention cspacitor (0.33µF)

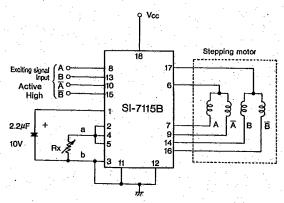
C₂: 47-100µF
D₁: Protection diodes
Required in case reversely blased between input and output

STR80000A Example of application circuit

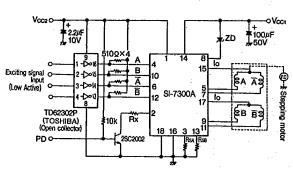


 $R_1: 2.2\, \Omega - R_2: 4.7\, \Omega$ $C_1: 2.2 \mu F/400 V - C_2: 100 \mu F/10 V - C_3: 0.1 \mu F/4: 0.047 \mu F$

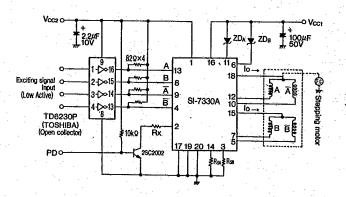
SI-7115B



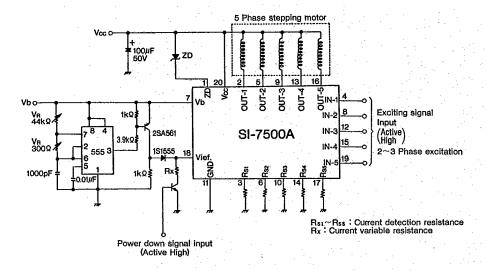
SI-7300A



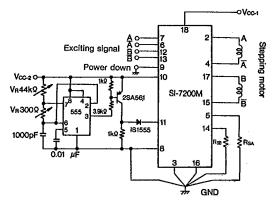
SI-7330A



SI-7500A

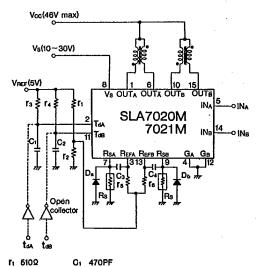


SI-7200M



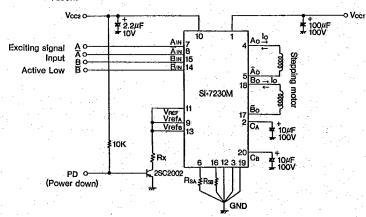
Power down Exciting signal : Active High Exciting signal Input : Active Low

SLA7020M·SLA7021M

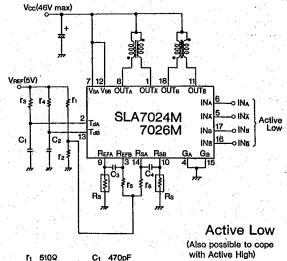


r₂ 100Ω(VR) C₂ 470PF r₃ 47kQ C₃ 2200PF ľ4 47kΩ C4 2200PF Γ₅ 2.4kΩ Da, Db EK03(7020M) RK34(7021M) Rs 1Ωtyp(7020M) 0.68Ωtyp(7021M) fs 2.4kQ

SI-7230M



SLA7024M-SLA7026M



C₁ 470pF C₂ 470pF r₂ 100Ω(VR)

C₃ 220pF r₃ 47kQ Γ₄ 47kΩ C4 220pF 1Q(typ)~2W(7024M) 0.68Q(typ)1~2W(7026M) r₅ 2.4kΩ re 2.4kΩ